

RJK03E0DNS

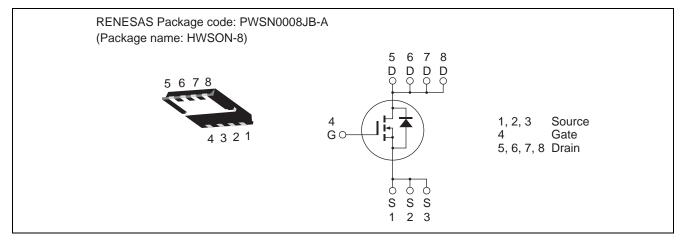
Silicon N Channel Power MOS FET **Power Switching**

R07DS0656EJ0300 (Previous: REJ03G1902-0200) Rev.3.00 Feb 01, 2012

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- $R_{DS(on)} = 4.3 \text{ m}\Omega \text{ typ.}$ (at $V_{GS} = 10 \text{ V}$)
- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit | |
|--|------------------------|-------------|------|--|
| Drain to source voltage | V _{DSS} | 30 | V | |
| Gate to source voltage | V _{GSS} | ±20 | V | |
| Drain current | I _D | 30 | А | |
| Drain peak current | Note1 D(pulse) | 120 | А | |
| Body-drain diode reverse drain current | I _{DR} | 30 | А | |
| Avalanche current | I _{AP} Note 2 | 13 | А | |
| Avalanche energy | E _{AR} Note 2 | 16.9 | mJ | |
| Channel dissipation | Pch Note3 | 20 | W | |
| Channel to case thermal impedance | θch-c ^{Note3} | 6.25 | °C/W | |
| Channel temperature | Tch | 150 | ٥° | |
| Storage temperature | Tstg | -55 to +150 | ۵° | |

- Notes: 1. PW \leq 10 μ s, duty cycle \leq 1% 2. Value at Tch = 25°C, Rg \geq 50 Ω

 - 3. Tc = 25°C



 $(T_a - 25^{\circ}C)$

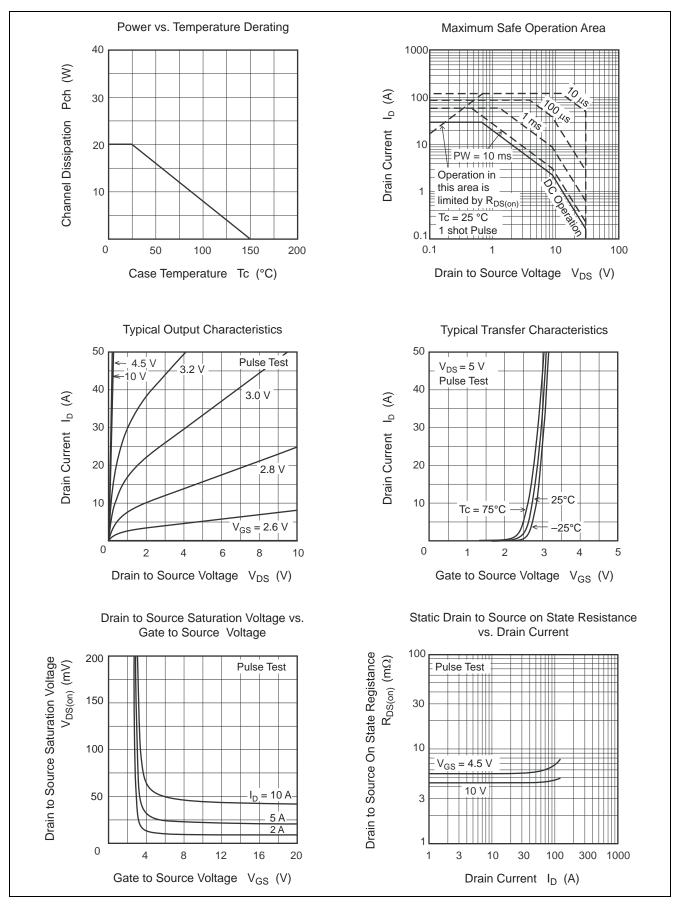
Electrical Characteristics

| | | | | | | $(Ta = 25^{\circ}C)$ |
|-----------------------------------|----------------------|-----|------|------|------|---|
| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
| Drain to source breakdown voltage | V _{(BR)DSS} | 30 | — | — | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ |
| Gate to source leak current | I _{GSS} | _ | — | ±0.1 | μA | $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$ |
| Zero gate voltage drain current | I _{DSS} | — | _ | 1 | μA | $V_{DS} = 30 V, V_{GS} = 0$ |
| Gate to source cutoff voltage | V _{GS(off)} | 1.2 | — | 2.5 | V | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$ |
| Static drain to source on state | R _{DS(on)} | _ | 4.3 | 5.6 | mΩ | $I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$ |
| resistance | R _{DS(on)} | _ | 5.6 | 7.8 | mΩ | $I_D = 15 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$ |
| Forward transfer admittance | y _{fs} | _ | 60 | — | S | $I_D = 15 \text{ A}, V_{DS} = 5 \text{ V}^{Note4}$ |
| Input capacitance | Ciss | _ | 2180 | 3050 | pF | V _{DS} = 10 V |
| Output capacitance | Coss | _ | 300 | — | pF | V _{GS} = 0 f = 1 MHz |
| Reverse transfer capacitance | Crss | _ | 175 | — | pF | |
| Gate Resistance | Rg | | 0.7 | 1.9 | Ω | |
| Total gate charge | Qg | | 15.2 | — | nC | V _{DD} = 10 V |
| Gate to source charge | Qgs | | 6.8 | — | nC | V _{GS} = 4.5 V I _D = 30 A |
| Gate to drain charge | Qgd | | 4.0 | — | nC | |
| Turn-on delay time | t _{d(on)} | | 13.6 | — | ns | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 15 \text{ A}$ |
| Rise time | tr | | 5.1 | — | ns | $V_{DD} \cong 10 \text{ V}$ $R_{L} = 0.67 \Omega$ $Rg = 4.7 \Omega$ |
| Turn-off delay time | t _{d(off)} | | 44 | | ns | |
| Fall time | t _f | | 7 | — | ns | |
| Body–drain diode forward voltage | V_{DF} | | 0.84 | 1.10 | V | $I_F = 30 \text{ A}, V_{GS} = 0^{\text{Note4}}$ |
| Body-drain diode reverse recovery | t _{rr} | | 18 | — | ns | I _F =30 A, V _{GS} = 0 |
| time | | | | | | di _F / dt = 100 A/ μs |

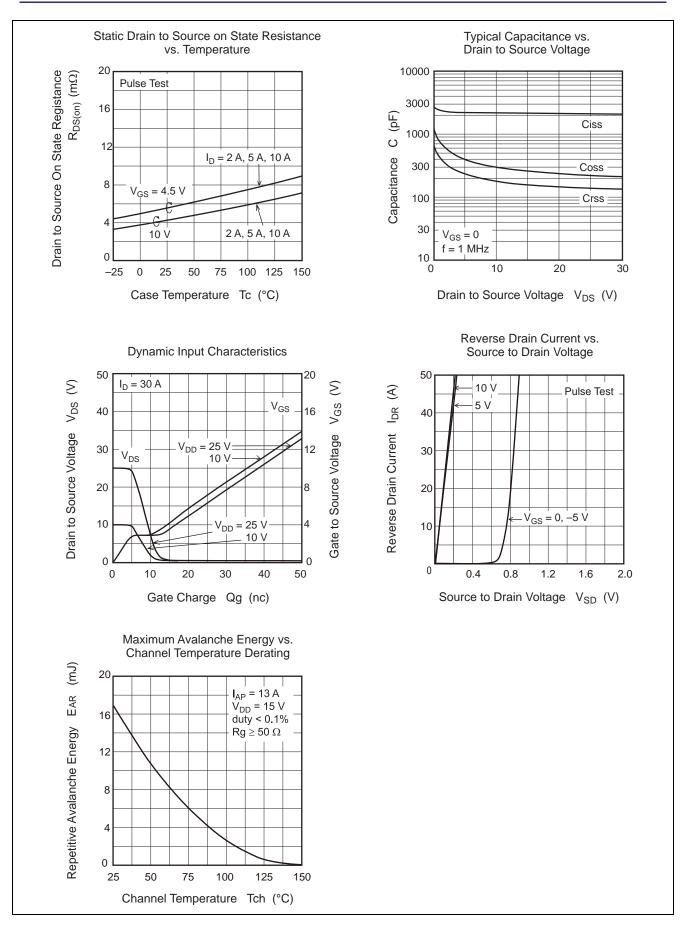
Notes: 4. Pulse test

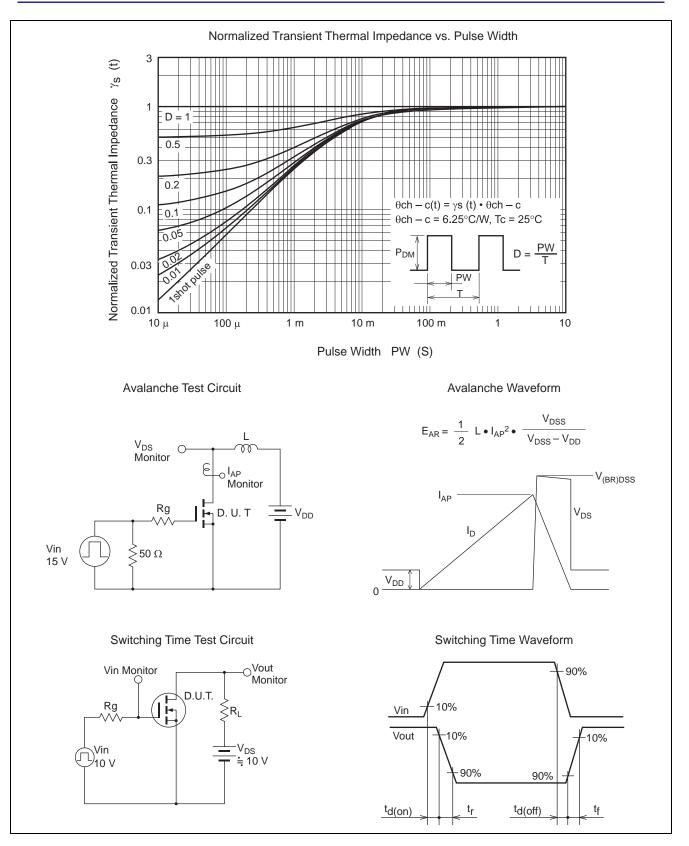


Main Characteristics

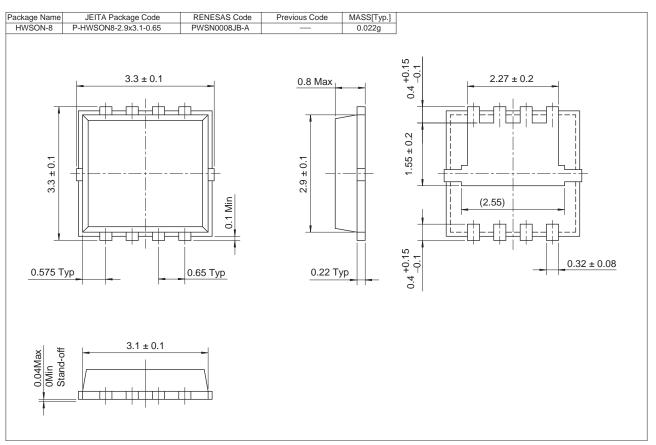








Package Dimensions



Ordering Information

| Orderable Part Number | Quantity | Shipping Container |
|-----------------------|----------|--------------------|
| RJK03E0DNS-00-J5 | 5000 pcs | Taping |

Note: The symbol of 2nd "-" is occasionally presented as "#".



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