Note: xxxFN (JEDEC SOP) is not available in

Japan.

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

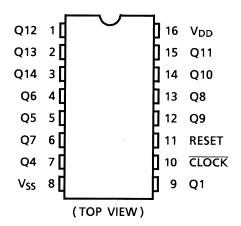
TC4020BP,TC4020BF,TC4020BFN

TC4020B 14 Stage Ripple-Carry Binary Counter/Dividers

TC4020B is 14 stage ripple carry binary counter having asynchronous clear function. The counter advances its counting stage by falling edge of $\overline{\text{CLOCK}}$ input. When RESET input is placed "H", all the circuits are reset regardless of $\overline{\text{CLOCK}}$ input making all the outputs (Q1, Q4~Q14) to be "L".

This is most suitable for frequency dividers, control circuits and timing circuits.

Pin Assignment



Truth Table

| $\overline{CLOCK}\Delta$ | RESET | Output State | | | |
|--------------------------|---------------------|-----------------------|--|--|--|
| * | H All Outputs = "L" | | | | |
| | L | No Change | | | |
| \neg | L | Advance to Next State | | | |

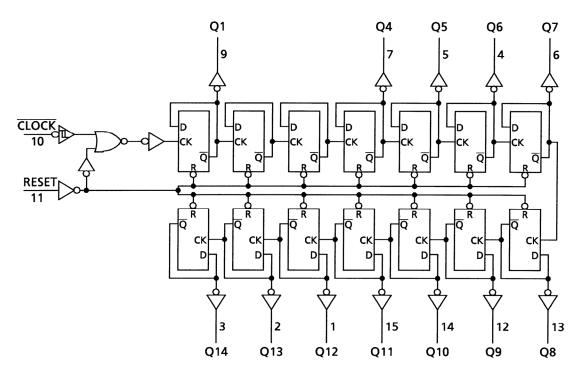
 $\Delta\!\!:$ Level change

*: Don't care

TC4020BP DIP16-P-300-2.54A TC4020BF HHHHHH SOP16-P-300-1.27A ममममम SOP16-P-300-1.27 TC4020BFN HHHH SOL16-P-150-1.27 Weight DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOP16-P-300-1.27 : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

<u>TOSHIBA</u>

Logic Diagram



Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|------------------|--------------------------------|------|
| DC supply voltage | V _{DD} | $V_{SS}-0.5V_{SS}+20$ | V |
| Input voltage | V _{IN} | $V_{SS}-0.5V_{DD}+0.5$ | V |
| Output voltage | V _{OUT} | $V_{SS}-0.5\text{-}V_{DD}+0.5$ | V |
| DC input current | I _{IN} | ±10 | mA |
| Power dissipation | PD | 300 (DIP)/180 (SOIC) | mW |
| Operating temperature range | T _{opr} | -40~85 | °C |
| Storage temperature range | T _{stg} | -65~150 | °C |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Recommended Operating Conditions (V_{SS} = 0 V) (Note)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-------------------|-----------------|----------------|-----|------|-----------------|------|
| DC supply voltage | V _{DD} | _ | 3 | _ | 18 | V |
| Input voltage | V _{IN} | | 0 | _ | V _{DD} | V |

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Static Electrical Characteristics ($V_{SS} = 0 V$)

| Characteristics | | Sym- | Test Condition | | -40°C | | 25°C | | | 85°C | | |
|-----------------------|-----------------------------|-----------------|--|------------------------|-------|------|-------|-------------------|------|-------|------|------|
| | | bol | | V _{DD} (V) | Min | Max | Min | Тур. | Max | Min | Max | Unit |
| | | | | 5 | 4.95 | _ | 4.95 | 5.00 | _ | 4.95 | _ | |
| High-level voltage | output | VOH | I _{OUT} < 1 μΑ V _{IN} = V _{SS} , V _{DD} | 10 | 9.95 | — | 9.95 | 10.00 | — | 9.95 | — | V |
| 0 | | | VIN – VSS, VDD | 15 | 14.95 | _ | 14.95 | 15.00 | _ | 14.95 | _ | |
| | | | I _{OUT} < 1 μΑ | 5 | — | 0.05 | | 0.00 | 0.05 | | 0.05 | |
| Low-level voltage | output | V _{OL} | $V_{IN} = V_{SS}, V_{DD}$ | 10 | — | 0.05 | _ | 0.00 | 0.05 | _ | 0.05 | V |
| | | | VIN - VSS, VDD | 15 | _ | 0.05 | — | 0.00 | 0.05 | — | 0.05 | |
| | | | $V_{OH} = 4.6 V$ | 5 | -0.61 | — | -0.51 | -1.0 | — | -0.42 | — | |
| | | | $V_{OH} = 2.5 V$ | 5 | -2.50 | — | -2.10 | -4.0 | — | -1.70 | — | mA |
| Output hig | h current | IOH | $V_{OH} = 9.5 V$ | 10 | -1.50 | — | -1.30 | -2.2 | — | -1.10 | — | |
| | | | V _{OH} = 13.5 V | 15 | -4.00 | — | -3.40 | -9.0 | — | -2.80 | — | |
| | | | $V_{IN} = V_{SS}, \ V_{DD}$ | | | | | | | | | |
| | | I _{OL} | $V_{OL}=0.4\;V$ | 5 | 0.61 | — | 0.51 | 1.2 | — | 0.42 | — | mA |
| Output Iow | / current | | $V_{OL} = 0.5 \ V$ | 10 | 1.50 | — | 1.30 | 3.2 | — | 1.10 | — | |
| Output low | Output low current | | $V_{OL} = 1.5 V$ | 15 | 4.00 | — | 3.40 | 12.0 | — | 2.80 | — | |
| | | | $V_{IN} = V_{SS}, \ V_{DD}$ | | | | | | | | | |
| | | V _{IH} | $V_{OUT}=0.5\ V,\ 4.5\ V$ | 5 | 3.5 | — | 3.5 | 2.75 | — | 3.5 | — | V |
| Input high | voltage | | $V_{OUT} = 1.0 V, 9.0 V$ | 10 | 7.0 | — | 7.0 | 5.50 | — | 7.0 | — | |
| input nigh | voltage | | $V_{OUT} = 1.5 \text{ V}, \ 13.5 \text{ V}$ | 15 | 11.0 | — | 11.0 | 8.25 | — | 11.0 | — | |
| | | | $ I_{OUT} < 1 \ \mu A$ | | | | | | | | | |
| | | VIL | $V_{OUT} = 0.5 V, 4.5 V$ | 5 | — | 1.5 | — | 2.25 | 1.5 | — | 1.5 | V |
| Input lows | voltage | | $V_{OUT} = 1.0 V, 9.0 V$ | 10 | — | 3.0 | | 4.50 | 3.0 | | 3.0 | |
| input iow v | Input low voltage | | $V_{OUT} = 1.5 \text{ V}, \ 13.5 \text{ V}$ | 15 | — | 4.0 | — | 6.75 | 4.0 | — | 4.0 | |
| | | | $ I_{OUT} < 1 \ \mu A$ | | | | | | | | | |
| Input | "H" level | IIH | $V_{IH} = 18 \ V$ | 18 | | 0.1 | _ | 10 ⁻⁵ | 0.1 | _ | 1.0 | μA |
| current | "L" level | Ι _{ΙL} | $V_{IL}=0 \ V$ | 18 | | -0.1 | _ | -10 ⁻⁵ | -0.1 | _ | -1.0 | μη |
| | | | | 5 | _ | 5 | | 0.005 | 5 | | 150 | |
| Quiescent current | Quiescent supply current | | V _{IN} = V _{SS} , V _{DD} (Note) | 10 | — | 10 | | 0.010 | 10 | | 300 | μA |
| | | | (10010) | 15 | — | 20 | — | 0.015 | 20 | — | 600 | |

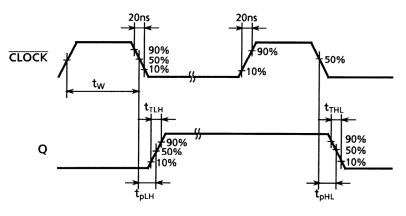
Note: All valid input combinations.

Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

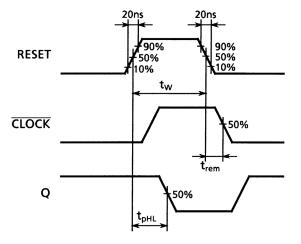
| Characteristics | Symbol | Test Condition | | Min | Тур. | Max | Unit |
|--|------------------|----------------|---------------------|----------|------|------|------|
| Characteristics | Cymbol | | V _{DD} (V) | IVIIII | | | |
| Output transition time | | | 5 | — | 70 | 200 | |
| (low to high) | t _{TLH} | _ | 10 | — | 35 | 100 | ns |
| | | | 15 | _ | 30 | 80 | |
| Output transition time | | | 5 | — | 70 | 200 | |
| (high to low) | t _{THL} | — | 10 | — | 35 | 100 | ns |
| | | | 15 | — | 30 | 80 | |
| Propagation delay time | | | 5 | — | 160 | 360 | ns |
| (CLOCK -Q1) | t _{pLH} | — | 10 | — | 80 | 160 | |
| | | | 15 | | 65 | 130 | |
| Propagation delay time | | | 5 | _ | 160 | 360 | |
| $(\overline{\text{CLOCK}} - \text{Q1})$ | t _{pHL} | _ | 10 | _ | 80 | 160 | ns |
| | | | 15 | — | 65 | 130 | |
| Propagation delay time | | | 5 | — | 1000 | 2000 | |
| $(\overline{\text{CLOCK}} - \text{Q14})$ | t _{pLH} | — | 10 | _ | 500 | 1000 | ns |
| | | | 15 | — | 400 | 800 | |
| Propagation delay time | | | 5 | — | 1000 | 2000 | |
| $(\overline{\text{CLOCK}} - Q14)$ | t _{pHL} | _ | 10 | — | 500 | 1000 | ns |
| | | | 15 | _ | 400 | 800 | |
| Propagation delay time | | | 5 | — | 150 | 280 | |
| (RESET-Q) | t _{pHL} | — | 10 | _ | 70 | 120 | ns |
| | | | 15 | — | 50 | 100 | |
| | | | 5 | 3.5 | 10 | — | |
| Max clock frequency | fCL | — | 10 | 8.0 | 20 | — | MHz |
| | | | 15 | 12.0 | 25 | — | |
| Min clock pulse width | | | 5 | — | 50 | 140 | |
| (RESET) | t _W | — | 10 | — | 20 | 60 | ns |
| (| | | 15 | — | 15 | 40 | |
| | | | 5 | — | 100 | 200 | |
| Min pulse width | t _W | — | 10 | — | 40 | 80 | ns |
| | | | 15 | — | 30 | 60 | |
| Min removal time | | | 5 | — | | 350 | |
| (RESET- CLOCK) | t _{rem} | — | 10 | — | | 150 | ns |
| () | | | 15 | — | | 100 | |
| Max clock input rise time | t _{rCL} | | 5 | | | | |
| Max clock input fall time | t _{fCL} | — | 10 | No limit | | | μs |
| | | 15 | | 1 | | | |
| Input capacitance | C _{IN} | — | | — | 5 | 7.5 | pF |

Operating Supply Current Test Circuit

Waveform 1



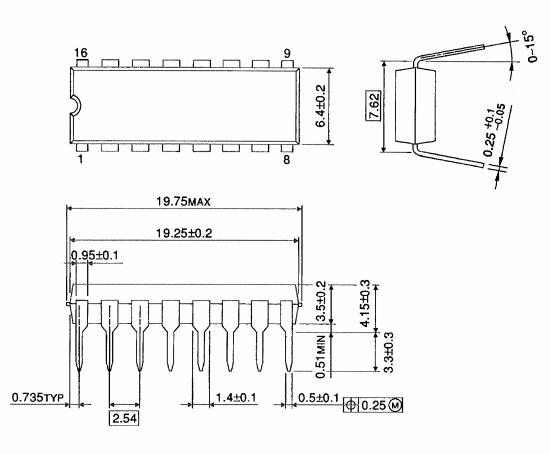
Waveform 2



Package Dimensions

DIP16-P-300-2.54A

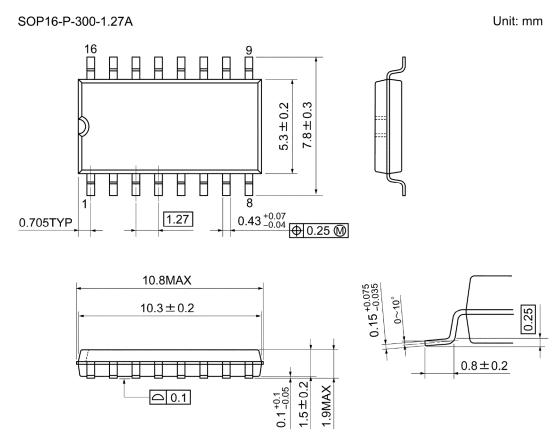
Unit : mm



Weight: 1.00 g (typ.)

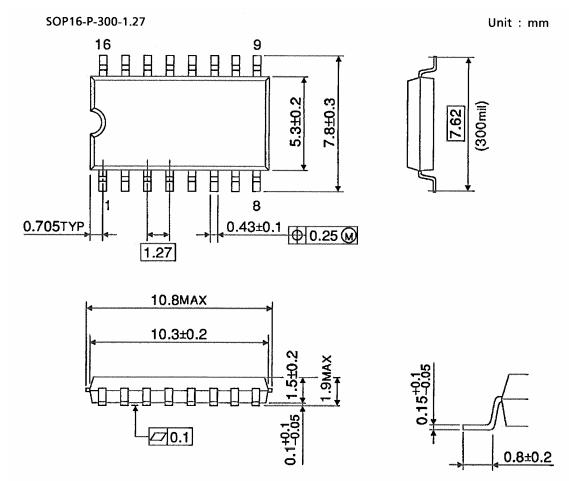
TOSHIBA

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions

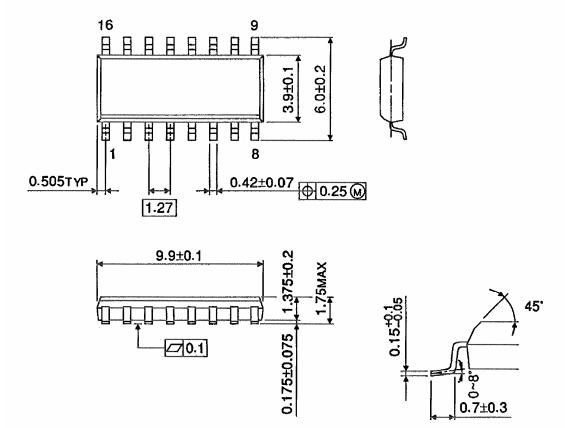


Weight: 0.18 g (typ.)

Package Dimensions (Note)

SOL16-P-150-1.27

Unit : mm



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

Note: Lead (Pb)-Free Packages DIP16-P-300-2.54A SOP16-P-300-1.27A SOL16-P-150-1.27

RESTRICTIONS ON PRODUCT USE

Handbook" etc. 021023_A

060116EBA

- The information contained herein is subject to change without notice. 021023_D
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability

• The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk, 021023 B

- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations. 060106_Q
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others. 021023_C
- The products described in this document are subject to the foreign exchange and foreign trade laws. 021023_E