October 2035

ISL9V3040D3S / ISL9V3040S3S / ISL9V3040P3 / ISL9V3040S3

EcoSPARK^a 300mJ, 400V, N-Channel Ignition IGBT

General Description

The ISL9V3040D3S, ISL9V3040S3S, ISL9V3040P3, and ISL9V3040S3 are the next generation ignition IGBTs that offer outstanding SCIS capability in the space saving D-Pak (TO-252), as well as the industry standard D²-Pak (TO-263), and TO-262 and TO-220 plastic packages. This device is intended for use in automotive ignition circuits, specifically as a coil driver. Internal diodes provide voltage clamping without the need for external components.

EcoSPARK¤ devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

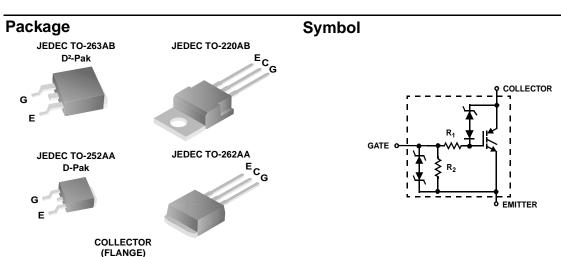
Formerly Developmental Type 49362

Applications

- Automotive Ignition Coil Driver CircuitsCoil- On Plug Applications

Features

- · Space saving D-Pak package availability
- SCIS Energy = 300mJ at T₁ = 25° C
- Logic Level Gate Drive



Device Maximum Ratings T_A = 25°C unless otherwise noted

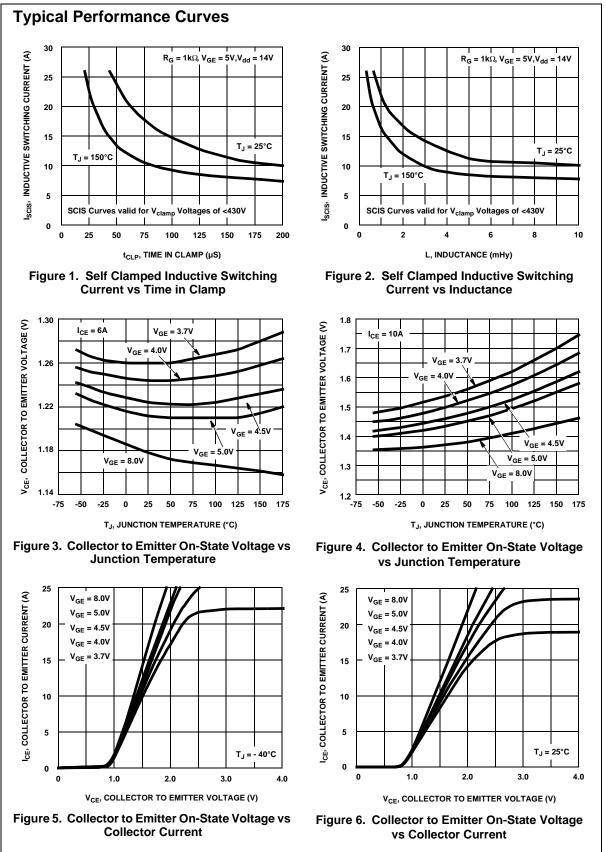
Symbol	Parameter	Ratings		
BV _{CER}	Collector to Emitter Breakdown Voltage (I _C = 1 mA)	430	V	
BV _{ECS}	Emitter to Collector Voltage - Reverse Battery Condition (I _C = 10 mA)	24	V	
E _{SCIS25}	At Starting T_J = 25°C, I_{SCIS} = 14.2A, L = 3.0 mHy	300	mJ	
E _{SCIS150}	At Starting T _J = 150°C, I _{SCIS} = 10.6A, L = 3.0 mHy	170	mJ	
I _{C25}	Collector Current Continuous, At T _C = 25°C, See Fig 9	21	Α	
I _{C110}	Collector Current Continuous, At T _C = 110°C, See Fig 9	17	Α	
V_{GEM}	GEM Gate to Emitter Voltage Continuous		V	
PD	Power Dissipation Total T _C = 25°C	150	W	
Power Dissipation Derating T _C > 25°C		1.0	W/°C	
ТJ	Operating Junction Temperature Range	-40 to 175	°C	
T _{STG} Storage Junction Temperature Range		-40 to 175	°C	
T _L Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)		300	°C	
T _{pkg} Max Lead Temp for Soldering (Package Body for 10s)		260	°C	
ESD	Electrostatic Discharge Voltage at 100pF, 1500 Ω	4	kV	

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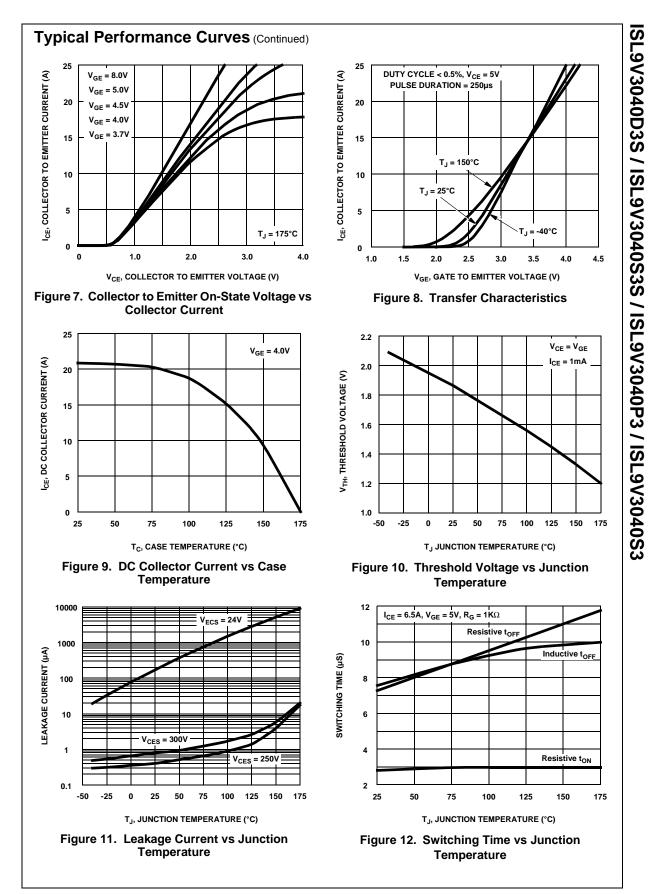
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Device Marking		Device		ackage	Reel Size	Tape Width		Quantity	
V3040	V3040D ISL9V3040D3ST TO		D-252AA	330mm	16mm		2500		
V3040S ISL9V3040S3ST		TC	D-263AB	330mm	24mm		800		
V3040P ISL9V3040P3			D-220AA	Tube	N/A		50		
			TO-262AA Tube		N/A		50		
V304		ISL9V3040D3S		D-252AA	Tube	N/A		75	
V304		ISL9V3040S3S		D-263AB	Tube		N/A		50
Symbol		Parameter	Cun	Test Con		Min	Тур	Max	Units
f State	Charact	eristics							
BV _{CER}	1	ector to Emitter Breakdown Voltage		$I_C = 2mA$, $V_{GE} = 0$, $R_G = 1K\Omega$, See Fig. 15 $T_1 = -40$ to 150°C		370	400	430	V
BV _{CES}	Collector	r to Emitter Breakdown Voltage		$I_{C} = 10mA, V_{GE} = 0,$ $R_{G} = 0, See Fig. 15$ $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		390	420	450	V
BV _{ECS}	Emitter t	o Collector Breakdown Vol	tage	$I_{C} = -75$ mA, $V_{GE} = 0$ V, $T_{C} = 25$ °C		30	-	-	V
BV_{GES}	Gate to I	Emitter Breakdown Voltage		I _{GES} = ± 2mA		±12	±14	-	V
I _{CER}	Collector	to Emitter Leakage Curre	nt	V _{CER} = 250V,	T _C = 25°C	-	-	25	μA
				R _G = 1KΩ, See Fig. 11	T _C = 150°C	-	-	1	mA
I _{ECS}	Emitter t	o Collector Leakage Curre	nt	$V_{EC} = 24V$, See		-	-	1	mA
				Fig. 11	T _C = 150°C	-	-	40	mA
R ₁		Bate Resistance				-	70	-	Ω
R ₂		Emitter Resistance				10K	-	26K	Ω
n State (•					
V _{CE(SAT)}	Collector	llector to Emitter Saturation Voltage		I _C = 6A, V _{GE} = 4V	T _C = 25°C, See Fig. 3	-	1.25	1.60	V
V _{CE(SAT)}	Collector	ollector to Emitter Saturation Voltage		I _C = 10A, V _{GE} = 4.5V	T _C = 150°C, See Fig. 4	-	1.58	1.80	V
V _{CE(SAT)}	Collector	ector to Emitter Saturation Voltage		I _C = 15A, V _{GE} = 4.5V	T _C = 150°C	-	1.90	2.20	V
/namic (Charact	eristics							
Q _{G(ON)}	Gate Ch	arge		I _C = 10A, V _{CE} = 12V, V _{GE} = 5V, See Fig. 14		-	17	-	nC
V _{GE(TH)}	Gate to	Emitter Threshold Voltage		-	T _C = 25°C	1.3	-	2.2	V
				V _{CE} = V _{GE,} See Fig. 10	T _C = 150°C	0.75	-	1.8	V
V _{GEP}	Gate to	Emitter Plateau Voltage		I _C = 10A, V _{CE} :	= 12V	-	3.0	-	V
	Charao	cteristics							
t _{d(ON)R}	Current	Turn-On Delay Time-Resis	tive	V _{CE} = 14V, R _L = 1Ω,		-	0.7	4	μs
t _{rR}	Current	Rise Time-Resistive		$V_{GE} = 5V$, $R_G = 1K\Omega$ $T_J = 25^{\circ}C$, See Fig. 12		-	2.1	7	μs
t _{d(OFF)L}		Turn-Off Delay Time-Induc	tive	V _{CE} = 300V, L = 500µHy,		-	4.8	15	μs
t _{fL}	Current	Fall Time-Inductive		V _{GE} = 5V, R _G = 1KΩ T _J = 25°C, See Fig. 12		-	2.8	15	μs
SCIS	Self Clar	mped Inductive Switching		$T_J = 25^{\circ}C$, L = 3.0 mHy, R _G = 1K Ω , V _{GE} = 5V, See Fig. 1 & 2		-	-	300	mJ
nermal C	haracte	eristics							
$R_{\theta JC}$	Thormol	Resistance Junction-Case	2	All packages		-	-	1.0	°C/W

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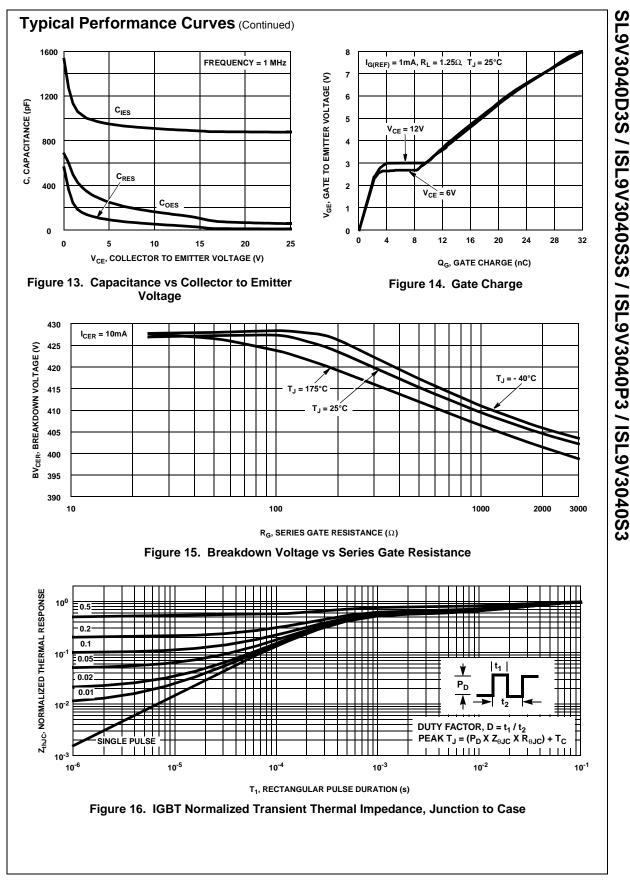


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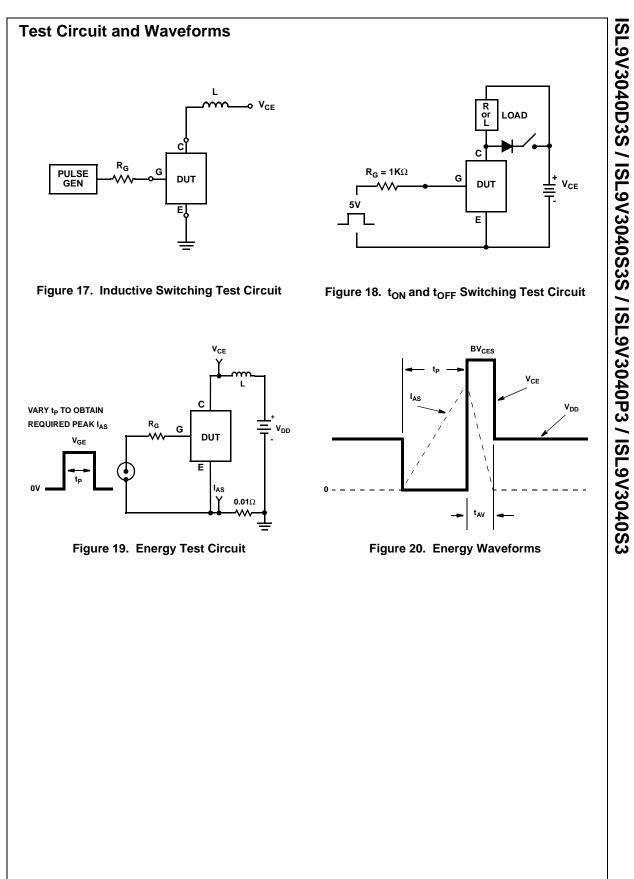
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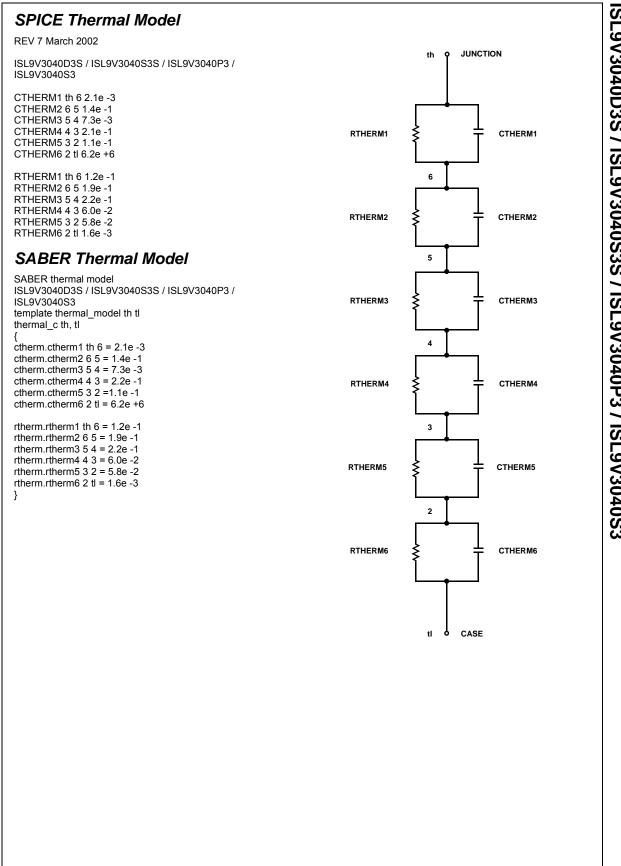
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