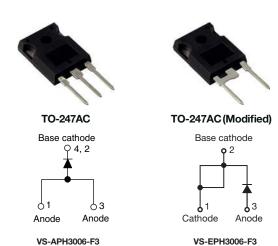


Vishay Semiconductors

### Hyperfast Rectifier, 30 A FRED Pt®



PRODUCT SUMMARY						
Package	TO-247AC, TO-247AC modified					
I <sub>F(AV)</sub>	30 A					
$V_{R}$	600 V					
V <sub>F</sub> at I <sub>F</sub>	2.65 V					
t <sub>rr</sub> (typ.)	27 ns					
T <sub>J</sub> max.	175 °C					
Diode variation	Single die					

#### **FEATURES**

- · Low forward voltage drop
- Hyperfast soft recovery time
- 175 °C operating junction temperature
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47





ROHS

#### **DESCRIPTION/APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 112 °C	30	^			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	180	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
Forward voltage	V <sub>E</sub>	I <sub>F</sub> = 30 A	-	2.0	2.65	V	
Forward voltage V <sub>F</sub>		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.4	1.8		
Poveree leekage current		$V_R = V_R$ rated	-	-	30	uА	
Reverse leakage current I <sub>R</sub>		$T_J = 150 ^{\circ}\text{C},  V_R = V_R \text{ rated}$	-	-	300	μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	20	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ	

# Vishay Semiconductors Hyperfast Rectifier, 30 A FRED Pt®



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 5$	$I_F = 1 \text{ A, } dI_F/dt = 50 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		26	35	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	26	-	ns
		T <sub>J</sub> = 125 °C	]	-	70	-	
Dools we consent assument	1	T <sub>J</sub> = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	3.5	-	^
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	7.6	-	A
Reverse recovery charge	0	T <sub>J</sub> = 25 °C	] .n	-	50	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C	]	-	280	-	IIC

THERMAL - MECHANICA	THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C		
Thermal resistance, junction to case	R <sub>thJC</sub>		-	0.7	1.1	°C/W		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-			
Maiabt			-	5.5	-	g		
Weight			-	0.2	-	OZ.		
Mounting torque			1.2 (10)	-	2.4 (20)	kgf · cm (lbf · in)		
Marking device		Case style TO-247AC	APH3006					
Marking device		Case style TO-247AC modified EPH30			13006			



# Hyperfast Rectifier, 30 A FRED Pt® Vishay Semiconductors

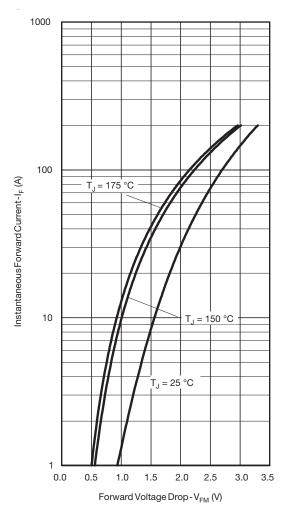


Fig. 1 - Typical Forward Voltage Drop Characteristics

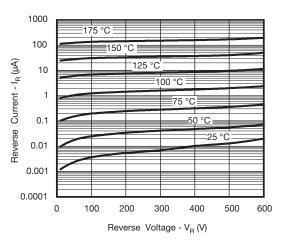


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

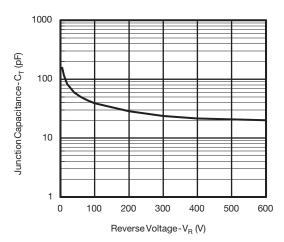


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

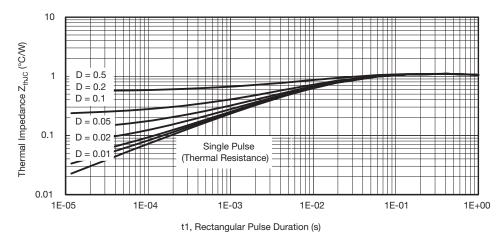


Fig. 4 - Max. Thermal Impedance Z<sub>thJC</sub> Characteristics

# Vishay Semiconductors Hyperfast Rectifier, 30 A FRED Pt®



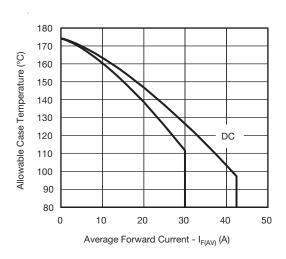


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

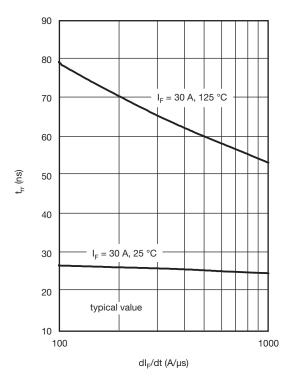


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

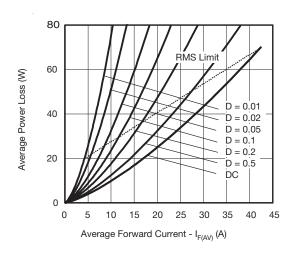


Fig. 6 - Forward Power Loss Characteristics

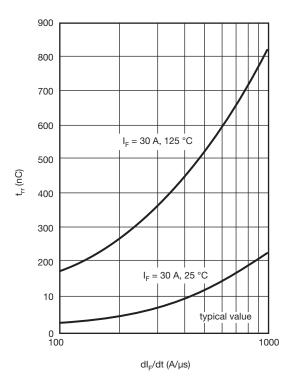


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



## Hyperfast Rectifier, 30 A FRED Pt® Vishay Semiconductors

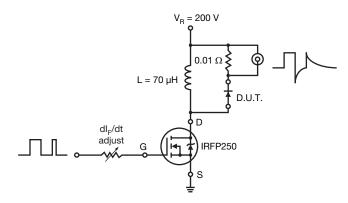
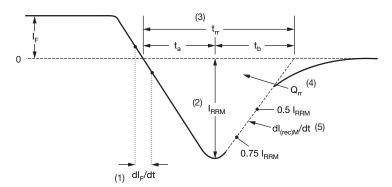


Fig. 9 - Reverse Recovery Parameter Test Circuit



- dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\boldsymbol{Q}_{rr}$  area under curve defined by  $\boldsymbol{t}_{rr}$  and  $\boldsymbol{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

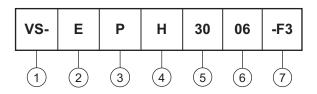
Fig. 10 - Reverse Recovery Waveform and Definitions

Vishay Semiconductors Hyperfast Rectifier, 30 A FRED Pt®



#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

Ultrafast MUR series

• A = Single diode

• E = Single diode (modified)

P = TO-247AC

H = Hyperfast recovery time

Current code (30 = 30 A)

Voltage code (06 = 600 V)

F3 = RoHS compliant and totally lead (Pb)-free

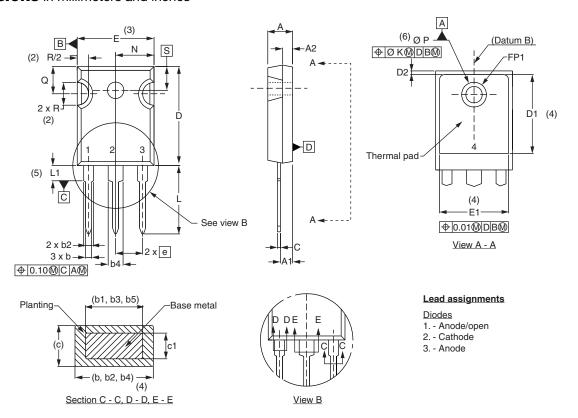
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-APH3006-F3	25	500	Antistatic plastic tube				
VS-EPH3006-F3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AC	www.vishay.com/doc?95223			
Differisions	TO-247AC modified	www.vishay.com/doc?95253			
Part marking information	TO-247AC	www.vishay.com/doc?95007			
Part marking information	TO-247AC modified	www.vishay.com/doc?95442			



### Vishay Semiconductors

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS INCHES  MIN. MAX. MIN. MAX.		NOTES	SY	
STIVIBOL	MIN.			NOTES	31	
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.50	2.49	0.059	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.37	0.065	0.094		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.86	0.015	0.034		
c1	0.38	0.76	0.015	0.030		
D	19.71	20.70	0.776	0.815	3	
D1	13.08	-	0.515	-	4	

SYMBOL	MILLIMETERS		INC	INCHES		
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.30	0.020	0.051		
E	15.29	15.87	0.602	0.625	3	
E1	13.72	-	0.540	-		
е	5.46	BSC	0.215	BSC		
FK	2.	54	0.0	)10		
L	14.20	16.10	0.559	0.634		
L1	3.71	4.29	0.146	0.169		
N	7.62	BSC	0.3			
ΦР	3.56	3.66	0.14	0.144		
ФР1	1	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	1.78	0.216		
S	5.51	BSC	0.217	BSC		

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional

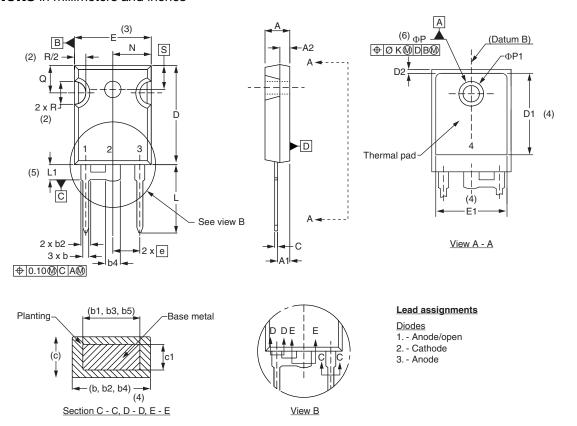
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

<sup>(3)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



### Vishay Semiconductors

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	HES NOTES		SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ΦК	2.	54	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ΦР	3.56	3.66	0.14	0.144	
С	0.38	0.86	0.015	0.034			ФР1	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	1.78	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

#### Notes

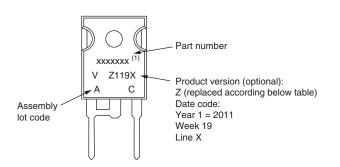
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- $^{(7)}\,$  Outline conforms to JEDEC outline TO-247 with exception of dimension c



## **Part Marking Information**

Vishay Semiconductors

### **TO-247AC** modified E



Example: This is a xxxxxxx (1) with assembly lot code AC,

assembly lot code AC, assembled on WW 19, 2011 in the assembly line "X"

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
A	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS compliant and termination lead (Pb)-free
F	RoHS compliant and totally lead (Pb)-free
M	Halogen-free, RoHS compliant and termination lead (Pb)-free
N	Halogen-free, RoHS compliant and totally lead (Pb)-free
G	Green





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