

FCPF400N60

600V N-Channel MOSFET

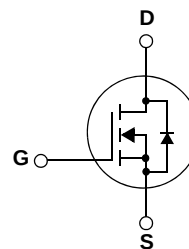
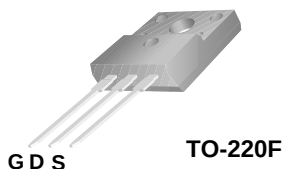
Features

- 650V @T_J = 150°C
- Max. R_{DS(on)} = 400mΩ
- Ultra low gate charge (typ. Q_g = 28nC)
- Low effective output capacitance (typ. C_{oss,eff} = 90pF)
- 100% avalanche tested

Description

SuperFET®II is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET®II is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	FCPF400N60	Units
V _{DSS}	Drain to Source Voltage	600	V
V _{GSS}	Gate to Source Voltage	-DC	V
		-AC (f>1HZ)	
I _D	Drain Current	-Continuous (T _C = 25°C)	A
		-Continuous (T _C = 100°C)	
I _{DM}	Drain Current	- Pulsed (Note 1)	A
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	mJ
I _{AR}	Avalanche Current	(Note 1)	A
E _{AR}	Repetitive Avalanche Energy	(Note 1)	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	V/ns
	MOSFET dv/dt		
P _D	Power Dissipation	(T _C = 25°C)	W
		- Derate above 25°C	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	°C

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FCPF400N60	Units
R _{θJC}	Thermal Resistance, Junction to Case	4	°C/W
R _{θCS}	Thermal Resistance, Case to Heat Sink (Typical)	0.5	
R _{θJA}	Thermal Resistance, Junction to Ambient	62.5	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCPF400N60	FCPF400N60	TO-220F	-	-	50

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0V, I_D = 10mA, T_J = 25^\circ\text{C}$	600	-	-	V
		$V_{GS} = 0V, I_D = 10mA, T_J = 150^\circ\text{C}$	650	-	-	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 10mA$, Referenced to 25°C	-	0.6	-	$V/^\circ\text{C}$
BV_{DS}	Drain-Source Avalanche Breakdown Voltage	$V_{GS} = 0V, I_D = 10A$	-	700	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 480V, V_{GS} = 0V$	-	-	1	μA
		$V_{DS} = 480V, T_C = 125^\circ\text{C}$	-	-	10	
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu A$	2.5	-	3.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 5A$	-	0.35	0.40	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 20V, I_D = 5A$	-	11	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ $f = 1MHz$	-	1180	1580	pF
C_{oss}	Output Capacitance		-	860	1144	pF
C_{rss}	Reverse Transfer Capacitance		-	43	54	pF
C_{oss}	Output Capacitance	$V_{DS} = 380V, V_{GS} = 0V, f = 1.0MHz$	-	22	-	pF
$C_{oss \text{ eff.}}$	Effective Output Capacitance	$V_{DS} = 0V \text{ to } 480V, V_{GS} = 0V$	-	90	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	$V_{DS} = 380V, I_D = 5A$ $V_{GS} = 10V$ (Note 4)	-	28	38	nC
Q_{gs}	Gate to Source Gate Charge		-	5	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	10	-	nC
ESR	Equivalent Series Resistance	Drain Open	-	1	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 380V, I_D = 5A$ $V_{GS} = 10V, R_G = 4.7\Omega$ (Note 4)	-	13	37	ns
t_r	Turn-On Rise Time		-	7	24	ns
$t_{d(off)}$	Turn-Off Delay Time		-	43	95	ns
t_f	Turn-Off Fall Time		-	6	21	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current	-	-	10	A	
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	30	A	
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 5A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 5A	-	240	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100A/μs	-	2.7	-	μC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 2.3A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 5A, di/dt \leq 200A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

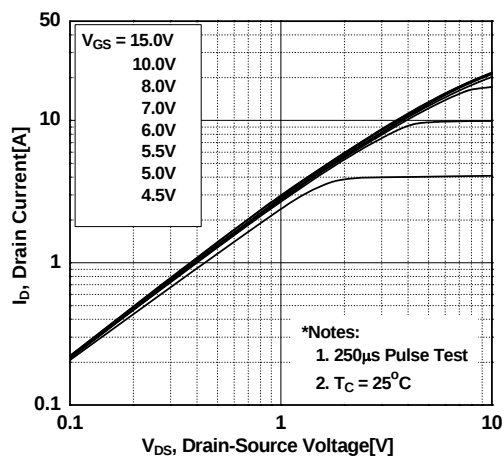


Figure 2. Transfer Characteristics

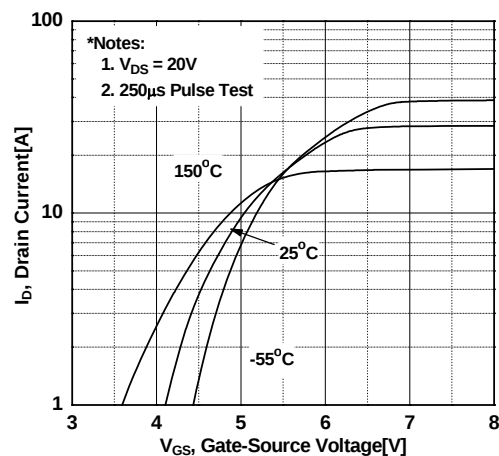


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

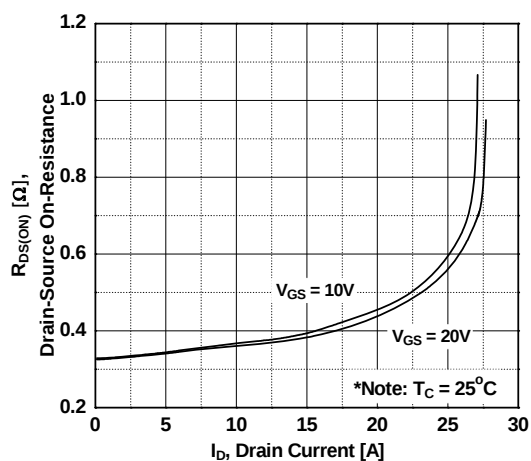


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

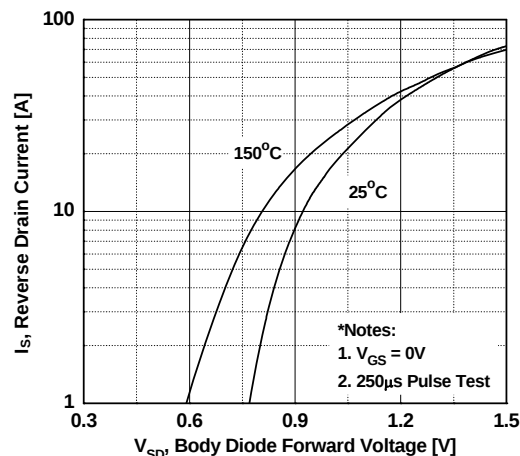


Figure 5. Capacitance Characteristics

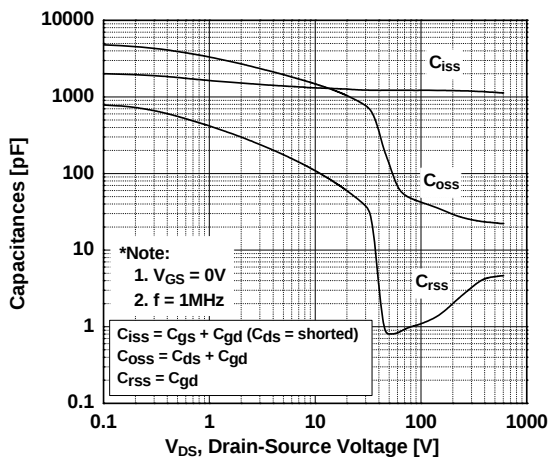
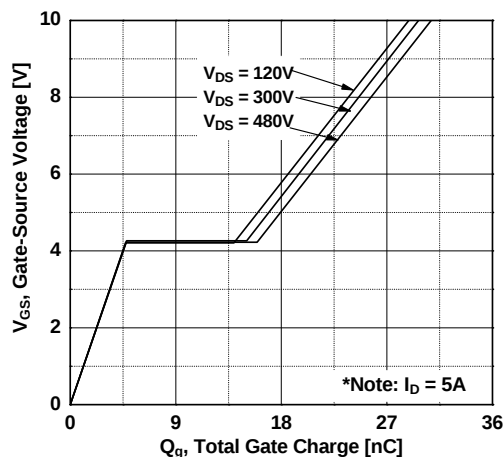


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

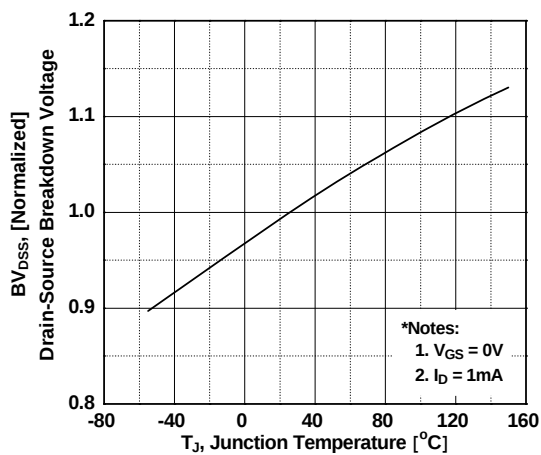


Figure 8. On-Resistance Variation vs. Temperature

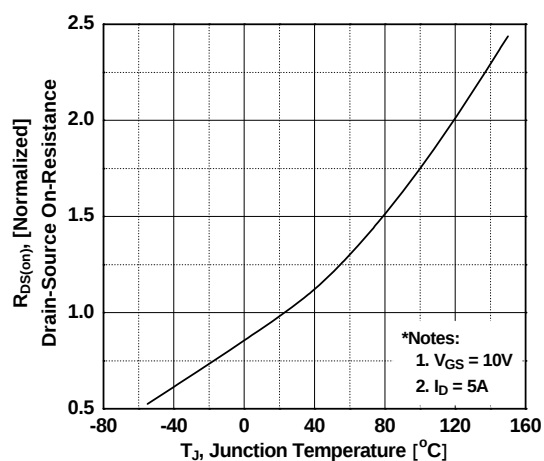


Figure 9. Maximum Safe Operating Area vs. Case Temperature

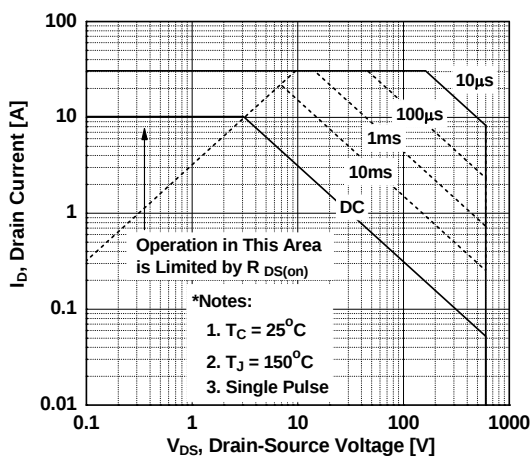


Figure 11. Maximum Drain Current

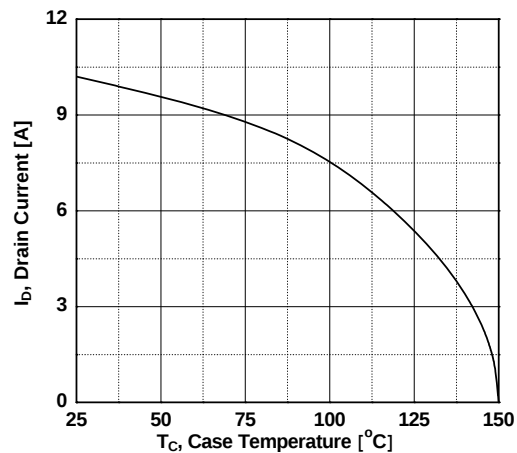
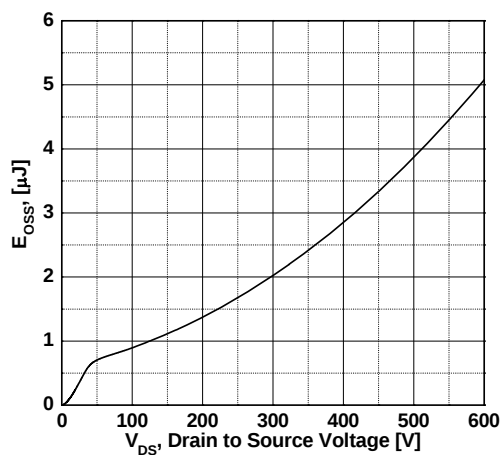
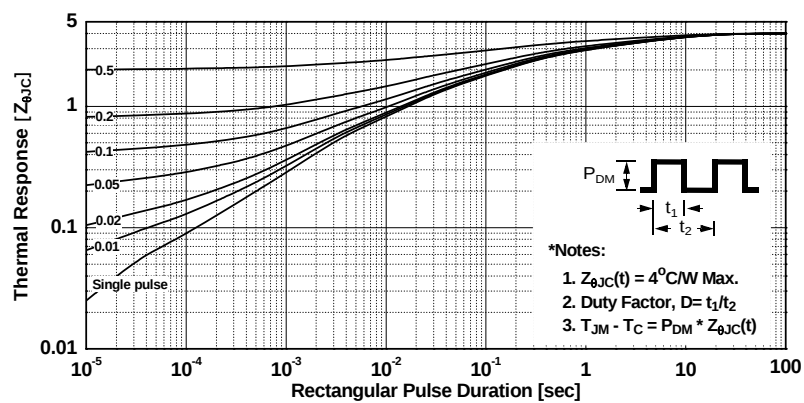


Figure 12. Eoss vs. Drain to Source Voltage Switching Capability

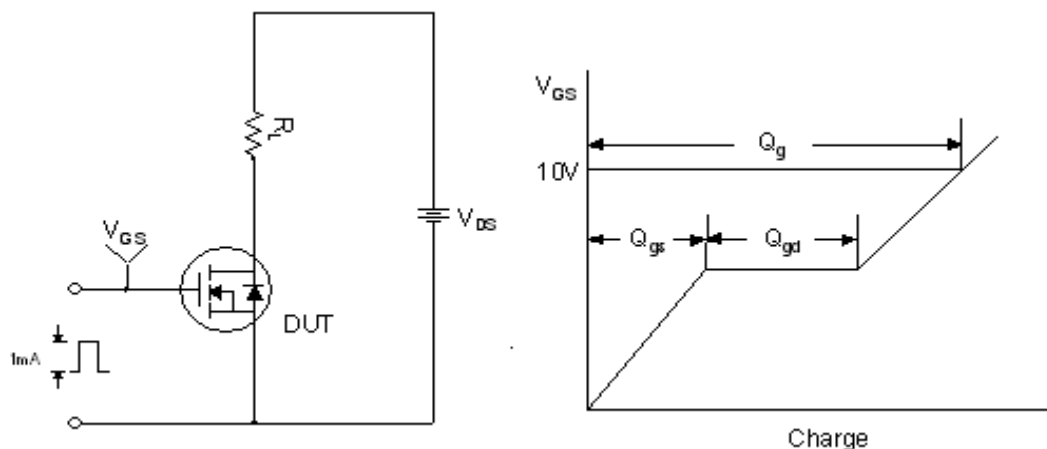


Typical Performance Characteristics (Continued)

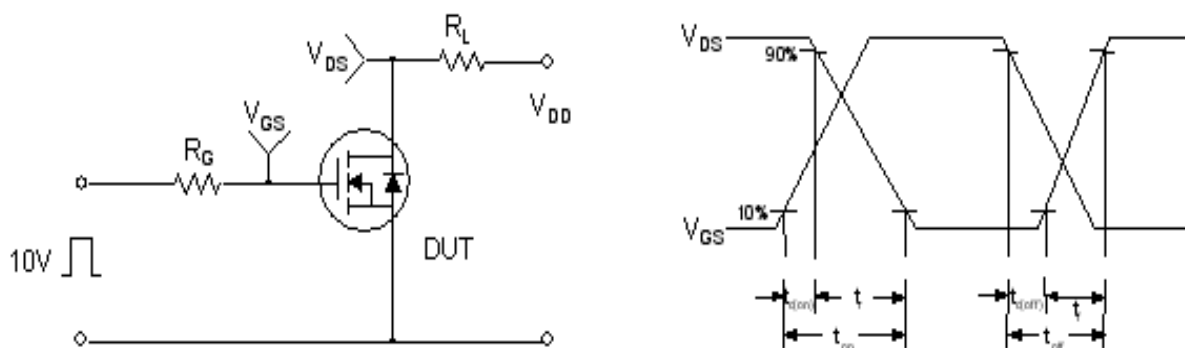
Figure 13. Transient Thermal Response Curve - FCPF400N60



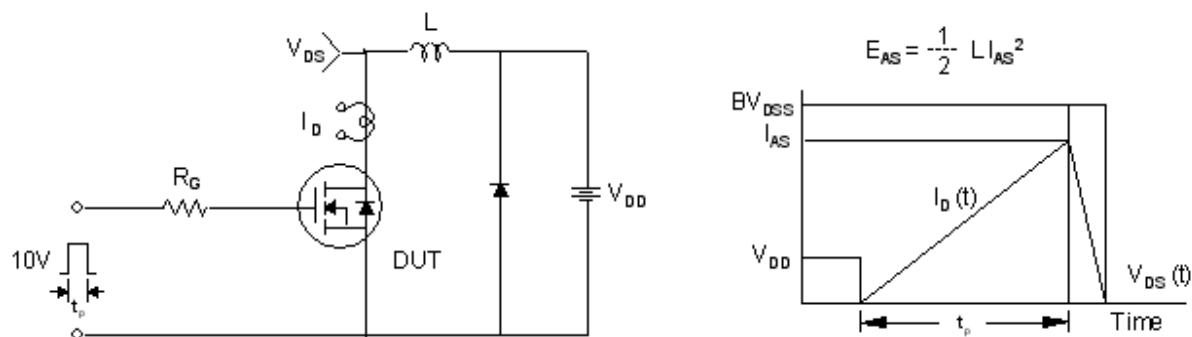
Gate Charge Test Circuit & Waveform



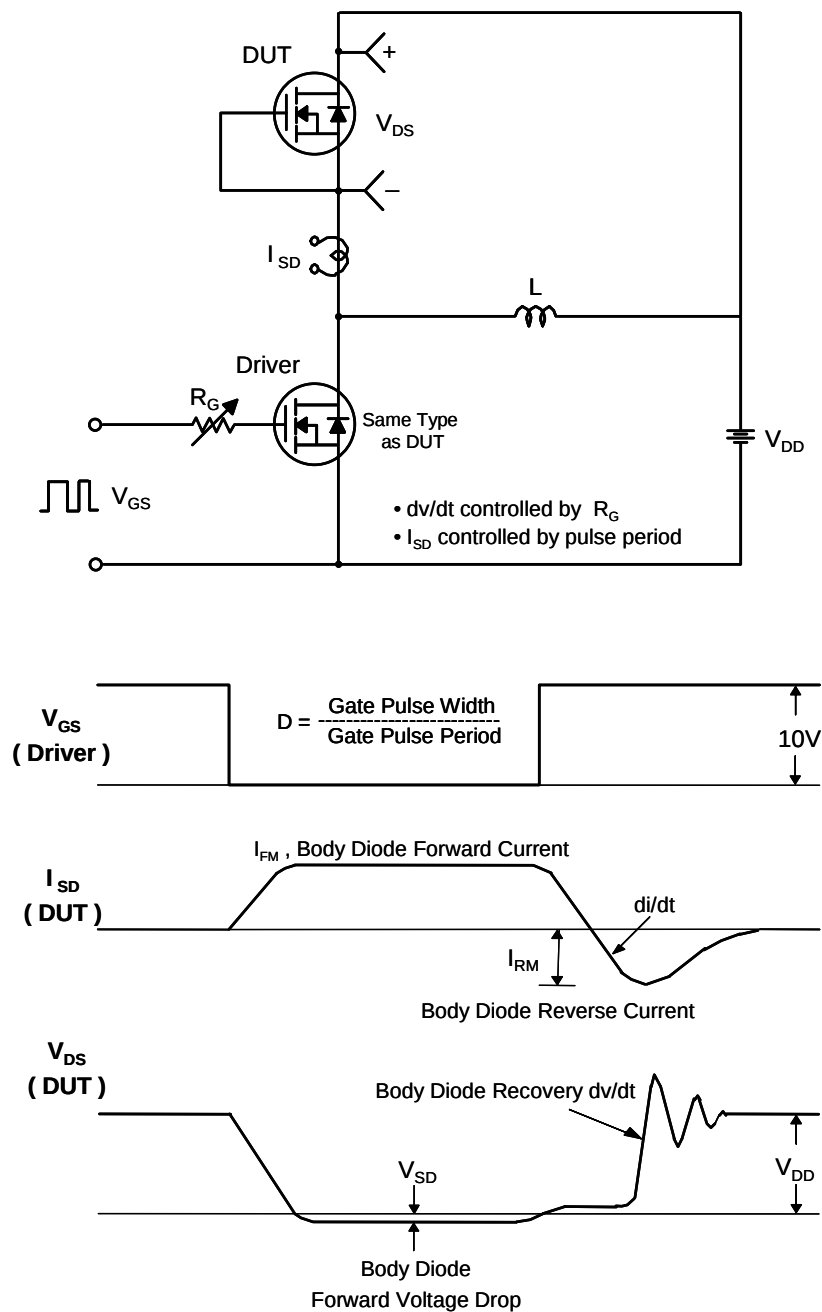
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms





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