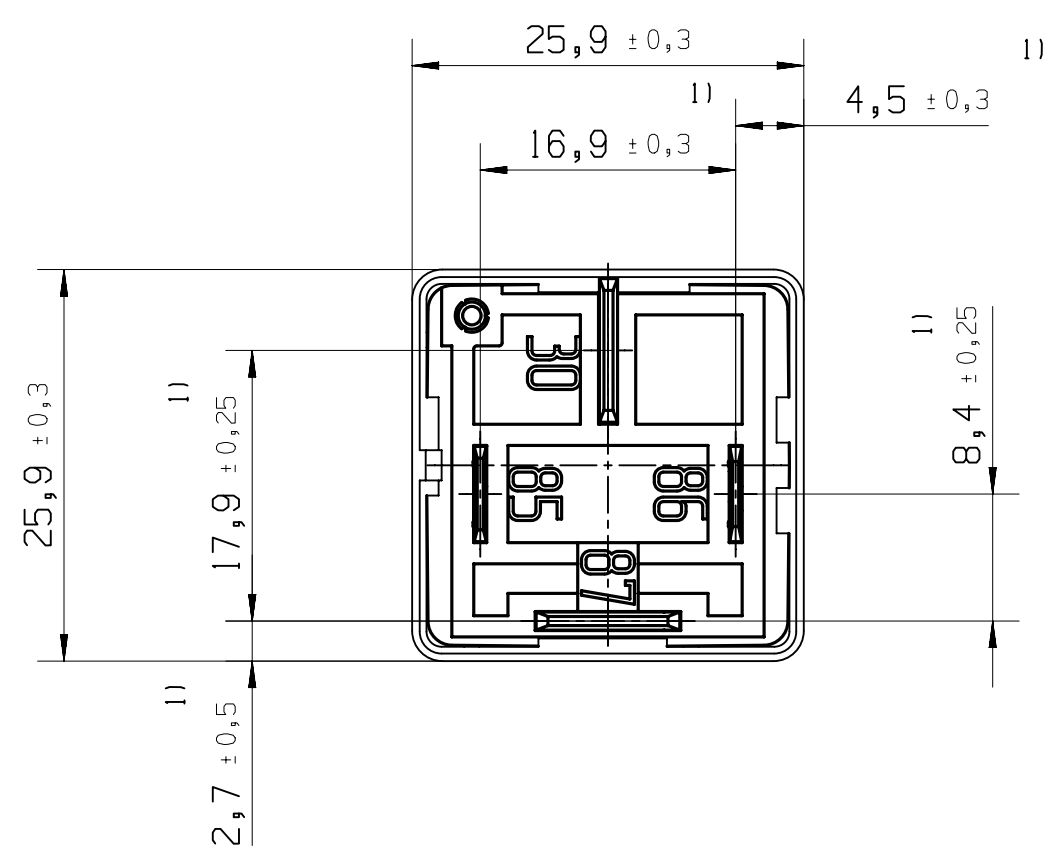
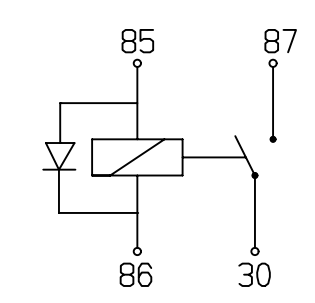
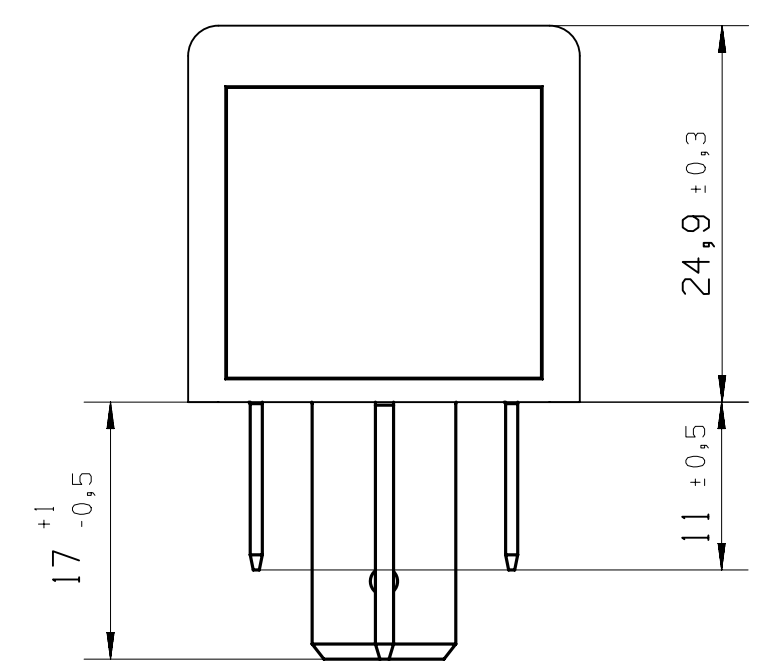
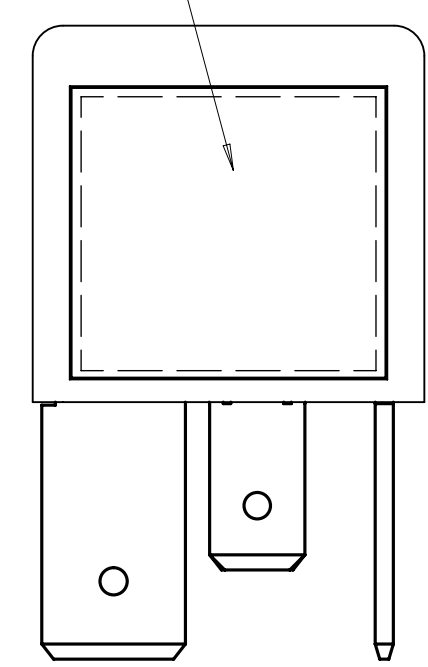




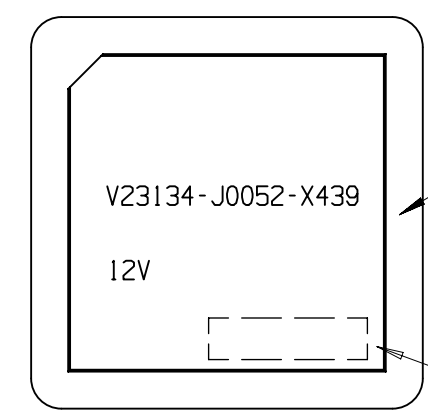
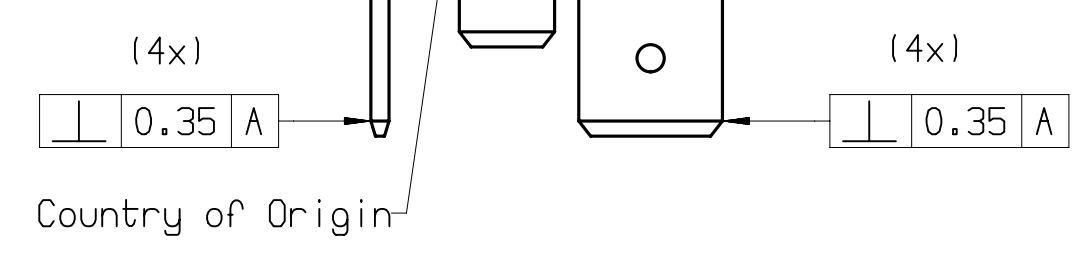
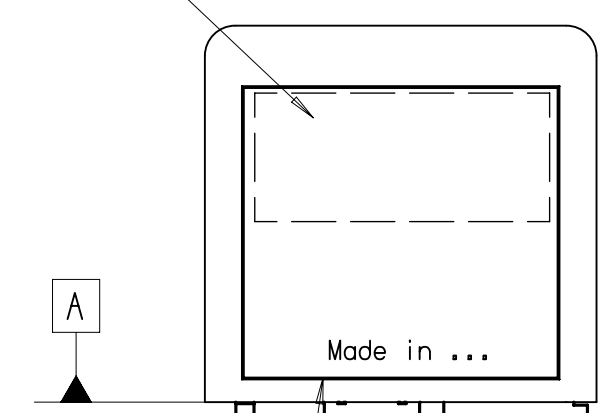
Circuit Diagram



Circuit Diagram



TE Logo



Marking specification injected or stamped.

Date code stamped

Dustproof:
Protection class IP 54 acc. to IEC 529 (EN 60529)
All plug-terminals
Terminals shape similar to ISO 8092-1
Coil Terminals 6,3 mm±0,1 mm x 0,8 mm±0,05 mm
Load Terminals 9,5 mm±0,1 mm x 1,2 mm±0,05 mm

Integrated component: Diode

PART	MATERIAL	COLOUR
Cover	PA 66 GF 25	Black sim. to RAL 90005
Base	PA 66 GF 25	Black sim. to RAL 90005
Coil Terminals 85 and 86	Cu Sn	Pre-tinned Sn
Load Terminals 30 and 87	E-Cu	Pre-tinned Sn

Nominal Voltage: 12 V
Coil Resistance: 91 Ohm ±10% at 23° C

1) At Base level

PART NUMBER	REV	TE PART NAME	COUNTRY OF ORIGIN
1-1414286-0	B	V23134J0052X439	Portugal

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APPLICABLE SPEC.:				FINISH	
				DIMENSIONS APPLY	PLATING
		TOLERANCE UNLESS SPECIFIED OTHERWISE		SCALE 2:1	WEIGHT
				MATERIAL	
		DATE	NAME	PART NAME POWER F RELAY Normally Open	
		DWN. 12.01.2011	L.Sandoval		
		APP.			
		REV.			
		LOCATION	AMR PE EVORA		
A2	---	26JUL2022	L.Sand	DWG NO. C - V23134-J0052-X439	
A1	---	22OCT2021	L.Sand		
A	---	2011-01-12	L.Sand		
REV. CHANGE ORDER				CUSTOMER DRAWING	
		DATE	APP.		

