

SensiBLE Extention Connector (Alternate Function)

J1 connector

	Schematic Name	Type	Func1	Func2	Func3	Func4	Description
1	+5V_USB	S					Input voltage from USB +5V
2	VDD	S					+3.3V from voltage regulator
3	PB1	I/O	TIM3_CH4	TIM3_CH3N	ADC12_16		
4	PC4	I/O	ADC12_13	USART3_TX			
5	PB12	I/O	SPI2_NSS				
6	PB13	I/O	SPI2_SCK	I2C2_SCL			
7	PB14	I/O	SPI2_MISO	I2C2_SDA			
8	PC3	I/O	SPI2_MOSI	ADC123_14			
9	PA3	I/O	USART2_RX	TIM2_CH4	TIM5_CH4	TIM15_CH2	
10	PA2	I/O	USART2_TX	TIM2_CH3 TIM5_CH3	TIM15_CH1	ADC12_7	
11	JTCK/SWCLK	I/O					Serial clk pin SWD
12	JTMS/SWDAT	I/O					Serial data pin SWD
13	nReset	I/O					Reset pin
14	GND	S					Ground pin

J2 connector (14 pin deleted, 2pin changed on GND)

	Schematic Name	Type	Func1	Func2	Func3	Func4	Description
1	PB5	I/O	TIM3_CH2		-	-	
2	GND	S	-	-	-	-	Ground pin
3	PA12	I/O	OTG_FS_DP	CAN1_TX			CAN/USB
4	PA11	I/O	OTG_FS_DM	CAN1_RX			CAN/USB
5	PA10	I/O	TIM1_CH3	OTG_ID			
6	PD2	I/O	SDMMC_CMD	UART5_RX			SDIO_CMD
7	PC12	I/O	SDMMC_CK	UART5_TX		SPI3_MOSI	SDIO_CLK
8	PC11	I/O	SDMMC_D3	USART3_RX	UART4_RX	SPI3_MISO	SDIO DATA3
9	PC10	I/O	SDMMC_D2	USART3_TX	UART4_TX	SPI3_SCK	SDIO DATA2
10	PC9	I/O	SDMMC_D1	TIM3_CH4	TIM8_CH4		SDIO DATA1
11	PC8	I/O	SDMMC_D0	TIM3_CH3	TIM8_CH3		SDIO DATA0
12	I2C1_SCL	I/O	I2C1_SCL				I2C1 bus clock
13	I2C1_SDA	I/O	I2C1_SDA				I2C1 bus data

Development Board (Arduino Compatible)

J2.1 connector

	Schematic Name	Type	Func1	Func2	Func3	Func4	Description
1	NC	S					Not connected
2	VDD	S					+3.3V from voltage regulator
3	nReset	I/O					Reset pin
4	VDD	S					+3.3V from voltage regulator
5	Vin	S					Input voltage 5-8V
6	GND	S					Ground
7	GND	S					Ground
8	Vin	S					Input voltage 5-8V

J2.2 connector

	Schematic Name	Type	Func1	Func2	Func3	Func4	Description
1	PA10	I/O	TIM1_CH3	OTG_ID			
2	PB5	I/O	TIM3_CH2				
3	PB1	I/O	TIM3_CH4	TIM3_CH3N	ADC12_16		
4	PC4	I/O	ADC12_13	USART3_TX			
5	I2C1_SCL	I/O	I2C1_SCL				I2C1 bus clock
6	I2C1_SDA	I/O	I2C1_SDA				I2C1 bus data

J2.3 connector

	Schematic Name	Type	Func1	Func2	Func3	Func4	Description
1	PA12	I/O	OTG_FS_DP	CAN1_TX			CAN/USB
2	PA11	I/O	OTG_FS_DM	CAN1_RX			CAN/USB
3	PB12	I/O	SPI2_NSS				
4	PC3	I/O	SPI2_MOSI	ADC123_14			
5	PB14	I/O	SPI2_MISO	I2C2_SDA			
6	PB13	I/O	SPI2_SCK	I2C2_SCL			
7	GND	S					
8	AVDD	S					Analog supply 3.3V
9	I2C1_SDA	I/O	I2C1_SDA				I2C1 bus clock
10	I2C1_SCL	I/O	I2C1_SCL				I2C1 bus data

J2.4 connector

	Schematic Name	Type	Func1	Func2	Func3	Func4	Description
1	PA3	I/O	USART2_RX	TIM2_CH4	TIM5_CH4	TIM15_CH2	
2	PA2	I/O	USART2_TX	TIM2_CH3 TIM5_CH3	TIM15_CH1	ADC12_7	
3	PC9	I/O	SDMMC_D1	TIM3_CH4	TIM8_CH4		SDIO DATA1
4	PC8	I/O	SDMMC_D0	TIM3_CH3	TIM8_CH3		SDIO DATA0
5	PC12	I/O	SDMMC_CK	UART5_TX		SPI3_MOSI	SDIO_CLK
6	PD2	I/O	SDMMC_CM D	UART5_RX			SDIO_CMD
7	PC10	I/O	SDMMC_D2	USART3_TX	UART4_TX	SPI3_SCK	SDIO DATA2
8	PC11	I/O	SDMMC_D3	USART3_RX	UART4_RX	SPI3_MISO	SDIO DATA3