



# N-Channel 25-V (D-S) MOSFET

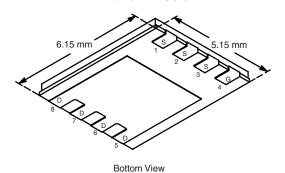
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
25	0.0046 at V <sub>GS</sub> = 10 V	40	13 nC		
25	0.0062 at V <sub>GS</sub> = 4.5 V	40	13110		

### **FEATURES**

- Halogen-free
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

# RoHS COMPLIANT

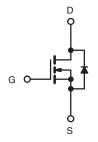
### PowerPAK SO-8



Ordering Information: SiR436DP-T1-GE3 (Lead (Pb)-free and Halogen-free)

### **APPLICATIONS**

- Low-Side Switch
- · Server, VRM



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATIN</b>	<b>IGS</b> $T_A = 25  ^{\circ}C$ ,	unless other	wise noted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		$V_{DS}$	25	V	
Gate-Source Voltage		$V_{GS}$	± 20	v	
	T <sub>C</sub> = 25 °C		40 <sup>a</sup>		
Continuous Drain Current (T = 150 °C)	T <sub>C</sub> = 70 °C	1 , [	40 <sup>a</sup>		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	l l <sup>D</sup>	25 <sup>b, c</sup>	^	
	T <sub>A</sub> = 70 °C	1	20 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	80		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	40		
Avalanche Energy	L=0.11IIII	E <sub>AS</sub>	80	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		40 <sup>a</sup>	A	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	4.1 <sup>b, c</sup>	A	
	T <sub>C</sub> = 25 °C		50		
Maximum Dawar Dissination	T <sub>C</sub> = 70 °C	1 5	32	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	5 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		3.2 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 10 s	$R_{thJA}$	20	25	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	2.0	2.5	O/ <b>VV</b>	

### Notes

- a. Based on  $T_C$  = 25 °C. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. See Solder Profile (<a href="http://www.vishay.com/ppg?73257">https://www.vishay.com/ppg?73257</a>). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 70 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	•					
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	25			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L = 250 uA		24		
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.5		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	1		3	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zava Cata Valtaga Dvain Cuvvant	I <sub>DSS</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V			1	μΑ
Zero Gate Voltage Drain Current		$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
Drain Course On State Registeres	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0038	0.0046	0
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 18 \text{ A}$		0.005	0.0062	Ω
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$		60		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			1715		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		425		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			170		
Total Cata Charge	0	$V_{DS} = 12.5 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		31	47	
Total Gate Charge	Q <sub>g</sub>			13	20	<b>5</b> C
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 12.5 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		4.5		nC
Gate-Drain Charge	$Q_{gd}$			3.9		
Gate Resistance	$R_g$	f = 1 MHz		1.0	2.0	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			22	40	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 12.5 V, $R_L$ = 12.5 $\Omega$		11	25	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1.0 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		32	50	
Fall Time	t <sub>f</sub>			8	25	
Turn-On Delay Time	t <sub>d(on)</sub>			13	25	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 12.5 V, $R_{L}$ = 12.5 $\Omega$		8	15	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 1.0 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		30	40	
Fall Time	t <sub>f</sub>			9	15	
Drain-Source Body Diode Characteristi	cs					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			40	Α
Pulse Diode Forward Current	I <sub>SM</sub>				80	
Body Diode Voltage	$V_{SD}$	$I_S = 4.1 A, V_{GS} = 0 V$		0.75	1.2	٧
Body Diode Reverse Recovery Time	t <sub>rr</sub>			28	56	ns
Body Diode Reverse Recovery Charge				20	40	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 4.1 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		15		nc
Reverse Recovery Rise Time	t <sub>b</sub>			13		ns

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

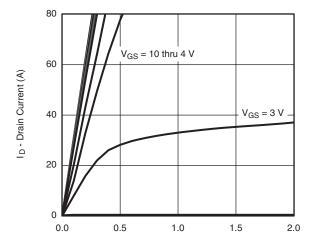
b. Guaranteed by design, not subject to production testing.





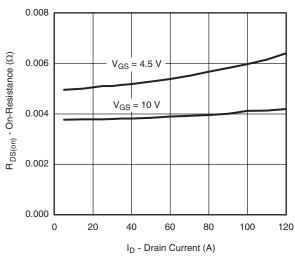


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

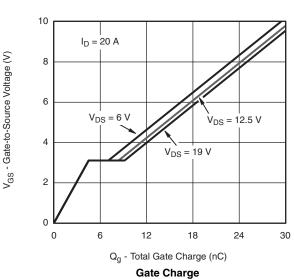


V<sub>DS</sub> - Drain-to-Source Voltage (V)

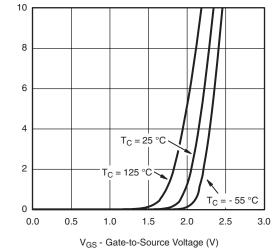
### **Output Characteristics**



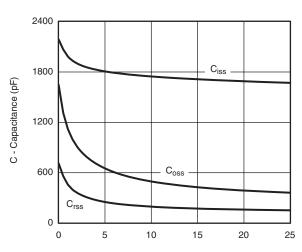
On-Resistance vs. Drain Current and Gate Voltage



ID - Drain Current (A)

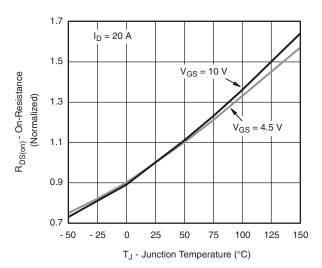


### **Transfer Characteristics**



V<sub>DS</sub> - Drain-to-Source Voltage (V)

### Capacitance

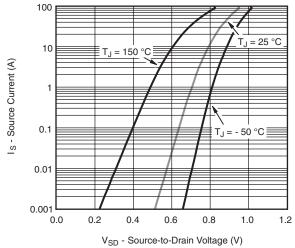


On-Resistance vs. Junction Temperature

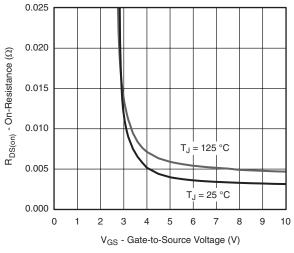
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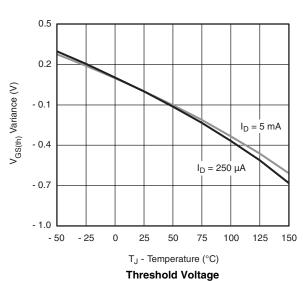
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

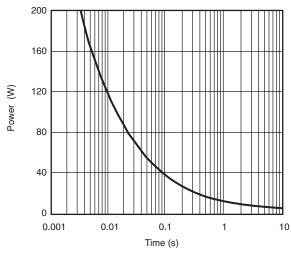


Source-Drain Diode Forward Voltage

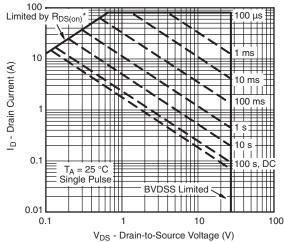


On-Resistance vs. Gate-to-Source Voltage





Single Pulse Power (Junction-to-Ambient)

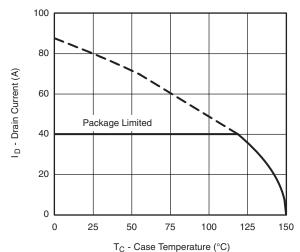


\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

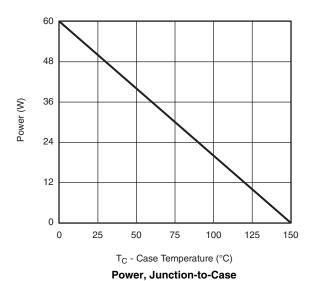
Safe Operating Area, Junction-to-Ambient

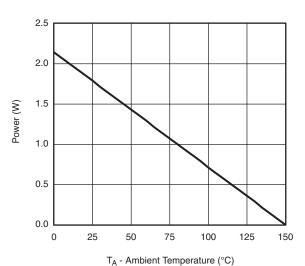


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating\*





Power, Junction-to-Ambient

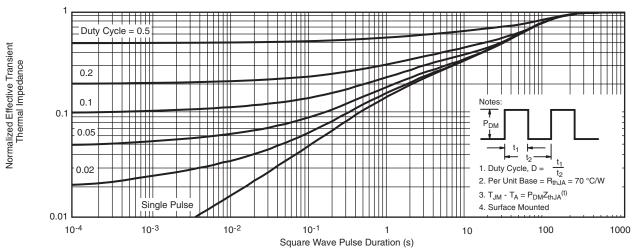
Document Number: 69011 S-82666-Rev. A, 03-Nov-08

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

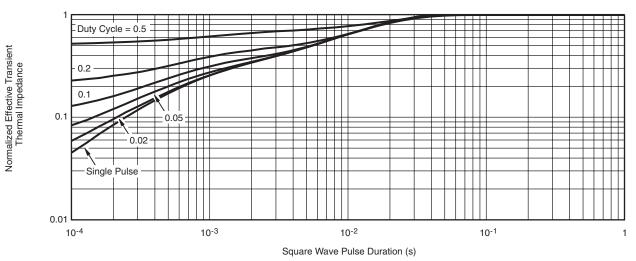
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



### Normalized Thermal Transient Impedance, Junction-to-Ambient



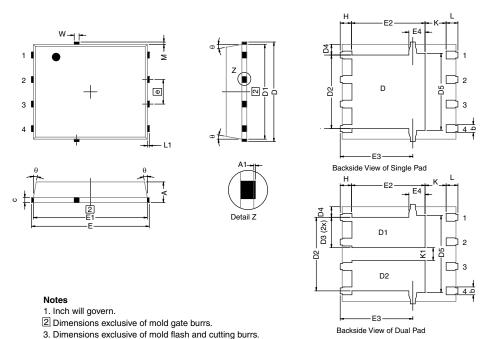
Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?69011">http://www.vishay.com/ppg?69011</a>.



DWG: 5881

# PowerPAK® SO-8, (Single/Dual)

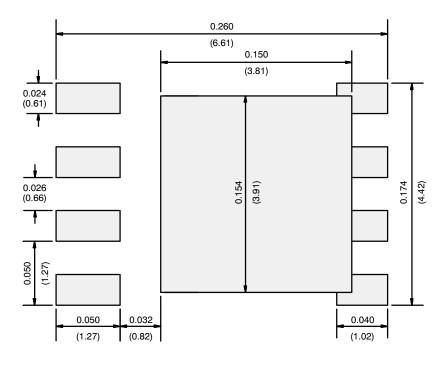


	MILLIMETERS			INCHES			
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
А	0.97	1.04	1.12	0.038	0.041	0.044	
A1		-	0.05	0	-	0.002	
b	0.33	0.41	0.51	0.013	0.016	0.020	
С	0.23	0.28	0.33	0.009	0.011	0.013	
D	5.05	5.15	5.26	0.199	0.203	0.207	
D1	4.80	4.90	5.00	0.189	0.193	0.197	
D2	3.56	3.76	3.91	0.140	0.148	0.154	
D3	1.32	1.50	1.68	0.052	0.059	0.066	
D4	0.57 typ. 0.0225 typ.			0.0225 typ.			
D5		3.98 typ.		0.157 typ.			
E	6.05	6.15	6.25	0.238	0.242	0.246	
E1	5.79	5.89	5.99	0.228	0.232	0.236	
E2 (for AL product)	3.30	3.48	3.66	0.130	0.137	0.144	
E2 (for other product)	3.48	3.66	3.84	0.137	0.144	0.151	
E3	3.68	3.78	3.91	0.145	0.149	0.154	
E4 (for AL product)	0.58 typ. 0.023 typ.			0.023 typ.			
E4 (for other product)	0.75 typ.			0.030 typ.			
е	1.27 BSC			0.050 BSC			
K (for AL product)	1.45 typ.			0.057 typ.			
K (for other product)	1.27 typ.			0.050 typ.			
K1	0.56	=	=	0.022	-	=	
Н	0.51	0.61	0.71	0.020	0.024	0.028	
L	0.51	0.61	0.71	0.020	0.024	0.028	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
θ	0°	-	12°	0°	-	12°	
W	0.15	0.25	0.36	0.006	0.010	0.014	
М	0.125 typ.			0.005 typ.			

Revison: 20-May-13 Document Number: 71655



### RECOMMENDED MINIMUM PADS FOR PowerPAK® SO-8 Single



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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