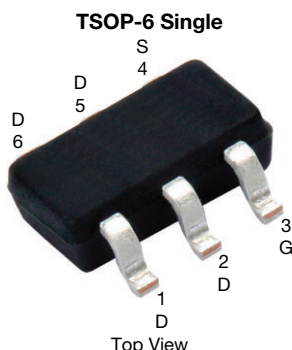


N-Channel 60 V (D-S) MOSFET



Marking code: BV

PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10$ V	0.0230
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5$ V	0.0330
Q_g typ. (nC)	4.4
I_D (A)	8 ^d
Configuration	Single

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	Si3462DV-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V_{DS}	60	V	
Gate-source voltage	V_{GS}	+20 / -20		
Continuous drain current ($T_J = 150$ °C)	$T_C = 25$ °C	8 ^e	A	
	$T_C = 70$ °C	8 ^e		
	$T_A = 25$ °C	6.9 ^{a,b}		
	$T_A = 70$ °C	5.5 ^{a,b}		
Pulsed drain current	I_{DM}	40		
Continuous source-drain diode current	$T_C = 25$ °C	3.8		
	$T_A = 25$ °C	1.8 ^{a,b}		
Maximum power dissipation	$T_C = 25$ °C	4.2	W	
	$T_C = 70$ °C	2.7		
	$T_A = 25$ °C	2 ^{a,b}		
	$T_A = 70$ °C	1.3 ^{a,b}		
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +150		
Soldering recommendations (peak temperature)		260		°C

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^{a,c}	$t \leq 5$ s	R_{thJA}	50	62.5
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	22	30

Notes

- Surface Mounted on 1" x 1" FR4 board
- $t = 5$ s
- Maximum under steady state conditions is 110 °C/W
- Based on $T_C = 25$ °C
- Package limited

FEATURES

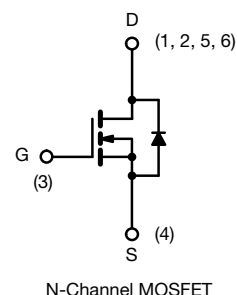
- TrenchFET® Gen IV power MOSFET
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912

APPLICATIONS

- Load Switch
- HDD



RoHS
COMPLIANT
HALOGEN
FREE
Available





SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	60	-	-	V
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	34	-	mV/°C
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-4.8	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.5	-	2.5	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = +20 / -20 V	-	-	100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 70 °C	-	-	10	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 6.9 A	-	0.0186	0.0230	Ω
		V _{GS} = 4.5 V, I _D = 5.5 A	-	0.0245	0.0330	
Forward transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 6.9 A	-	19	-	S
Dynamic ^b						
Input capacitance	C _{iss}	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz	-	580	-	pF
Output capacitance	C _{oss}		-	126	-	
Reverse transfer capacitance	C _{rss}		-	8	-	
Total gate charge	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 6.9 A	-	9.5	19	nC
Gate-source charge	Q _{gs}	V _{DS} = 30 V, V _{GS} = 4.5 V, I _D = 6.9 A	-	4.4	8.8	
Gate-drain charge	Q _{gd}		-	2.2	-	
Gate resistance	R _g	f = 1 MHz	0.28	1.4	2.8	Ω
Turn-on delay time	t _{d(on)}	V _{DD} = 30 V, R _L = 4.3 Ω, I _D ≐ 6.9 A, V _{GEN} = 10 V, R _g = 1 Ω	-	9	18	ns
Rise time	t _r		-	3	6	
Turn-off delay time	t _{d(off)}		-	15	30	
Fall time	t _f		-	2	4	
Turn-on delay time	t _{d(on)}	V _{DD} = 30 V, R _L = 5.4 Ω, I _D ≐ 5.5 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	13	26	
Rise time	t _r		-	28	56	
Turn-off delay time	t _{d(off)}		-	12	24	
Fall time	t _f		-	6	12	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	3.8	A
Pulse diode forward current	I _{SM}		-	-	40	
Body diode voltage	V _{SD}	I _S = 1.8 A, V _{GS} = 0 V	-	0.81	1.1	V
Body diode reverse recovery time	t _{rr}	I _F = 1.8 A, di/dt = 100 A/μs, T _J = 25 °C	-	15	30	ns
Body diode reverse recovery charge	Q _{rr}		-	8	16	nC
Reverse recovery fall time	t _a		-	9	-	ns
Reverse recovery rise time	t _b		-	6	-	

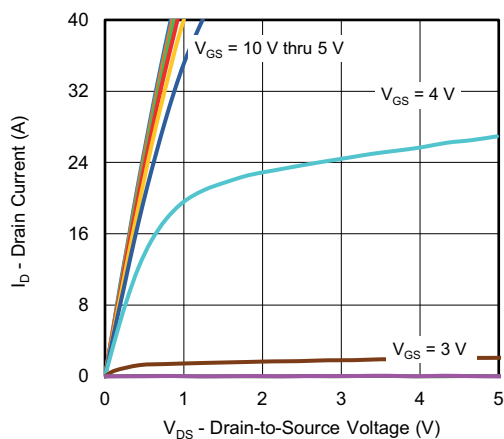
Notes

- a. Pulse test: pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing

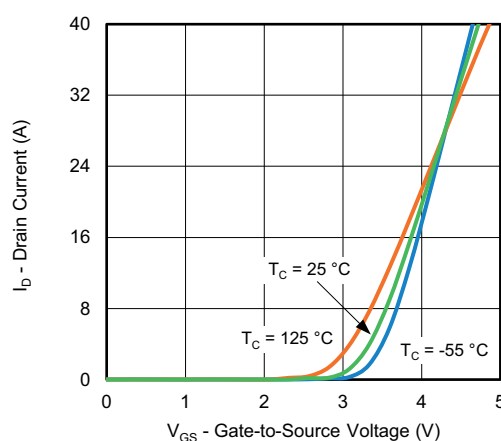
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



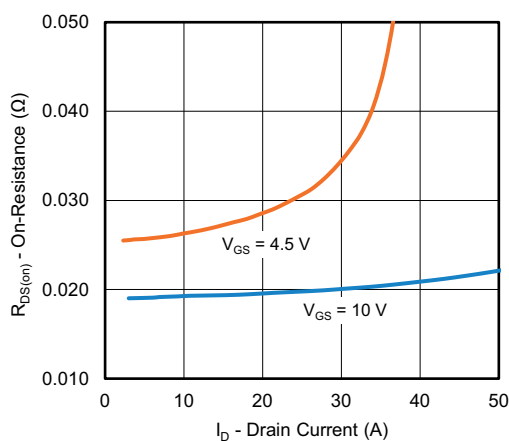
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



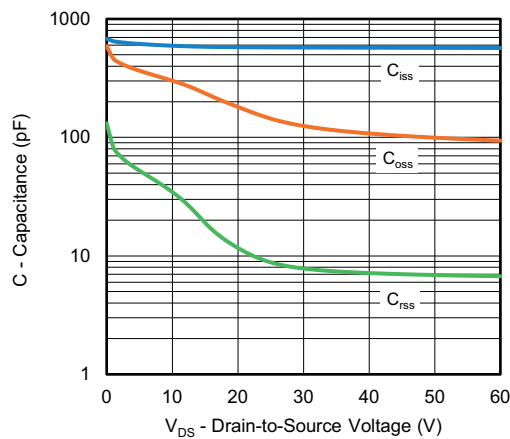
Output Characteristics



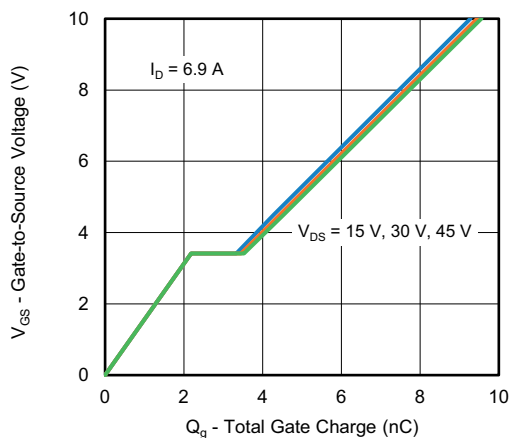
Transfer Characteristics



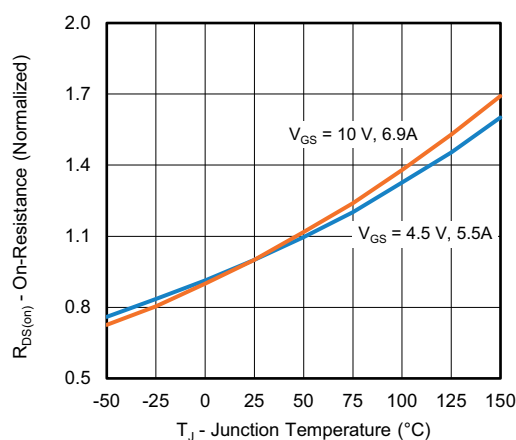
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



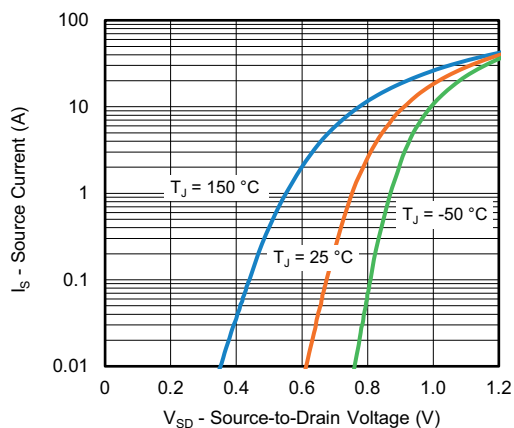
Gate Charge



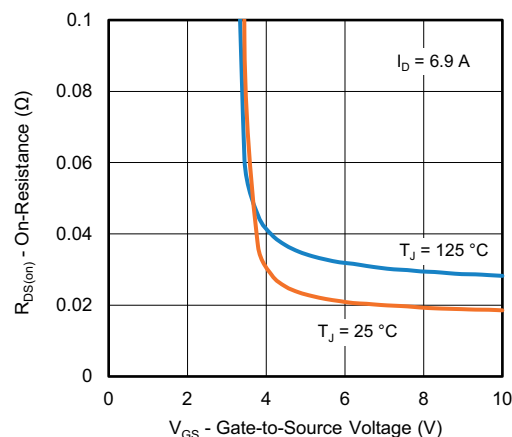
On-Resistance vs. Junction Temperature



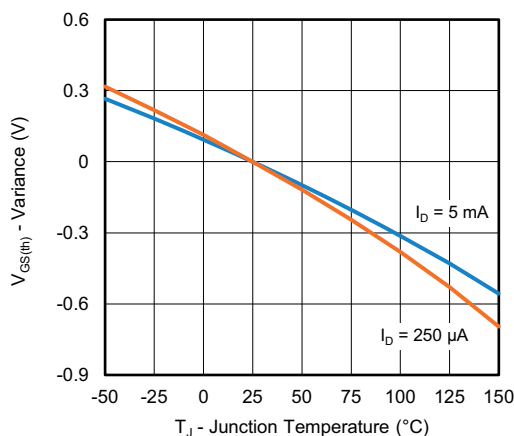
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



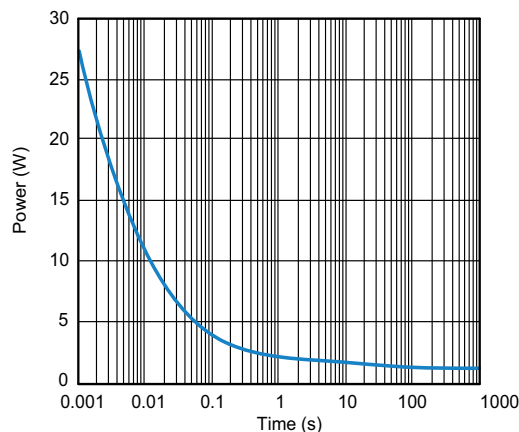
Source-Drain Diode Forward Voltage



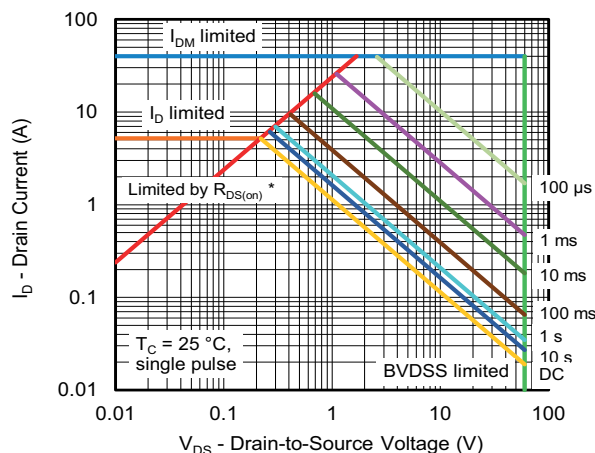
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



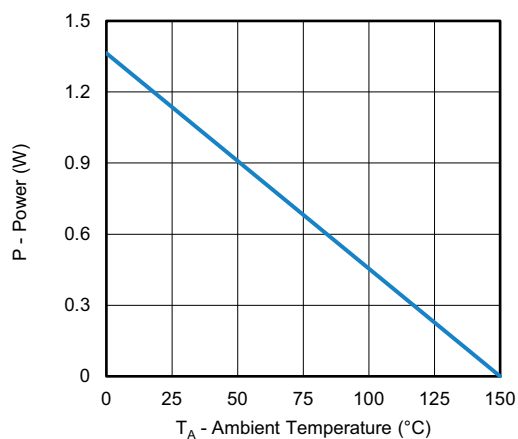
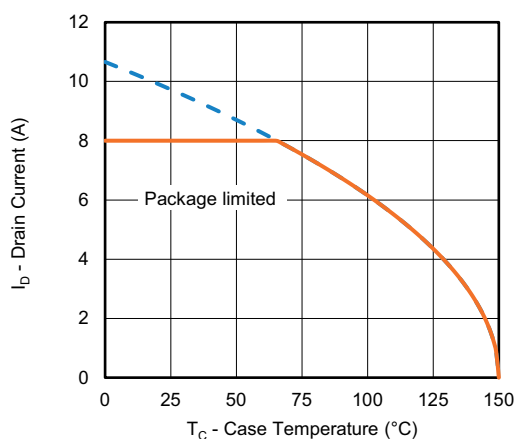
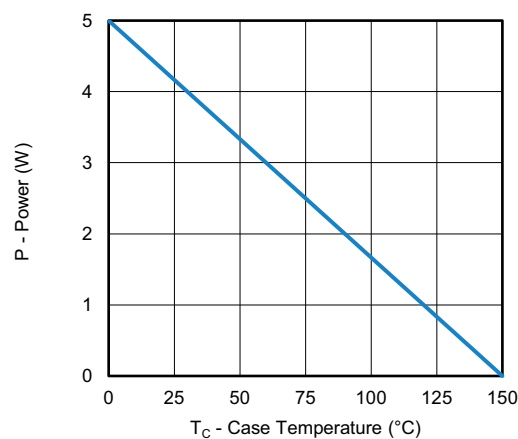
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

Note

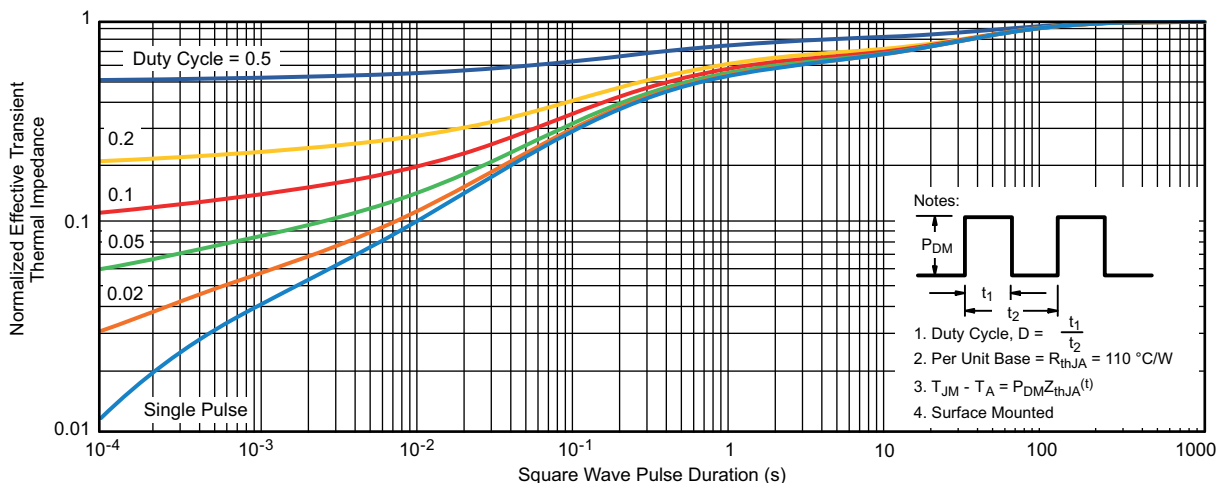
- a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Power, Junction-to-Ambient

Current Derating ^a

Power Derating
Note

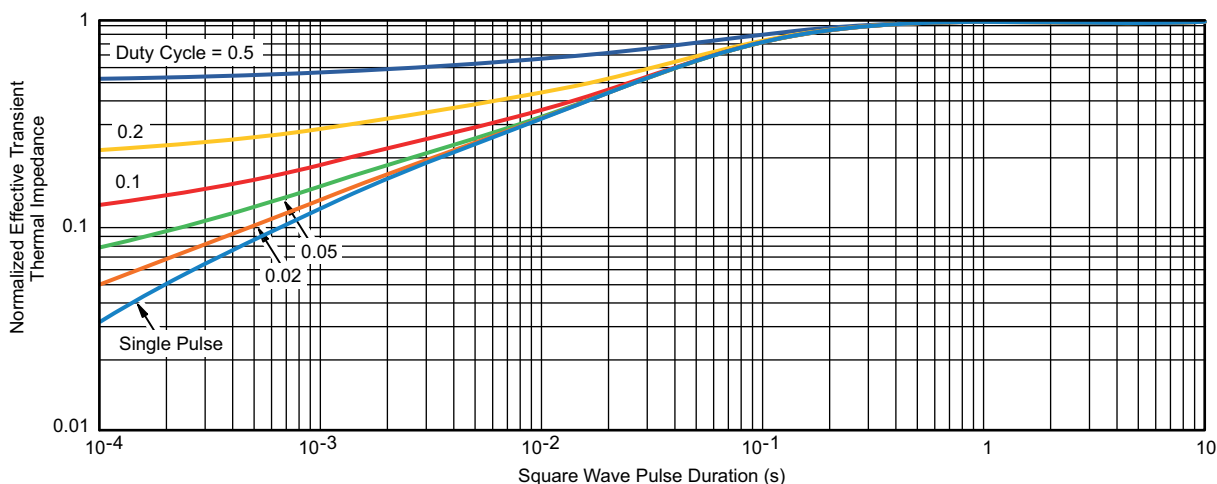
- a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-foot thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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