

#### SBR2A40P1

2.0A SBR<sup>®</sup> SURFACE MOUNT SUPER BARRIER RECTIFIER *PowerDI*<sup>®</sup>123

#### **Features**

- Low Forward Voltage Drop
- Low Leakage Current
- Superior Reverse Avalanche Capability
- Excellent High Temperature Stability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- ±16KV ESD Protection (HBM, 3B)
- ±25KV ESD Protection (IEC61000-4-2 Level 4, Air Discharge)
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q 101 Standards for High Reliability

#### Mechanical Data

- Case: PowerDl<sup>®</sup>123
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 🔞
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.018 grams (approximate)



Top View

### **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Output Current (See Figure 1)	lo	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	50	А
Repetitive Peak Avalanche Power (1µs, 25°C)	PARM	6,000	W

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resistance Junction to Soldering (Note 2) Thermal Resistance Junction to Ambient (Note 3) Thermal Resistance Junction to Ambient (Note 4)	R <sub>0</sub> Js R <sub>0</sub> JA R <sub>0</sub> JA	5 180 115	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

2. Theoretical R<sub>eUS</sub> calculated from the top center of the die straight down to the PCB cathode tab solder junction.

3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

4. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

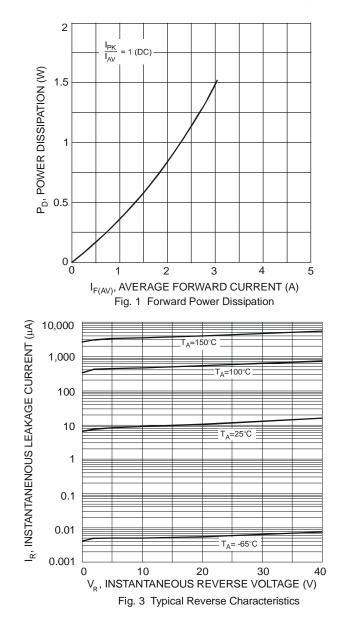


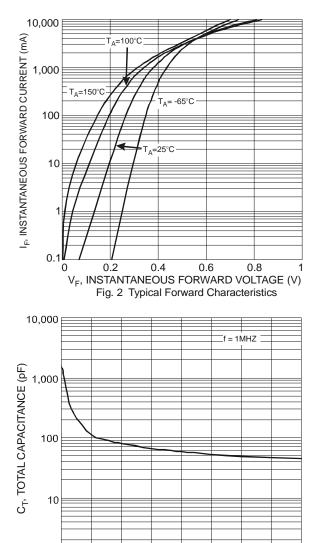
# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V <sub>(BR)R</sub>	40	-	-	V	I <sub>R</sub> = 100μA
	VF	-	0.265	0.315		$I_F = 0.1A, T_J = 25^{\circ}C$
		-	0.38	0.43	V	I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C
Forward Voltage Drop		-	0.45	0.50		I <sub>F</sub> = 2.0A, T <sub>J</sub> = 25°C
		-	0.17	0.22		I <sub>F</sub> = 0.1A, T <sub>J</sub> = 125°C
		-	0.325	0.375		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C
		-	0.42	0.47		I <sub>F</sub> = 2.0A, T <sub>J</sub> = 125°C
	I <sub>R</sub>	-	8	40	μA	V <sub>R</sub> = 5V, T <sub>J</sub> = 25°C
Leakage Current (Note 5)		-	16	100	μA	V <sub>R</sub> = 40V, T <sub>J</sub> = 25°C
		-	1.3	8	mA	V <sub>R</sub> = 5V, T <sub>J</sub> = 125°C
		-	2.1	10	mA	V <sub>R</sub> = 40V, T <sub>J</sub> = 125°C

Notes:

5. Short duration pulse test used to minimize self-heating effect.





20 V<sub>R</sub>, DC REVERSE VOLTAGE (V) Fig. 4 Total Capacitance vs. Reverse Voltage

25

30

35

SBR and PowerDI are registered trademarks of Diodes Incorporated.

1

0

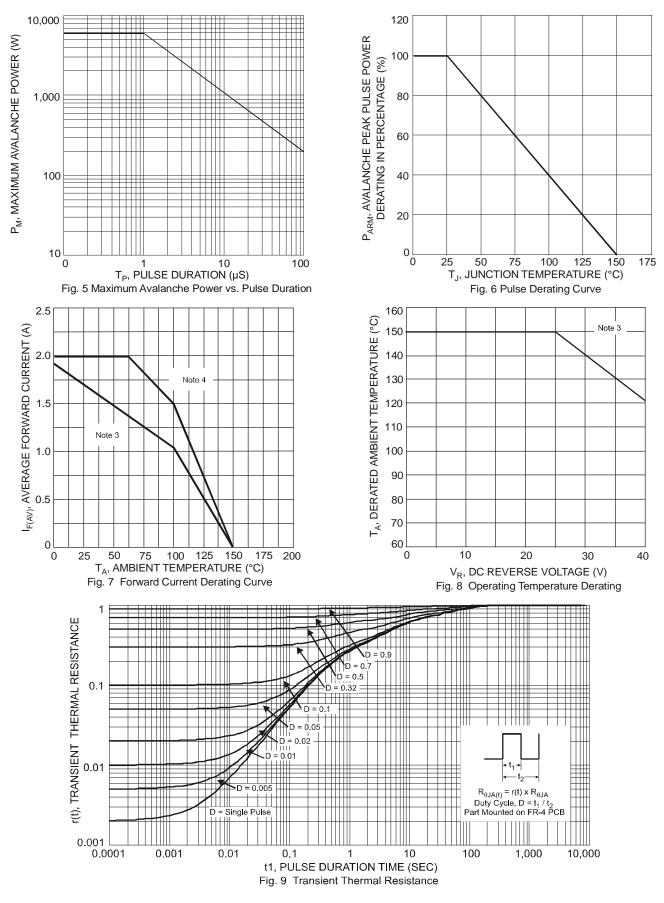
5

10

15

40





SBR and PowerDI are registered trademarks of Diodes Incorporated.

3 of 5 www.diodes.com

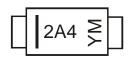


## Ordering Information (Note 6)

Part Number	Case	Packaging		
SBR2A40P1-7	PowerDI <sup>®</sup> 123	3000/Tape & Reel		

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**

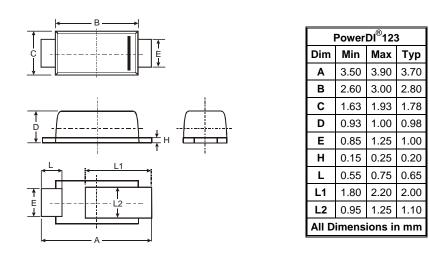


2A4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

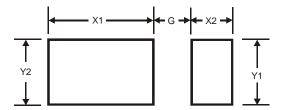
Date Code Key

Year	2006	2007	20	08	2009	2010	2011	2012	20	13	2014	2015
Code	Т	U	١	/	W	Х	Y	Z	1	۹.	В	С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

## **Package Outline Dimensions**



## Suggested Pad Layout



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

SBR and PowerDI are registered trademarks of Diodes Incorporated. SBR2A40P1 4 of 5 Document number: DS30919 Rev. 10 - 2 www.diodes.com



#### **IMPORTANT NOTICE**

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

#### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2010, Diodes Incorporated

www.diodes.com