SMF05 and SMF12
TVS Diode Array
For ESD and Latch-Up Protection

PROTECTION PRODUCTS

Description
The SMF series TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD and other voltage-induced transient events. They are designed for use in applications where board space is at a premium. Each device will protect up to four lines. They are unidirectional devices and may be used on lines where the signal polarities are above ground.

TVS diodes are solid-state devices designed specifically for transient suppression. They feature large cross-sectional area junctions for conducting high transient currents. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

The SMF series devices may be used to meet the immunity requirements of IEC 61000-4-2, level 4. The small SC70 package makes them ideal for use in portable electronics such as cell phones, PDA’s, notebook computers, and digital cameras.

Features
- Transient protection for data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- Small package for use in portable electronics
- Protects four I/O lines
- Working voltage: 5V and 12V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon-avalanche technology

Mechanical Characteristics
- EIAJ SC70-5L package
- Molding compound flammability rating: UL 94V-0
- Marking: Marking Code
- Packaging: Tape and Reel

Applications
- Cellular Handsets and Accessories
- Cordless Phones
- Personal Digital Assistants (PDA’s)
- Notebooks & Handhelds
- Portable Instrumentation
- Digital Cameras
- Peripherals
- MP3 Players

Circuit Diagram

Schematic & PIN Configuration

SC70-5L (Top View)
## Absolute Maximum Rating

<table>
<thead>
<tr>
<th>Rating</th>
<th>Symbol</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Peak Pulse Power (tp = 8/20μs)</td>
<td>$P_{pp}$</td>
<td>200</td>
<td>Watts</td>
</tr>
<tr>
<td>Peak Forward Voltage (I$_f$ = 1A, tp=8/20μs)</td>
<td>$V_{fp}$</td>
<td>1.5</td>
<td>V</td>
</tr>
<tr>
<td>ESD per IEC 61000-4-2 (Air)</td>
<td>$V_{ESD}$</td>
<td>20</td>
<td>kV</td>
</tr>
<tr>
<td>ESD per IEC 61000-4-2 (Contact)</td>
<td></td>
<td>15</td>
<td>kV</td>
</tr>
<tr>
<td>Lead Soldering Temperature</td>
<td>$T_L$</td>
<td>260 (10 seconds)</td>
<td>°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>$T_J$</td>
<td>-55 to +125</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>$T_{STG}$</td>
<td>-55 to +150</td>
<td>°C</td>
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## Electrical Characteristics

### SMF05

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
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<tbody>
<tr>
<td>Reverse Stand-Off Voltage</td>
<td>$V_{RWM}$</td>
<td></td>
<td>5</td>
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<td></td>
<td>V</td>
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<tr>
<td>Reverse Breakdown Voltage</td>
<td>$V_{BR}$</td>
<td>I$_f$ = 1mA</td>
<td>6</td>
<td></td>
<td></td>
<td>V</td>
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<tr>
<td>Reverse Leakage Current</td>
<td>I$_r$</td>
<td>$V_{RWM}$ = 5V, T=25°C</td>
<td>10</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Clamping Voltage</td>
<td>$V_C$</td>
<td>I$_{pp}$ = 1A, t$_p$ = 8/20μs</td>
<td>9.5</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Clamping Voltage</td>
<td>$V_C$</td>
<td>I$_{pp}$ = 12A, t$_p$ = 8/20μs</td>
<td>12.5</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Peak Pulse Current</td>
<td>I$_{pp}$</td>
<td>t$_p$ = 8/20μs</td>
<td>12</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>Junction Capacitance</td>
<td>C$_j$</td>
<td>Between I/O pins and Ground V$_r$ = 0V, f = 1MHz</td>
<td>150</td>
<td></td>
<td>175</td>
<td>pF</td>
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### SMF12

<table>
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<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reverse Stand-Off Voltage</td>
<td>$V_{RWM}$</td>
<td></td>
<td>12</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Reverse Breakdown Voltage</td>
<td>$V_{BR}$</td>
<td>I$_f$ = 1mA</td>
<td>13.3</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Reverse Leakage Current</td>
<td>I$_r$</td>
<td>$V_{RWM}$ = 12V, T=25°C</td>
<td>1</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Clamping Voltage</td>
<td>$V_C$</td>
<td>I$_{pp}$ = 1A, t$_p$ = 8/20μs</td>
<td>19</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Clamping Voltage</td>
<td>$V_C$</td>
<td>I$_{pp}$ = 8A, t$_p$ = 8/20μs</td>
<td>25</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Peak Pulse Current</td>
<td>I$_{pp}$</td>
<td>t$_p$ = 8/20μs</td>
<td>8</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>Junction Capacitance</td>
<td>C$_j$</td>
<td>Between I/O pins and Ground V$_r$ = 0V, f = 1MHz</td>
<td>60</td>
<td></td>
<td>75</td>
<td>pF</td>
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</tbody>
</table>
PROTECTION PRODUCTS

Typical Characteristics

Non-Repetitive Peak Pulse Power vs. Pulse Time

Pulse Waveform

Clamping Voltage vs. Peak Pulse Current

Capacitance vs. Reverse Voltage

SMF05 Insertion Loss S21
SMF05 ESD Clamping
(8kV Contact per IEC 61000-4-2)

C1 Max
19.2 V

SMF12 ESD Clamping
(8kV Contact per IEC 61000-4-2)

C2 Max
24.0 V
Device Connection for Protection of Four Data Lines

The SMFxx is designed to protect up to four unidirectional data lines. The device is connected as follows:

1. Unidirectional protection of four I/O lines is achieved by connecting pins 1, 3, 4, and 5 to the data lines. Pin 2 is connected to ground. The ground connection should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.

Circuit Board Layout Recommendations for Suppression of ESD.

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the SMFxx near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the SMFxx and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.
## Typical Applications

![Diagram of SMF05 and SMF12 protection products](image)

- **Connector**
- **IC to be protected**

**SMF05**
SMF05 and SMF12

PROTECTION PRODUCTS
Outline Drawing - SC70 5L

NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. DATUMS A1 AND B1 TO BE DETERMINED AT DATUM PLANE X1.
3. DIMENSIONS E1 AND D DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
4. REFERENCE JEDEC STD MO-203, VARIATION AA.

Land Pattern - SC70 5L

NOTES:
1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.
PROTECTION PRODUCTS

Marking Codes

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Marking Code</th>
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<tbody>
<tr>
<td>SMF05</td>
<td>F05</td>
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<tr>
<td>SMF12</td>
<td>F12</td>
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</tbody>
</table>

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Lead Finish</th>
<th>Qty per Reel</th>
<th>Reel Size</th>
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<tbody>
<tr>
<td>SMF05.TC</td>
<td>SnPb</td>
<td>3,000</td>
<td>7 Inch</td>
</tr>
<tr>
<td>SMF12.TC</td>
<td>SnPb</td>
<td>3,000</td>
<td>7 Inch</td>
</tr>
<tr>
<td>SMF05.TCT</td>
<td>Pb free</td>
<td>3,000</td>
<td>7 Inch</td>
</tr>
<tr>
<td>SMF12.TCT</td>
<td>Pb free</td>
<td>3,000</td>
<td>7 Inch</td>
</tr>
</tbody>
</table>

Contact Information

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