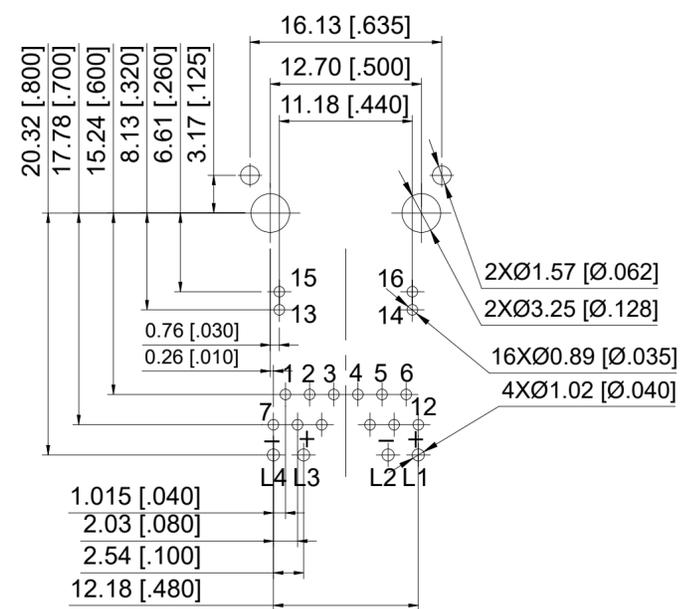
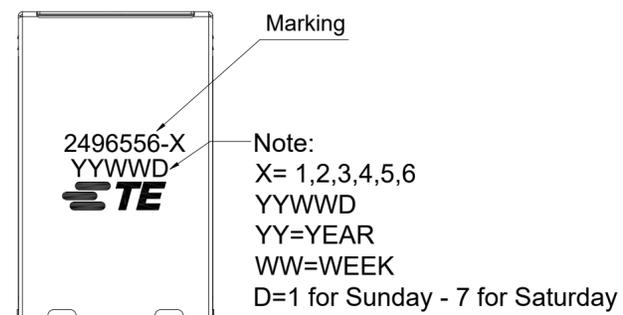
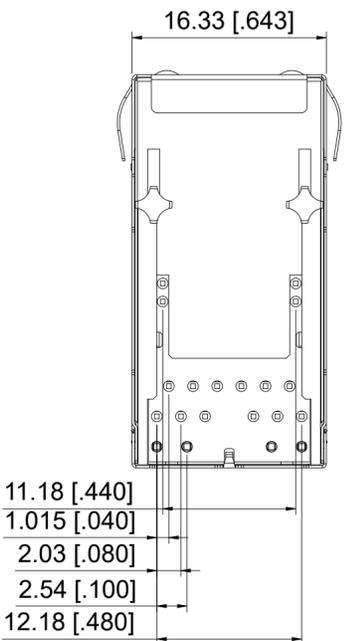
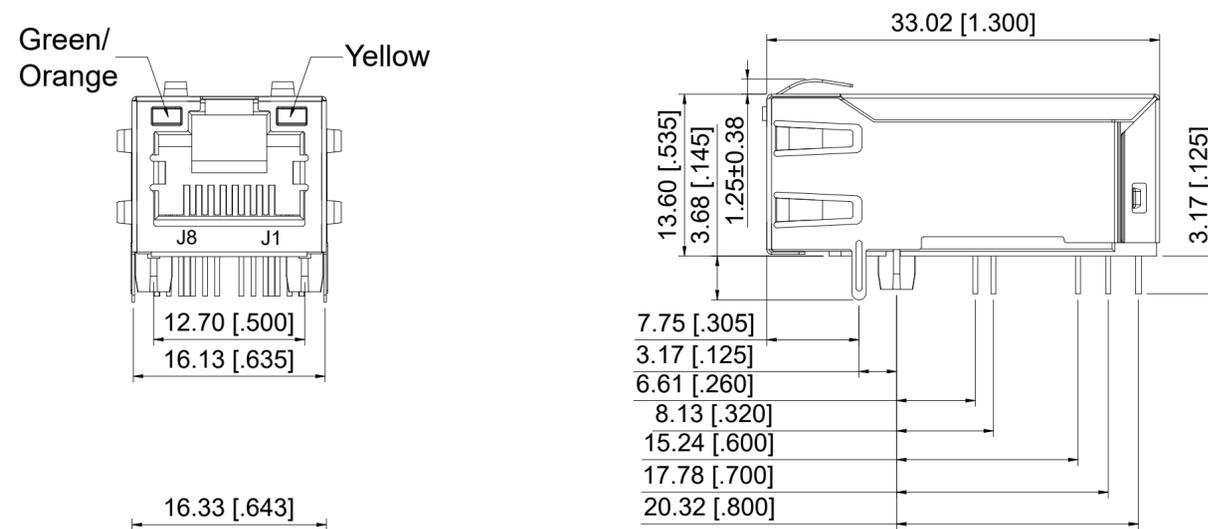


REVISIONS				
P	LTR	DESCRIPTION	DATE	APVD
A		REVISED PER ECN-25-301496	10JAN2025	TR RCG

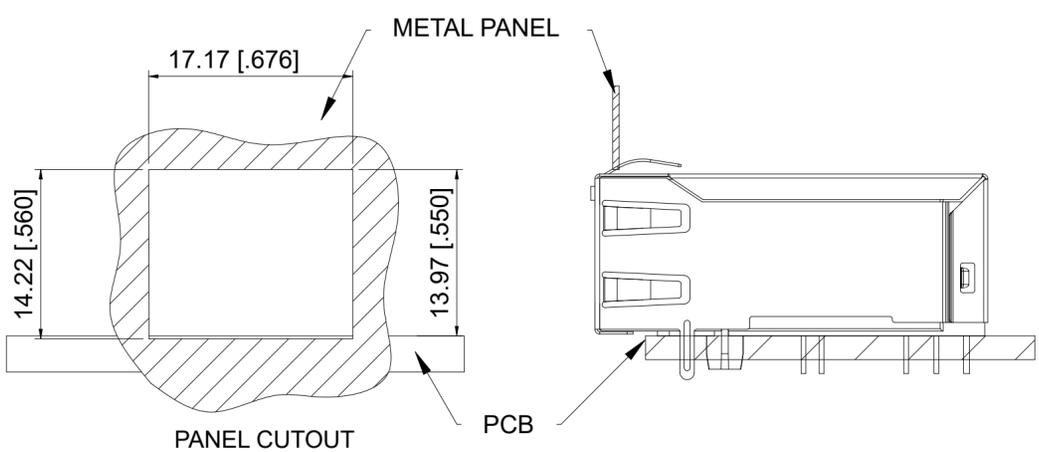
NOTE :

- 1.HOUSING:GLASS FILLED PA46(BLACK) UL94V-0(RoHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICRINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH (REFER PART NUMBER TABLE) GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS.
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE:-40°C TO +85°C.  
STORAGE TEMPERATURE:-40°C TO +85°C.



RECOMMENDED P.C.B LAYOUT  
TOP VIEW (COMPONENT SIDE)

Dimension Tolerance:  
All PCB LAYOUT  
Tolerance:±0.05[.002]

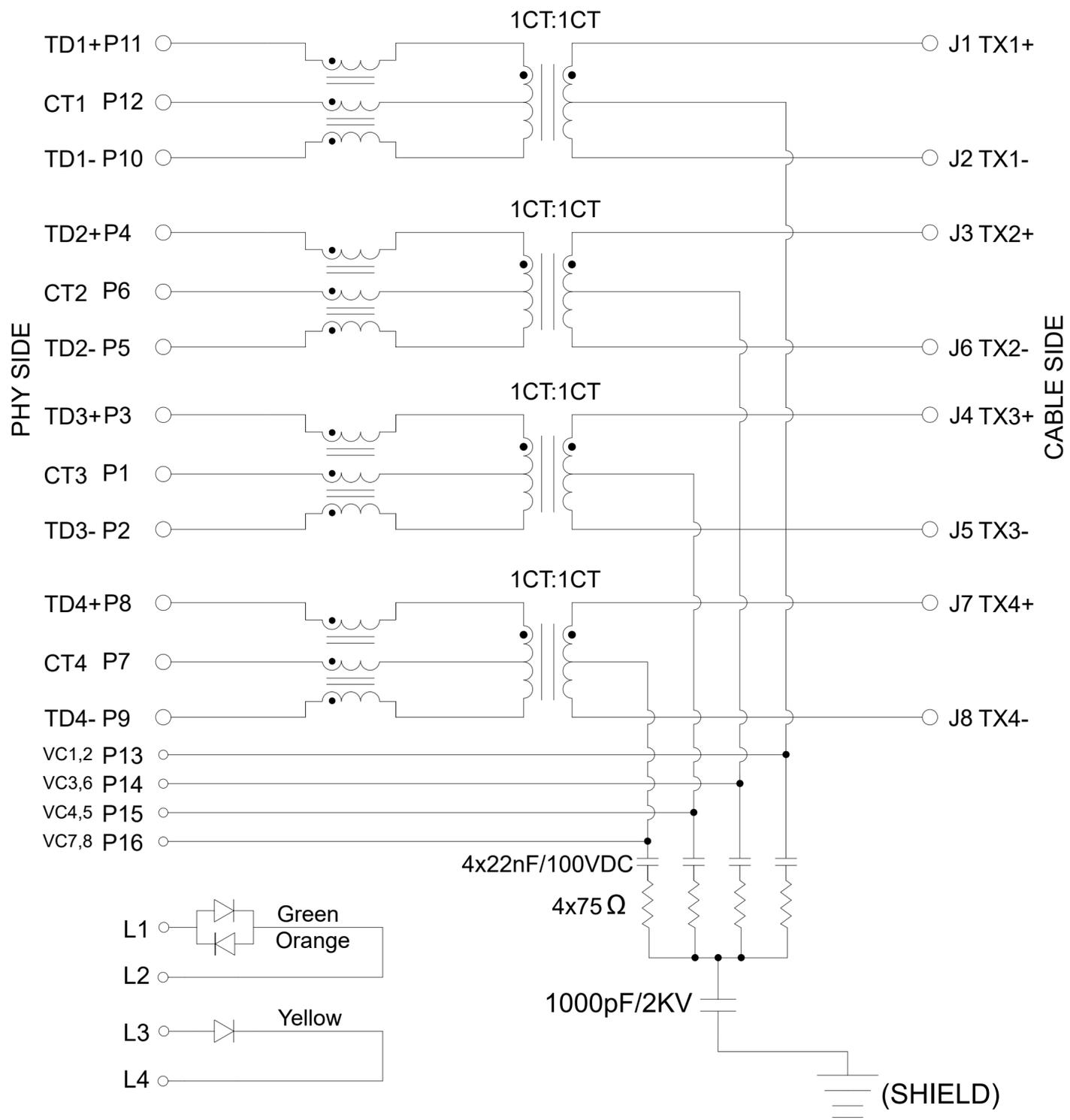


GOLD PLATING THICKNESS	PART NUMBER
GF	2496556-6
0.0762 μm [3μ"]	2496556-5
0.1524 μm [6μ"]	2496556-4
0.381 μm [15μ"]	2496556-3
0.762 μm [30μ"]	2496556-2
1.27 μm [50μ"]	2496556-1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TARUN, R 23OCT2024	TE Connectivity	
DIMENSIONS: mm [INCHES]		CHK RAMESH, KIVADER 25OCT2024	NAME RJ45 W/MAGNET 1X1 1G POE 60W LED	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD RAGHAVAN, CG 25OCT2024	PRODUCT SPEC 108-161622	
0 PLC ± -		APPLICATION SPEC 114-161219		
1 PLC ± 0.35		SIZE A2		
2 PLC ± 0.25		CAGE CODE 00779		
3 PLC ± 0.15		DRAWING NO 2496556		
4 PLC ± - ± 2'		RESTRICTED TO -		
MATERIAL -		WEIGHT -		
FINISH -		CUSTOMER DRAWING		
		SCALE 1:1		
		SHEET 1 of 2		
		REV A		

REVISIONS				
P	LTR	DESCRIPTION	DATE	APVD
-	-	SEE SHEET 1	-	-

# Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
TURNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	220uH MIN@100KHz/100mV With 19mA DC Bias For(CHANNEL1,2,3,4) 350uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1,2,3,4)
INSERTION LOSS(IL)	-1.2dB MAX@0.3MHZ-100MHZ;
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHZ-30MHZ; -16dB MIN@30MHZ-60MHZ; -12dB MIN@60MHZ-80MHZ;-10dB MIN@80MHZ-100MHZ;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHZ-100MHZ;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHZ-100MHZ;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS) For PSE Channel (P13 & P14, P15 & P16)
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	570nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V
ORANGE	605nm	2.5V	2.1V

\*WITH A FORWARD CURRENT OF 20mA

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN TARUN, R 23OCT2024		NAME RJ45 W/MAGNET 1X1 1G POE 60W LED
CHK RAMESH, KIVADER 25OCT2024		PRODUCT SPEC 108-161622
APVD RAGHAVAN, CG 25OCT2024		APPLICATION SPEC 114-161219
SIZE: A2 CAGE CODE: 00779 DRAWING NO: 114-161219		
MATERIAL: - FINISH: -		RESTRICTED TO: -
CUSTOMER DRAWING		SCALE: 1:1 SHEET: 2 OF 2 REV: A