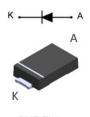


650 V, 4 A high surge silicon carbide power Schottky diode



SMB Flat





Product status link

STPSC4G065UF

Product summary			
I _{F(AV)}	4 A		
V _{RRM}	650 V		
T _j (max.) 175 °C			
V _F (typ.)	1.30 V		

Features

- None or negligible reverse recovery charge in application current range
- · Switching behaviour independent of temperature
- · High forward surge capability
- Operating T_i from -55 °C to +175 °C
- ECOPACK2 compliant component

Application

- SMPS in telecom power
- Datacenter
- Industrial equipment
- Solar converter
- Air conditioning equipment

Description

The SiC diode, available in SMB-Flat, is an ultrahigh performance power Schottky rectifier. It is manufactured using a silicon carbide substrate. The wide band-gap material allows the design of a low V_{F} Schottky diode structure with a 650 V rating. Thanks to the Schottky construction, no recovery is shown at turn-off and ringing patterns are negligible. The minimal capacitive turn-off behavior is independent of temperature.

Based on technology optimization, this diode has an improved forward surge current capability, making it ideal for use in PFC, where this ST SiC diode boosts the performance in hard switching conditions while bringing robustness to the design. Its high forward surge capability ensures a good robustness during transient phases.





Characteristics

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified)

Symbol	Parameter			Value	Unit
V_{RRM}	Repetitive peak reverse voltage			650	V
I _{F(RMS)}	Forward rms current			10	Α
I _{F(AV)}	Average forward current	$T_{I} = 70 ^{\circ}\text{C}, T_{j} = 17$	5 °C, δ = 1	4	Α
I _{FRM}	Repetitive peak forward current	$T_{I} = 70 ^{\circ}\text{C}, T_{j} = 175 ^{\circ}\text{C}, \delta = 0.1,$ $f_{SW} > 10 \text{kHz}$		16	А
	$t_{\rm p}$ = 10 ms $T_{\rm c}$ = 25 °C	T _c = 25 °C	30		
I _{FSM}	Surge non repetitive forward current	sinusoidal	T _c = 150 °C	27	Α
			T _C = 25 °C	400	
T _{stg}	Storage temperature range			-65 to +175	°C
Tj	Operating junction temperature range			-55 to +175	°C

Table 2. Thermal resistance parameters

Symbol	bol Parameter –	Value		Unit
Symbol	Falallielei		Max.	
$R_{th(j-l)}$	Junction to lead	10.5	15	°C/W

For more information, you can refer to the following application note:

• AN5088 : Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
		T _j = 25 °C	\\- - \\ · ·	-	4	40	μA
I _R ⁽¹⁾ Reverse leakage current	T _j = 175 °C	$V_R = V_{RRM}$	-	23	170		
V _F ⁽²⁾ Forward voltage drop		T _j = 25 °C	I _F = 4 A	-	1.3	1.45	V
V _F ⁽²⁾ Forward voltage drop	T _j = 175 °C	IF - 4 A	-	1.49	1.70	V	

- 1. Pulse test: $t_p = 10 \text{ ms}, \ \delta < 2\%$
- 2. Pulse test: t_p = 380 μ s, δ < 2%

To evaluate the conduction losses, use the following equation:

 $P = 0.879 \text{ x } I_{F(AV)} + 0.206 \text{ x } I_{F}^{2}_{(RMS)}$

For more information, you can refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses on a power diode

DS14892 - Rev 1 page 2/9



Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Q _{Cj} (1)	Total capacitive charge	V _R = 400 V	-	14.5	-	nC
C _i Total capacitance	V _R = 0 V, T _c = 25 °C, F = 1 MHz	-	285	-	pF	
	V_R = 400 V, T_c = 25 °C, F = 1 MHz	-	20	-	þΓ	

1. Most accurate value for the capacitive charge: $Q_{cj}(V_R) = \int\limits_0^{V_R} C_j(V) dV$

Figure 1. Thermal transient impedance model circuit of the diode – $Z_{th(j-l)}$

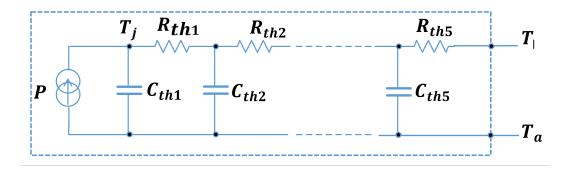


Table 5. Components typical values of the diode thermal transient impedance model $Z_{th(j-l)}$

Ref.	ef. Value (K/W) Ref.		Value (J/K)
R _{th1}	606.69 m	C _{th1}	0.45 m
R _{th2}	1442.64 m	C _{th2}	1.67 m
R _{th3}	5125.21 m	C _{th3}	6.15 m
R _{th4}	2580.19 m	C _{th4}	42.94 m
R _{th5}	701.27 m	C _{th5}	928.09 m

DS14892 - Rev 1 page 3/9



Characteristics (curves) 1.1

(typical values) Pulse test : t_p=380 μs T_o=100 °C

Figure 2. Forward voltage drop versus forward current

T_a=150 °C

Figure 3. Reverse leakage current versus reverse voltage applied (typical values)

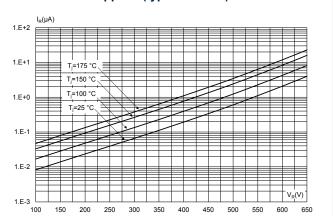


Figure 4. Peak forward current versus lead temperature $(f_{sw} > 10 \text{ kHz})$

2.2

2.0

0.4 0.6

0.8 1.0 1.2 1.4 1.6

0.2

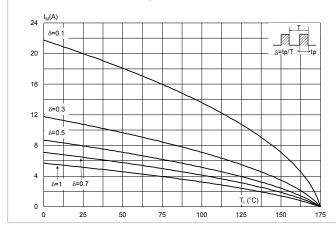


Figure 5. Junction capacitance versus reverse voltage applied (typical values)

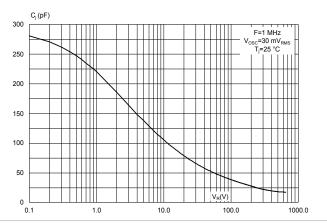


Figure 6. Relative variation of thermal impedance junction to lead versus pulse duration

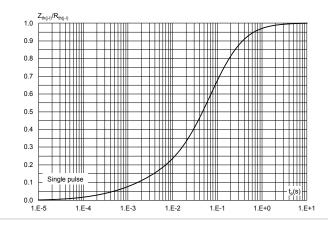
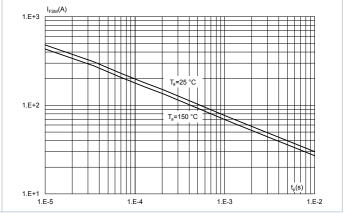


Figure 7. Non-repetitive peak surge forward current versus pulse duration (sinusoidal waveform)



DS14892 - Rev 1 page 4/9



Figure 8. Total capacitive charges versus reverse voltage applied (typical values)

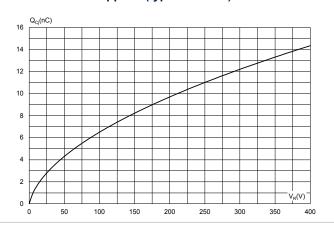
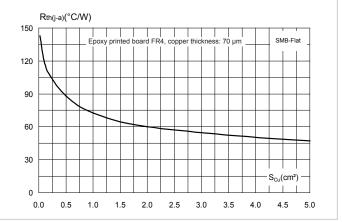


Figure 9. Thermal resistance junction to ambient versus copper surface under each lead (typical values)



DS14892 - Rev 1 page 5/9



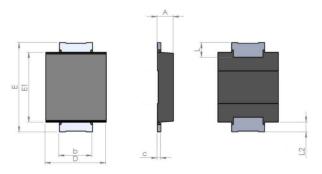
Package information

To meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions, and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 SMB Flat package information

- Epoxy meets UL94, V0
- · Lead-free package

Figure 10. SMB Flat package outline

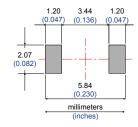


Note: Note: This package drawing may slightly differ from the physical package. However, all the specified dimensions are guaranteed.

Dimensions Ref. Millimeters Inches Min. Max. Min. Тур. Max. Тур. 0.90 1.10 0.035 0.043 Α 2.20 0.077 0.087 b 1.95 0.40 0.006 0.016 С 0.15 3.30 3.95 0.130 0.156 D 0.200 0.220 Ε 5.10 5.60 4.05 4.60 0.159 0.181 E1 0.75 1.50 0.030 0.060 L L2 0.60 0.024

Table 6. SMB Flat mechanical data

Figure 11. Footprint recommendations, dimensions in mm (inches)



DS14892 - Rev 1 page 6/9



3 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPSC4G065UF	4G65	SMB Flat	0.050 g	5000	Tape and reel

DS14892 - Rev 1 page 7/9



Revision history

Table 8. Document revision history

Date	Revision	Changes
11-Feb-2025	1	Initial release.

DS14892 - Rev 1 page 8/9



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DS14892 - Rev 1 page 9/9