

6 A Snubberless™ Triac

Datasheet - production data

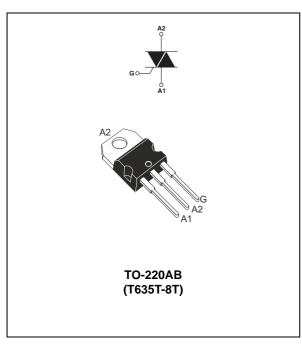


Table 1. Device summary

Symbol	Value	Unit
I _{T(rms)}	6	Α
V_{DRM}, V_{RRM}	800	V
V _{DSM} , V _{RSM}	900	V
I _{GT}	35	mA

Features

- Medium current Triac
- High static and dynamic commutation
- Three quadrants
- ECOPACK[®]2 compliant component

Applications

- · General purpose AC line load switching
- · Motor control circuits
- Small home appliances
- Lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

Description

Available in through-hole package, the T635T-8T Triac can be used for the on/off or phase angle control function in general purpose AC switching where high commutation capability is required. This device can be used without a snubber circuit when the limits defined in this datasheet are respected.

TM: Snubberless is a trademark of STMicroelectronics

Characteristics T635T-8T

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Table 2. Absolute maximum ratings ($T_j = 25$ °C unless otherwise stated)

Symbol	Paramete	Value	Unit		
I _{T(rms)}	On-state rms current (full sine wave)	T _c = 135 °C	6	Α
l	Non repetitive surge peak on-state	F = 50 Hz	t = 20 ms	45	Α
I _{TSM}	current (full cycle, T _j initial = 25 °C)	F = 60 Hz	t = 16.7 ms	47	^
l ² t	I ² t value for fusing, T _j initial = 25 °C		$t_p = 10 \text{ ms}$	13	A ² s
V _{DRM} ,	Popotitivo curao poak off stato volta		T _j = 150 °C	600	V
V _{RRM}	Repetitive surge peak off-state voltage		T _j = 125 °C	800	V
V _{DSM} , V _{RSM}	Non repetitive surge peak off-state voltage $t_p =$			900	V
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$	1 + 100 Hz		100	A/µs
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α
P _{G(AV)}	Average gate power dissipation	1	W		
T _{stg}	Storage junction temperature range			- 40 to + 150	ů
Tj	Operating junction temperature range	- 40 to + 150			
T _L	Maximum lead temperature for soldering during 10 s			260	°C
V _{ins}	Insulation rms voltage, 1 minute	1500	V		

Table 3. Electrical characteristics (T_j = 25 °C, unless otherwise stated)

Symbol	Test conditions Quadrant			Value	Unit
I _{GT} ⁽¹⁾	$I_{GT}^{(1)}$ $V_D = 12 \text{ V}, R_L = 30 \Omega$		Min.	1.75	mΛ
'GT`	VD = 12 V, IXL = 30 \$2	1 - 11 - 111	Max.	35	mA
V _{GT}	V_D = 12 V, R_L = 30 Ω	1 - 11 - 111	Max.	1.3	V
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, T_j = 150 \text{ °C}$	1 - 11 - 111	Min.	0.2	V
I _H ⁽¹⁾	I _T = 500 mA		Max.	40	mA
	I _G = 1.2 I _{GT}	I - III	Max.	60	mA
IL		II	Max.	65	mA
dV/dt ⁽¹⁾	V _D = 536 V, gate open	T _j = 125 °C	Min.	2000	V/µs
u v/ui · /	V _D = 402 V, gate open	T _j = 150 °C	IVIII I.	1000	V/µs
(dl/dt)c (1)	Without snubber (dV/dt)c ≥ 20 V/μs)	T _j = 125 °C	Min.	6	A/ms
	Williout Silubber (a Vidt)c 2 20 Vips)	T _j = 150 °C	IVIII I.	3	

^{1.} For both polarities of A2 referenced to A1

T635T-8T Characteristics

Table	4	Static	charac	cteristics
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Symbol	Test conditions			Value	Unit
V _T ⁽¹⁾	$I_{TM} = 8.5 \text{ A}, t_p = 380 \mu\text{s}$	T _j = 25 °C	Max.	1.55	V
V _{t0} (1)	Threshold voltage	T _j = 150 °C	Max.	0.85	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	Max.	75	mΩ
	V - V - 800 V	T _j = 25 °C	Max.	5	μΑ
I _{DRM}	$V_{DRM} = V_{RRM} = 800 \text{ V}$	T _j = 125 °C	iviax.	0.6	mA
'KKIVI	V _{DRM} = V _{RRM} = 600 V	T _j = 150 °C	Max.	2.0	IIIA

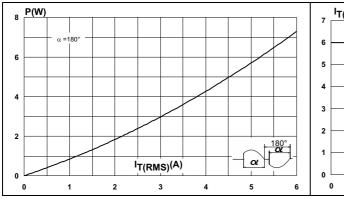
^{1.} For both polarities of A2 referenced to A1

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.1	°C/W
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus on-state rms current (full cycle)

Figure 2. On-state rms current versus case temperature (full cycle)



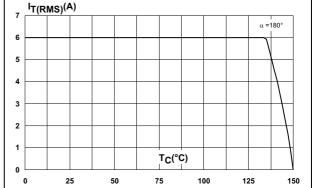
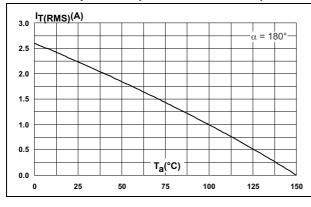
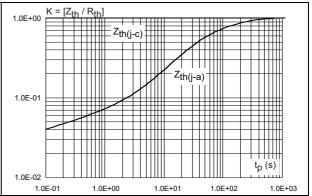


Figure 3. On-state rms current versus ambient temperature (free air convection)

Figure 4. Relative variation of thermal impedance versus pulse duration





Characteristics T635T-8T

Figure 5. On-state characteristics (maximum values)

Figure 6. Surge peak on-state current versus number of cycles

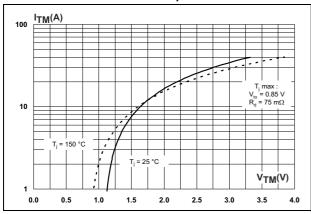
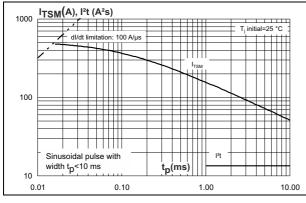


Figure 7. Non repetitive surge peak on-state current and corresponding values of I²t

Figure 8. Relative variation of gate trigger current and gate voltage versus junction temperature (typical values)



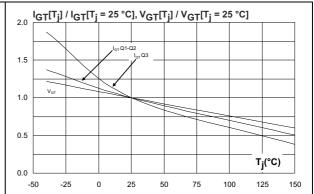
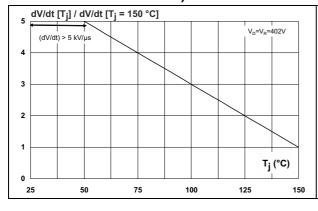
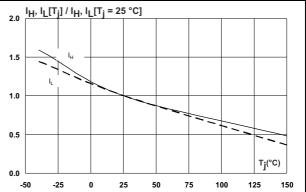


Figure 9. Relative variation of static dV/dt immunity versus junction temperature (typical values)

Figure 10. Relative variation of holding current and latching current versus junction temperature (typical values)





T635T-8T Characteristics

Figure 11. Relative variation of critical rate of decrease of main current (dl/dt)c versus reapplied (dV/dt)c (typical values)

Figure 12. Relative variation of critical rate of decrease of main current (dl/dt)c versus junction temperature (typical values)

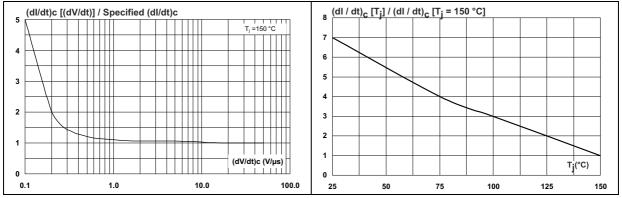
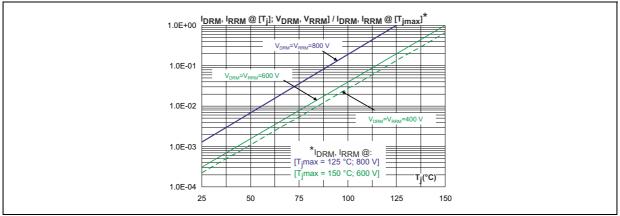


Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage (typical values)



Package information T635T-8T

2 Package information

- Epoxy meets UL94, V0
- Lead-free package
- Recommended torque: 0.4 to 0.6 N⋅m

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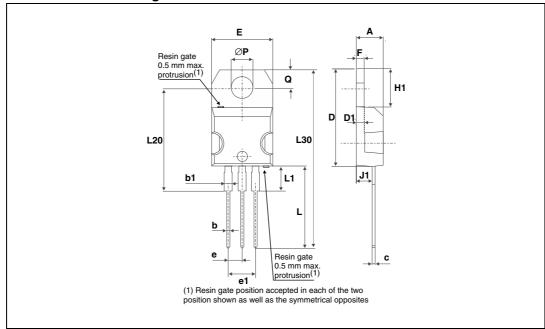


Figure 14. TO-220AB dimension definitions

T635T-8T Package information

Table 6. TO-220AB dimension values

	Dimensions				
Ref.	Millim	neters	Inc	hes	
	Min. Max.		Min.	Max.	
A	4.40	4.60	0.17	0.18	
b	0.61	0.88	0.024	0.035	
b1	1.14	1.70	0.045	0.067	
С	0.48	0.70	0.019	0.027	
D	15.25	15.75	0.60	0.62	
D1	1.27	typ.	0.05	typ.	
E	10	10.40	0.39	0.41	
е	2.40	2.70	0.094	0.106	
e1	4.95	5.15	0.19	0.20	
F	1.23	1.32	0.048	0.052	
H1	6.20	6.60	0.24	0.26	
J1	2.40	2.72	0.094	0.107	
L	13	14	0.51	0.55	
L1	3.50	3.93	0.137	0.154	
L20	16.40 typ.		0.64	typ.	
L30	28.90 typ.		1.13	typ.	
ØP	3.75	3.85	0.147	0.151	
Q	2.65	2.95	0.104	0.116	

Ordering information T635T-8T

3 Ordering information

Figure 15. Ordering information scheme

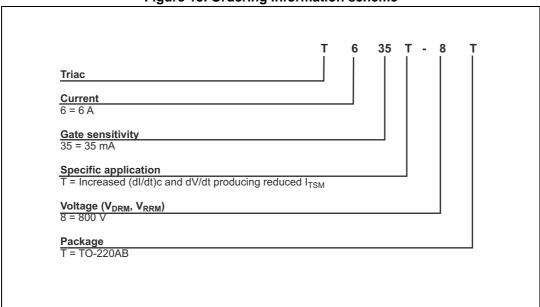


Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T635T-8T	T635T-8T	TO-220AB	2.0 g	50	Tube

4 Revision history

Table 8. Document revision history

Date	Revision	Changes
05-Aug-2013	1	Initial release.

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