

Product Overview

MC74HC02A: Quad 2-Input NOR Gate

For complete documentation, see the data sheet.

High-Performance Silicon-Gate CMOS

The MC74HC02A is identical in pinout to the LS02. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs.

Features

- · Output Drive Capability: 10 LSTTL Loads
- · Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0 mA
- · High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 40 FETs or 10 Equivalent Gates
- · Pb-Free Packages are Available

Part Electrical Specifications									
Product	Compliance	Status	Туре	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HC02ADG	Pb-free Halide free	Active	NOR	4	2	6	15	4	SOIC-14
MC74HC02ADR2G	Pb-free Halide free	Active	NOR	4	2	6	15	4	SOIC-14
MC74HC02ADTR2G	Pb-free Halide free	Active	NOR	4	2	6	15	4	TSSOP-14
NLV74HC02ADR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	NOR	4	2	6	15	4	SOIC-14
NLV74HC02ADTR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	NOR	4	2	6	15	4	TSSOP-14

For more information please contact your local sales support at www.onsemi.com.

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