

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Q1 N-CHANNEL	Q2 P-CHANNEL	Unit
Drain-Source Voltage			V _{DSS}	20	-20	V
Gate-Source Voltage			V _{GSS}	±10	±8	V
Continuous Drain Current (Note 6) N-Channel: V _{GS} = 4.5V P-Channel: V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	6.0 4.8	-3.5 -2.8	A
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	2	-1.0	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	20	-10	A
Avalanche Current (L = 0.1mH) (Note 7)			I _{AS}	8	-13	A
Avalanche Energy (L = 0.1mH) (Note 7)			E _{AS}	8	8.5	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	0.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	178	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	1.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	92	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	30	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics Q1 N-CHANNEL (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±10V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	25	mΩ	V _{GS} = 4.5V, I _D = 4A
			—	35		V _{GS} = 2.5V, I _D = 4A
Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 5A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	486	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	92	—		
Reverse Transfer Capacitance	C _{rss}	—	77	—		
Gate Resistance	R _g	—	3.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	5.9	—	nC	V _{DS} = 10V, I _D = 6.5A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	12.3	—		
Gate-Source Charge	Q _{gs}	—	0.8	—		
Gate-Drain Charge	Q _{gd}	—	2.2	—		
Turn-On Delay Time	t _{D(ON)}	—	3.4	—	ns	V _{DS} = 10V, V _{GS} = 4.5V, R _g = 6Ω, R _L = 10Ω, I _D = 1A
Turn-On Rise Time	t _r	—	5.4	—		
Turn-Off Delay Time	t _{D(OFF)}	—	17.6	—		
Turn-Off Fall Time	t _f	—	9.3	—		
Reverse Recovery Time	t _{RR}	—	7.7	—	ns	I _F = 1A, di/dt = 100A/μs
Reverse Recovery Charge	Q _{RR}	—	1.5	—	nC	I _F = 1A, di/dt = 100A/μs

Electrical Characteristics Q2 P-CHANNEL (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	-1.0	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-0.35	—	-1.4	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	75	mΩ	V _{GS} = -4.5V, I _D = -2.9A
		—	—	140		V _{GS} = -2.5V, I _D = -2.3A
Diode Forward Voltage	V _{SD}	—	—	-1.2	V	V _{GS} = 0V, I _S = -3.0A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	642	—	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	98	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	87	—	pF	
Gate Resistance	R _g	—	26.5	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _g	—	8.8	—	nC	V _{DS} = -10V, I _D = -3.7A
Total Gate Charge (V _{GS} = -8V)		—	15	—	nC	
Gate-Source Charge	Q _{gs}	—	0.9	—	nC	
Gate-Drain Charge	Q _{gd}	—	2.9	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	5.5	—	ns	V _{DD} = -10V, V _{GS} = -4.5V, R _L = 3.3Ω, R _g = 1Ω
Turn-On Rise Time	t _R	—	22.6	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	34.1	—	ns	
Turn-Off Fall Time	t _F	—	34.3	—	ns	
Body Diode Reverse Recovery Time	t _{RR}	—	13	—	ns	I _S = -3.0A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	—	3.3	—	nC	I _S = -3.0A, dI/dt = 100A/μs

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

Typical Characteristics—N-CHANNEL

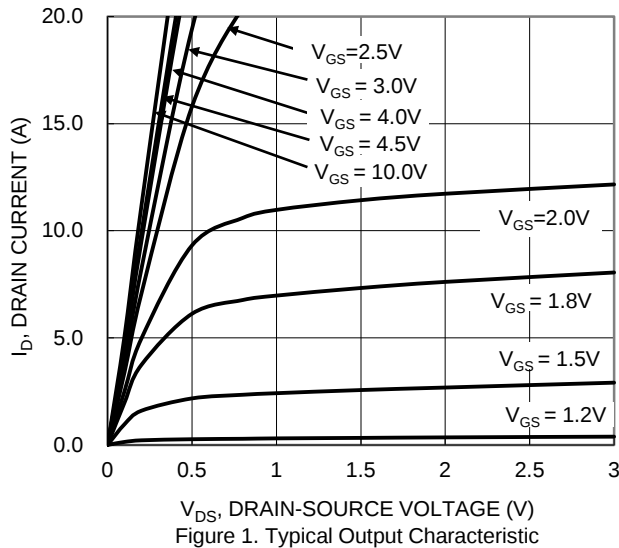


Figure 1. Typical Output Characteristic

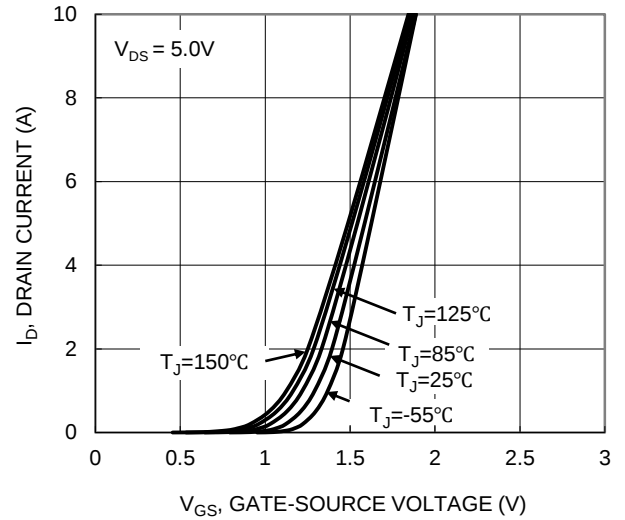


Figure 2. Typical Transfer Characteristic

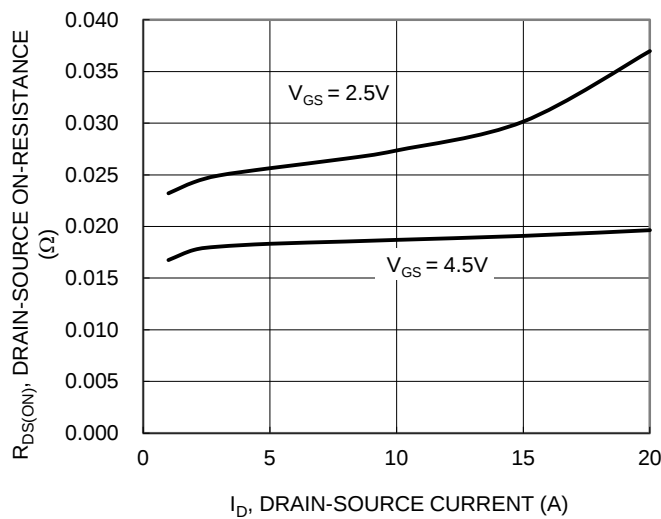


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

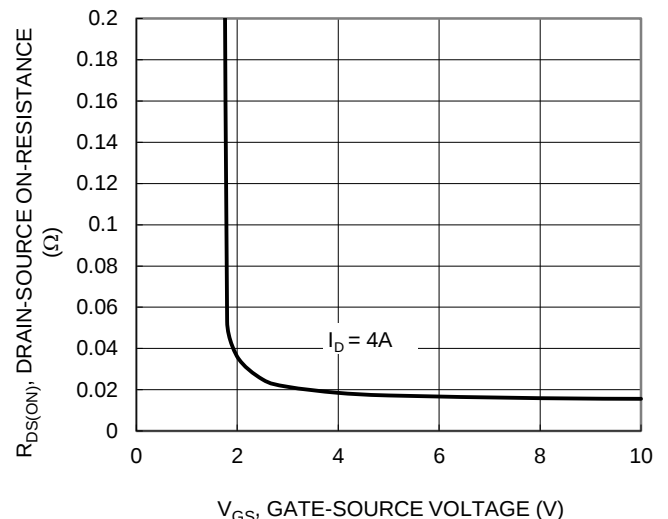


Figure 4. Typical Transfer Characteristic

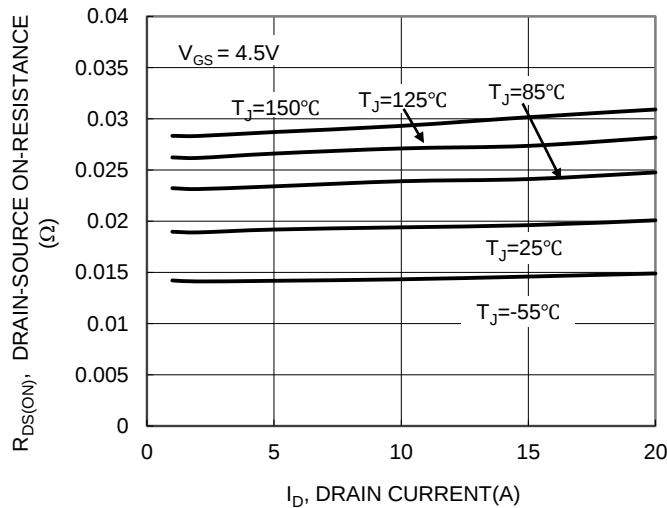


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

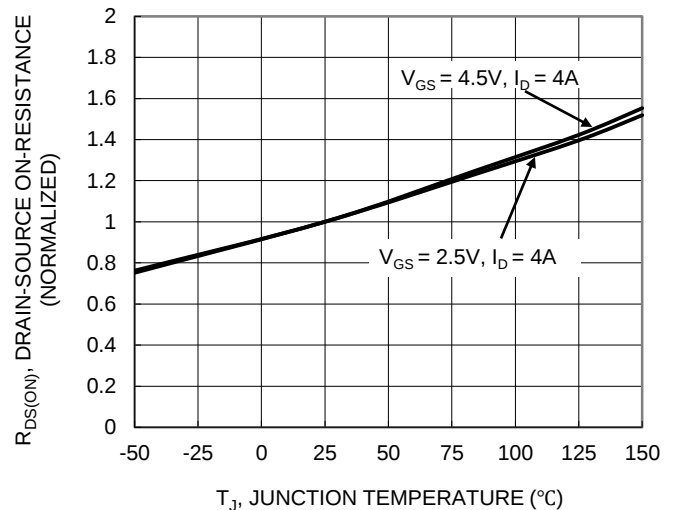


Figure 6. On-Resistance Variation with Temperature

Typical Characteristics—N-CHANNEL (continued)

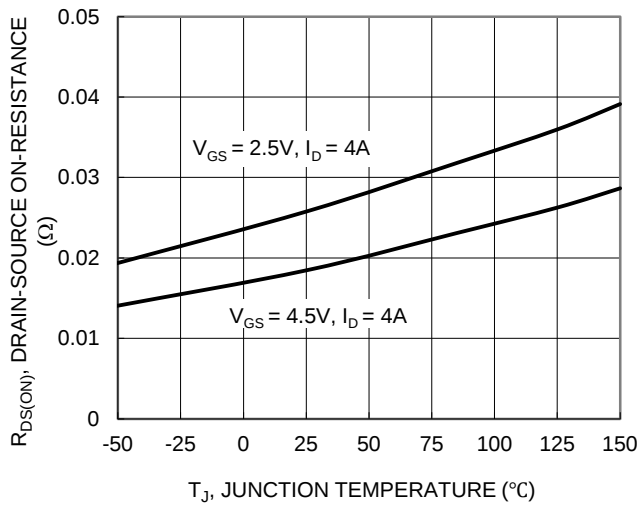


Figure 7. On-Resistance Variation with Temperature

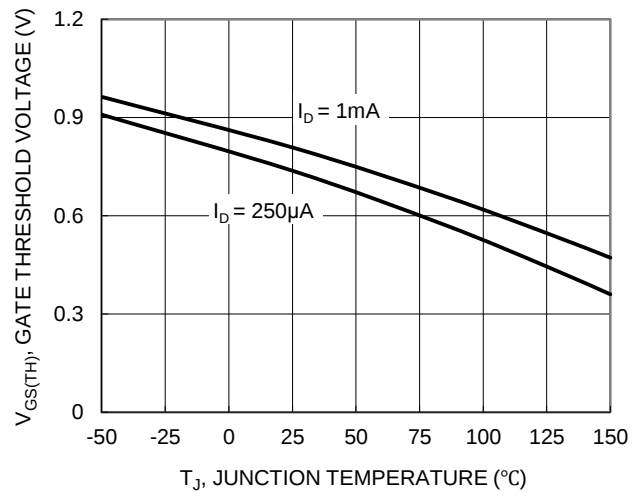


Figure 8. Gate Threshold Variation vs. Junction Temperature

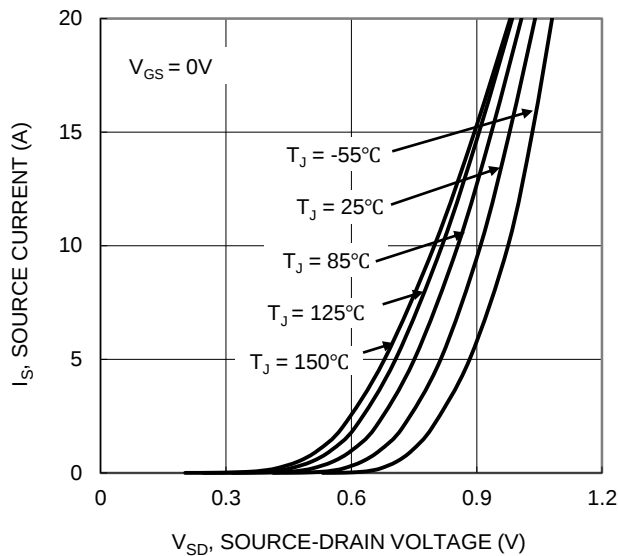


Figure 9. Diode Forward Voltage vs. Current

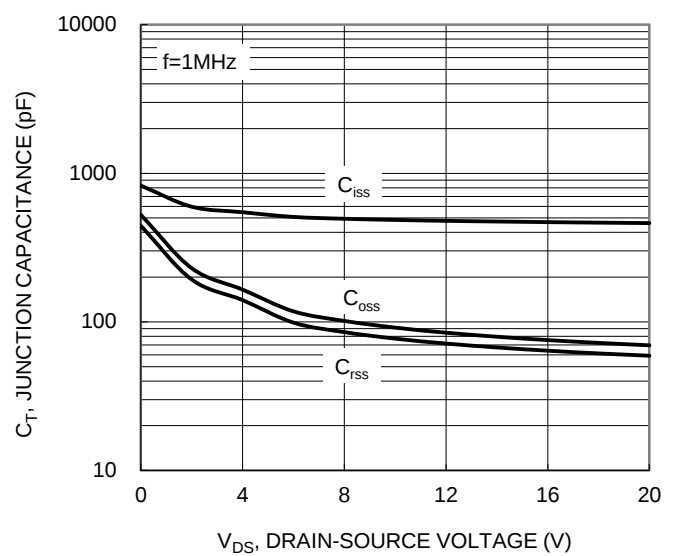


Figure 10. Typical Junction Capacitance

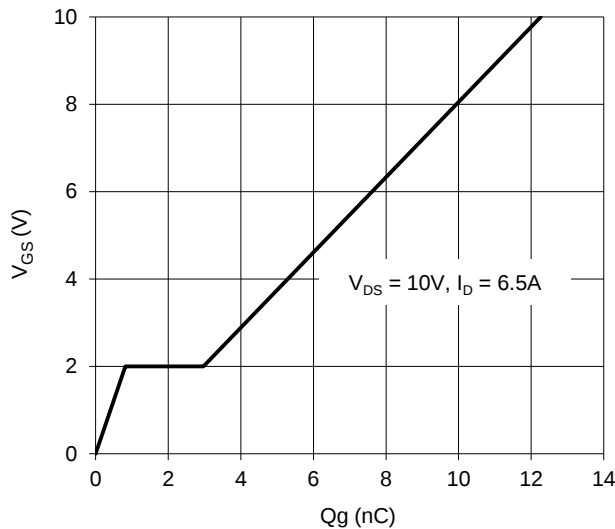


Figure 11. Gate Charge

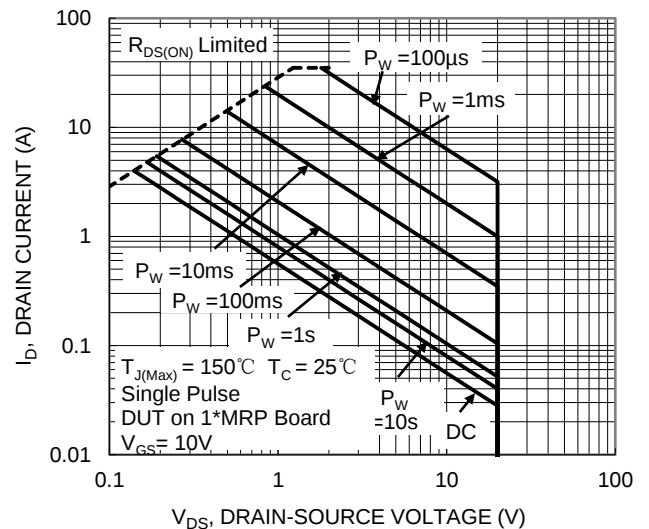


Figure 12. SOA, Safe Operation Area

Typical Characteristics—P-CHANNEL

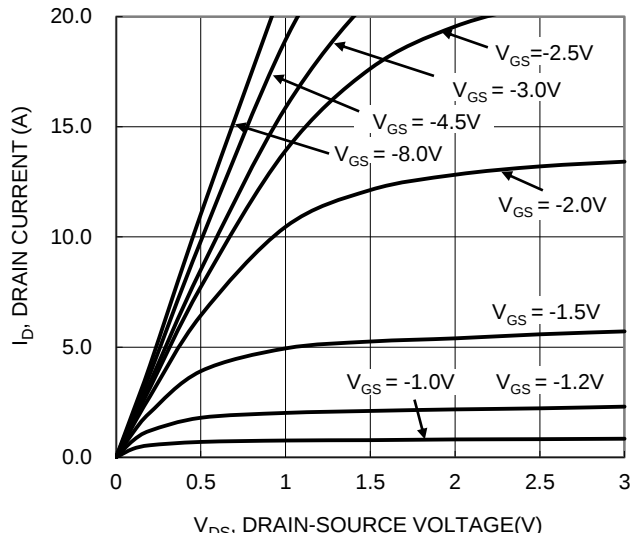


Figure 13. Typical Output Characteristic

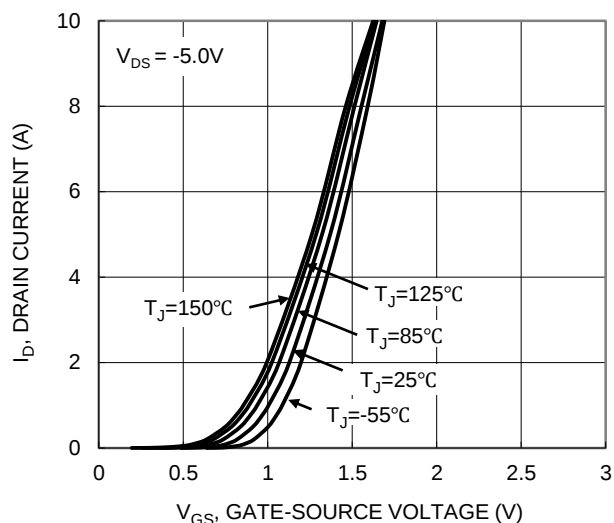


Figure 14. Typical Transfer Characteristic

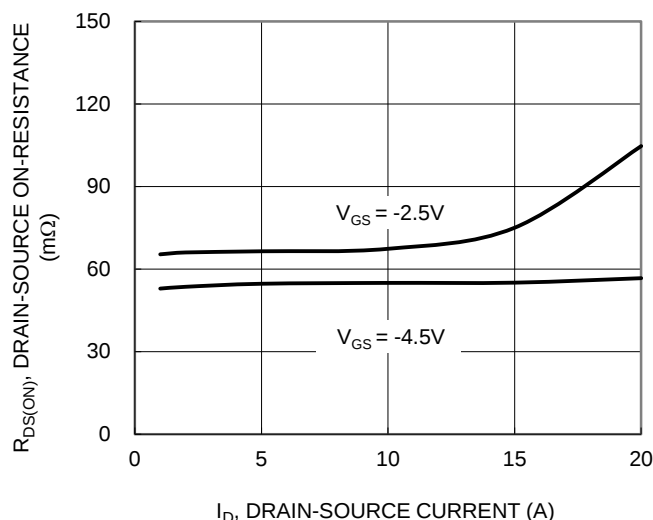


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

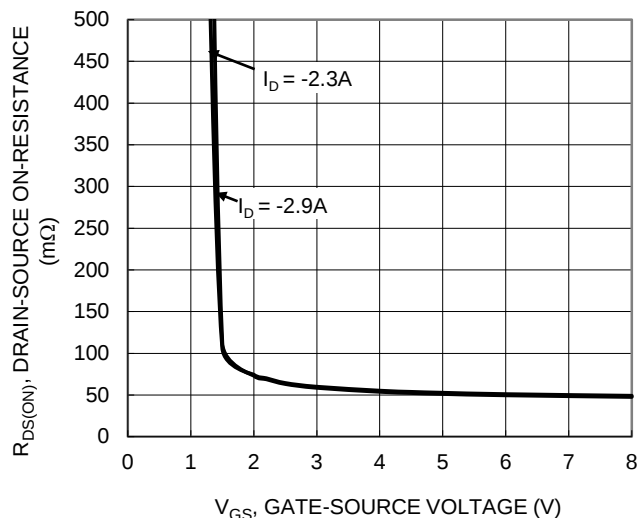


Figure 16. Typical Transfer Characteristic

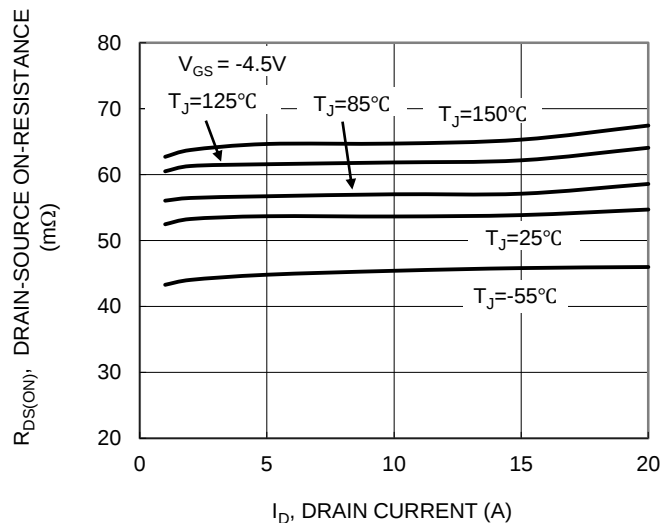


Figure 17. Typical On-Resistance vs. Drain Current and Temperature

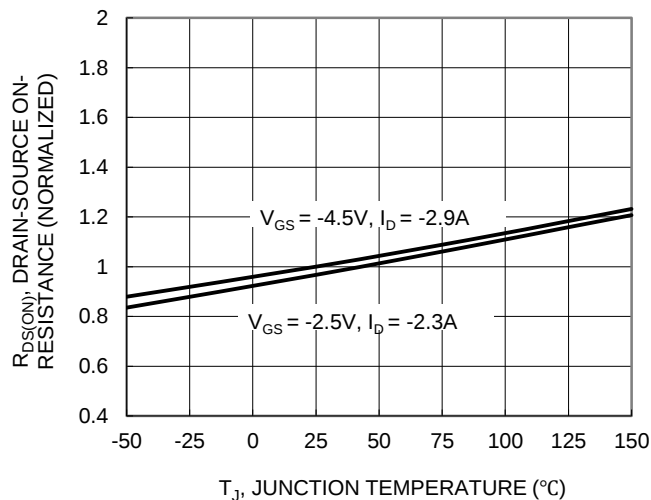


Figure 18. On-Resistance Variation with Temperature

Typical Characteristics—P-CHANNEL (continued)

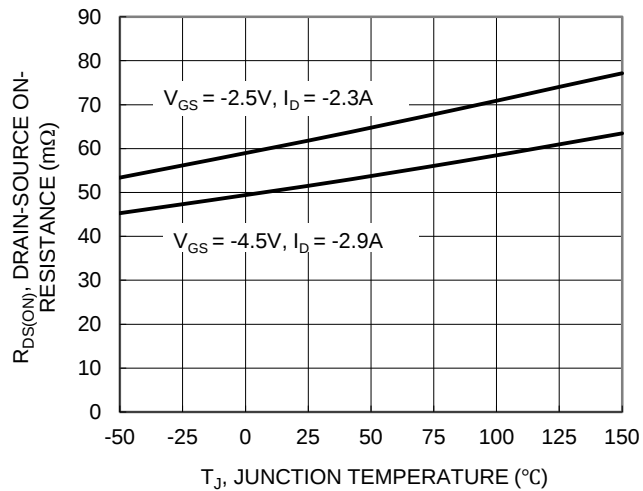


Figure 19. On-Resistance Variation with Temperature

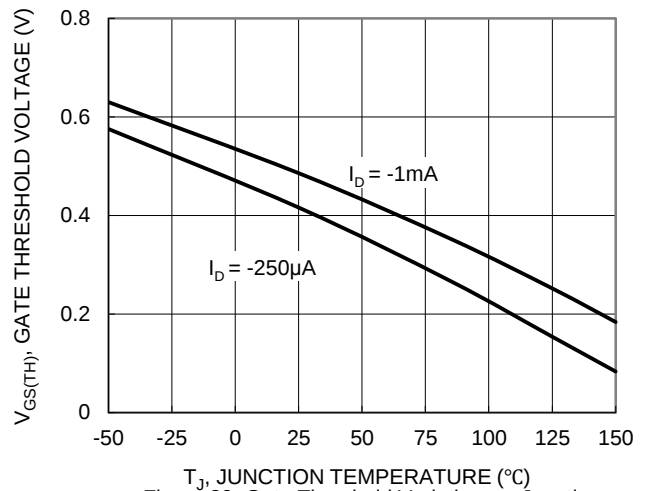


Figure 20. Gate Threshold Variation vs. Junction Temperature

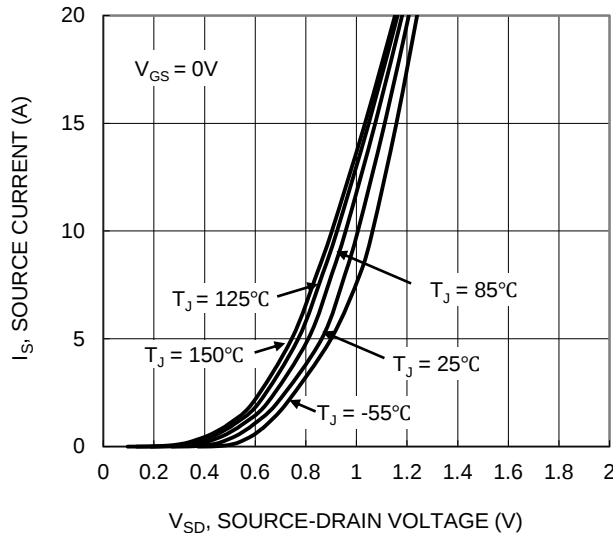


Figure 21. Diode Forward Voltage vs. Current

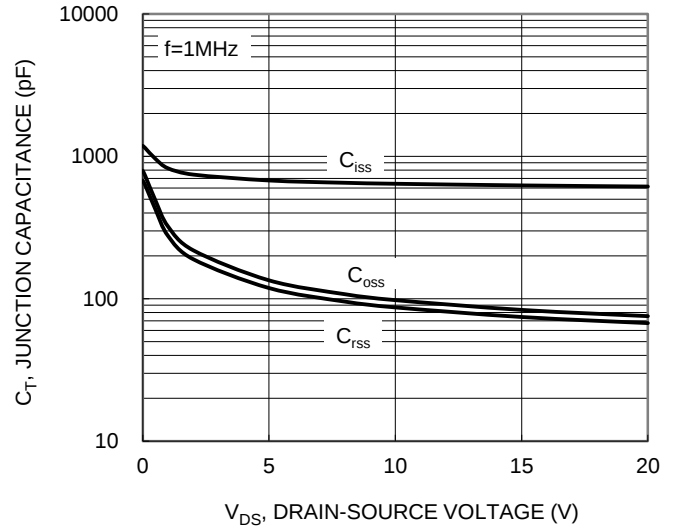


Figure 22. Typical Junction Capacitance

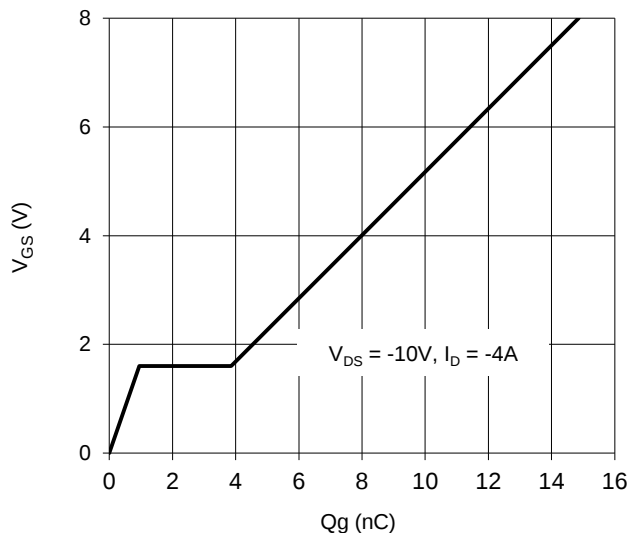


Figure 23. Gate Charge

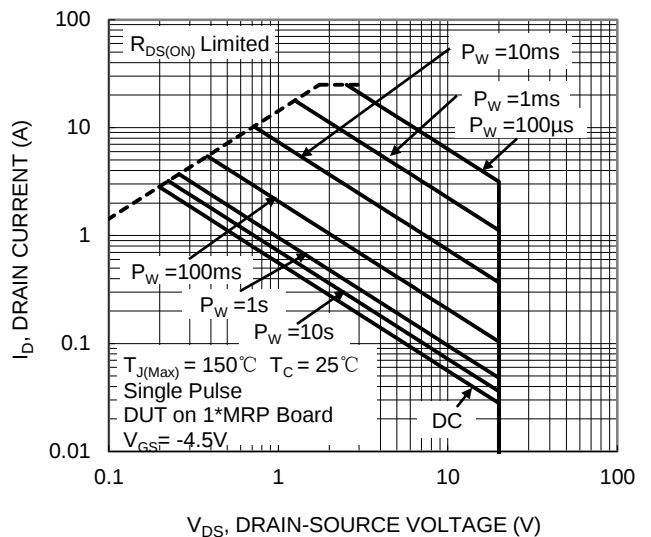


Figure 24. SOA, Safe Operation Area

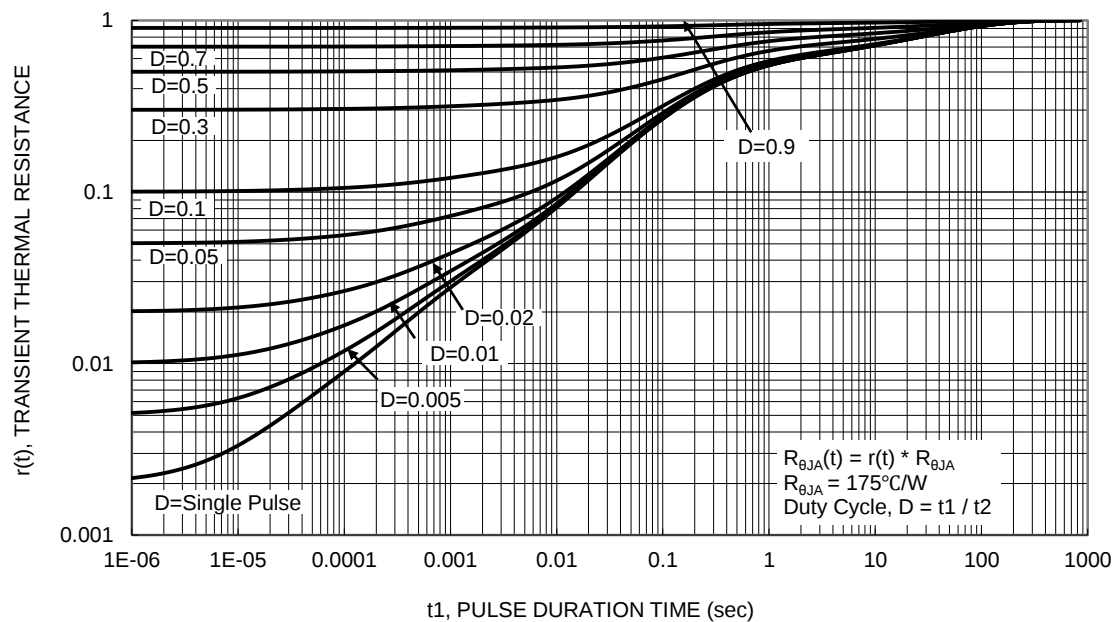
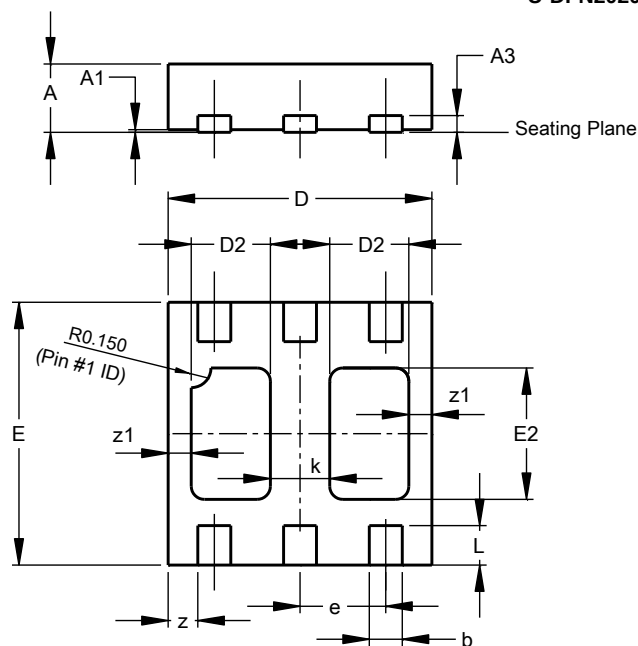


Figure 25. Transient Thermal Resistance

Package Outline Dimensions

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U-DFN2020-6 (Type B)

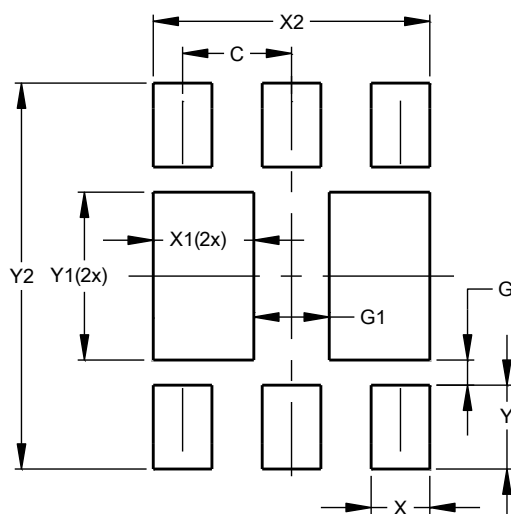


U-DFN2020-6 Type B			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	—	—	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
e	—	—	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	—	—	0.45
L	0.25	0.35	0.30
z	—	—	0.225
z1	—	—	0.175
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
C	0.650
G	0.150
G1	0.450
X	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300

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