

N-channel 600 V - 0.25 Ω typ., 13 A FDmesh™ II Power MOSFET (with fast diode) in D²PAK, TO-220FP, TO-220 and TO-247 packages

Datasheet – production data

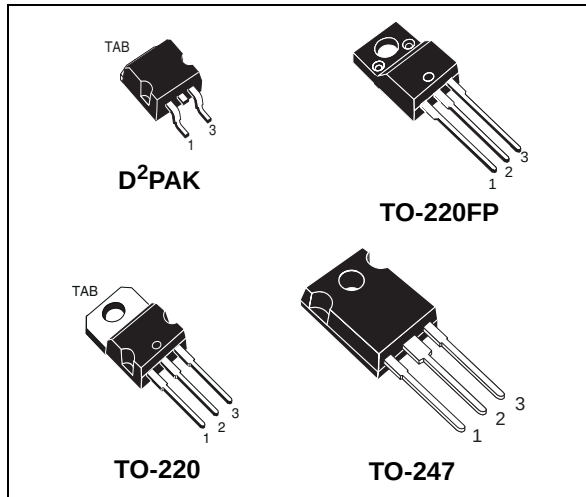
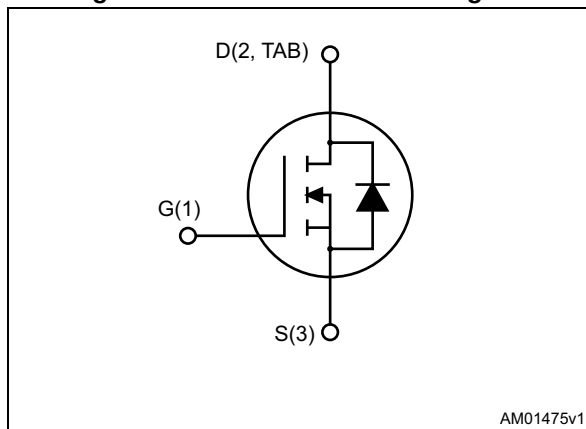


Figure 1. Internal schematic diagram



Features

Order codes	$V_{DS} @ T_{Jmax}$	$R_{DS(on) max}$	I_D
STB18NM60ND	650 V	<0.29 Ω	13 A
STF18NM60ND			
STP18NM60ND			
STW18NM60ND			

- The worldwide best $R_{DS(on)}$ * area amongst the fast recovery diode devices
- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance
- Extremely high dv/dt and avalanche capabilities

Applications

- Switching applications

Description

These FDmesh™ II Power MOSFETs with intrinsic fast-recovery body diode are produced using the second generation of MDmesh™ technology. Utilizing a new strip-layout vertical structure, these revolutionary devices feature extremely low on-resistance and superior switching performance. They are ideal for bridge topologies and ZVS phase-shift converters.

Table 1. Device summary

Order codes	Marking	Package	Packaging
STB18NM60ND	18NM60ND	D ² PAK	Tape and reel
STF18NM60ND		TO-220FP	Tube
STP18NM60ND		TO-220	
STW18NM60ND		TO-247	

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value		Unit
		D ² PAK, TO-220, TO-247	TO-220FP	
V _{DS}	Drain-source voltage	600		V
V _{GS}	Gate-source voltage	± 25		V
I _D	Drain current (continuous) at T _C = 25 °C	13	13 ⁽¹⁾	A
I _D	Drain current (continuous) at T _C = 100 °C	8.19	8.19 ⁽¹⁾	A
I _{DM} ⁽²⁾	Drain current (pulsed)	52	52 ⁽¹⁾	A
P _{TOT}	Total dissipation at T _C = 25 °C	130	30	W
dv/dt ⁽³⁾	Peak diode recovery voltage slope	40		V/ns
dv/dt ⁽⁴⁾	MOSFET dv/dt ruggedness	40		V/ns
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t=1 s; T _C =25 °C)	--	2500	V
T _{stg}	Storage temperature	-55 to 150		°C
T _j	Operating junction temperature	150		°C

1. Limited by maximum junction temperature
2. Pulse width limited by safe operating area
3. I_{SD} ≤ 13 A, di/dt ≤ 400 A/μs, V_{DD} = 80% V_{(BR)DSS}, V_{DS(peak)} ≤ V_{(BR)DSS}
4. V_{DS} ≤ 480 V

Table 3. Thermal data

Symbol	Parameter	D ² PAK	TO-220FP	TO-220	TO-247	Unit
R _{thj-case}	Thermal resistance junction-case max	0.96	4.17	0.96	0.96	°C/W
R _{thj-amb}	Thermal resistance junction-amb max		62.5		50	°C/W
R _{thj-pcb}	Thermal resistance junction-pcb max	30				°C/W

Table 4. Avalanche characteristics

Symbol	Parameter	Max value	Unit
I _{AS}	Avalanche current, repetitive or not-repetitive (pulse width limited by T _{j max})	3.5	A
E _{AS}	Single pulse avalanche energy (starting T _j = 25 °C, I _D = I _{AS} , V _{DD} = 50 V)	187	mJ

2 Electrical characteristics

($T_{CASE}=25^{\circ}\text{C}$ unless otherwise specified)

Table 5. On/off states

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1\text{ mA}$, $V_{GS} = 0$	600			V
I_{DSS}	Zero gate voltage drain current ($V_{GS} = 0$)	$V_{DS} = 600\text{ V}$, $V_{DS} = 600\text{ V}$, $T_C=125^{\circ}\text{C}$			1 100	μA μA
I_{GSS}	Gate body leakage current ($V_{DS} = 0$)	$V_{GS} = \pm 20\text{ V}$			± 100	nA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	3	4	5	V
$R_{DS(on)}$	Static drain-source on resistance	$V_{GS} = 10\text{ V}$, $I_D = 6.5\text{ A}$		0.25	0.29	Ω

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input capacitance	$V_{DS} = 50\text{ V}$, $f = 1\text{ MHz}$, $V_{GS} = 0$	-	1030	-	pF
C_{oss}	Output capacitance		-	30	-	pF
C_{rss}	Reverse transfer capacitance		-	3.2	-	pF
$C_{oss\text{ eq.}}^{(1)}$	Equivalent output capacitance	$V_{GS} = 0$, $V_{DS} = 0\text{ V to } 480\text{ V}$	-	148	-	pF
R_g	Gate input resistance	$f=1\text{ MHz}$ Gate DC Bias=0 Test signal level=20 mV open drain	-	3.6	-	Ω
Q_g	Total gate charge	$V_{DD} = 480\text{ V}$, $I_D = 13\text{ A}$ $V_{GS} = 10\text{ V}$ (see Figure 18)	-	34	-	nC
Q_{gs}	Gate-source charge		-	5.5	-	nC
Q_{gd}	Gate-drain charge		-	20	-	nC

1. $C_{oss\text{ eq.}}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Table 7. Switching times

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 300\text{ V}$, $I_D = 6.5\text{ A}$, $R_G = 4.7\ \Omega$, $V_{GS} = 10\text{ V}$ (see Figure 17)	-	55	-	ns
t_r	Rise time		-	15.5	-	ns
$t_{d(off)}$	Turn-off delay time		-	13	-	ns
t_f	Fall time		-	18	-	ns

Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
I_{SD}	Source-drain current		-		13	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)		-		52	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 13\text{ A}$, $V_{GS}=0$	-		1.6	V
t_{rr}	Reverse recovery time	$I_{SD}=13\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$, $V_{DD} = 100\text{ V}$ (see Figure 19)	-	136		ns
Q_{rr}	Reverse recovery charge		-	843		nC
I_{RRM}	Reverse recovery current		-	12.5		A
t_{rr}	Reverse recovery time	$V_{DD} = 100\text{ V}$ $di/dt=100\text{ A}/\mu\text{s}$, $I_{SD} = 13\text{ A}$ $T_j = 150\text{ }^\circ\text{C}$ (see Figure 19)	-	198		ns
Q_{rr}	Reverse recovery charge		-	1425		nC
I_{RRM}	Reverse recovery current		-	14.5		A

1. Pulse width limited by safe operating area

2. Pulsed: pulse duration = 300 μs , duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area for D²PAK and TO-220

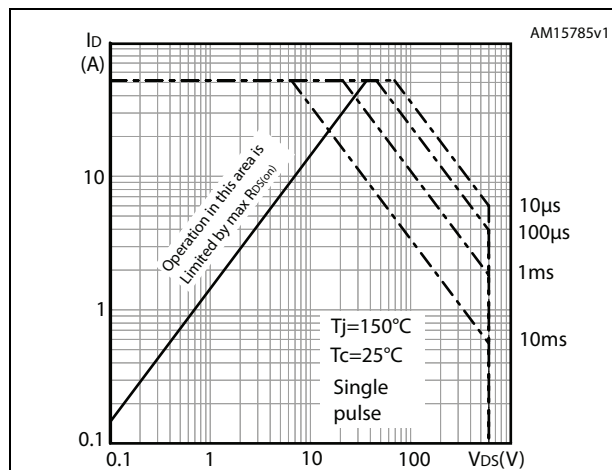


Figure 3. Thermal impedance for D²PAK and TO-220

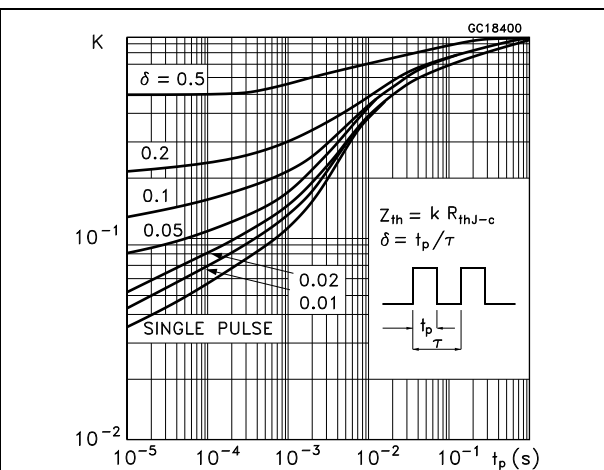


Figure 4. Safe operating area for TO-220FP

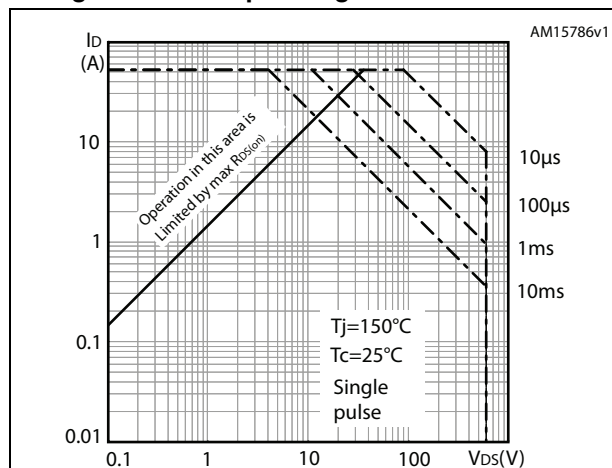


Figure 5. Thermal impedance for TO-220FP

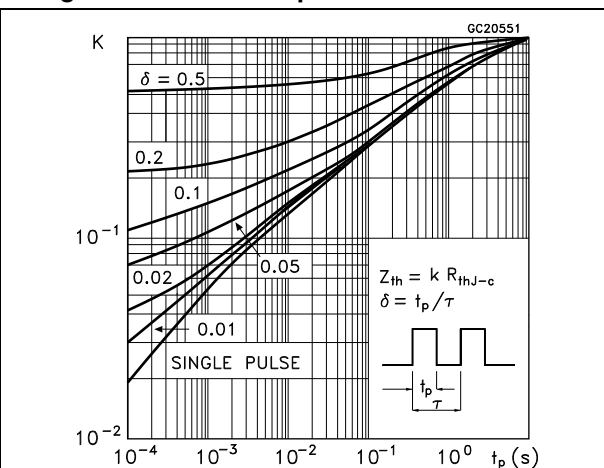


Figure 6. Safe operating area for TO-247

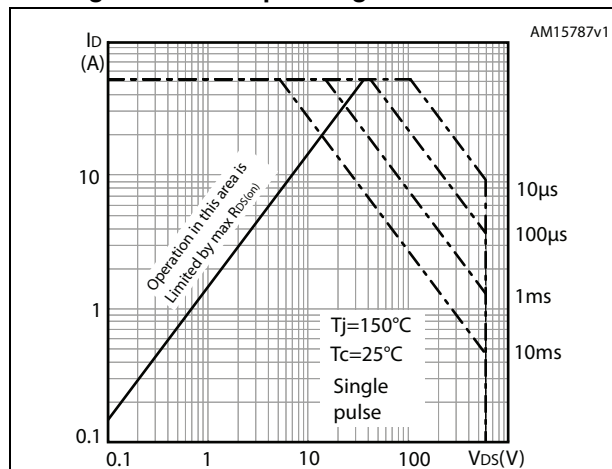


Figure 7. Thermal impedance for TO-247

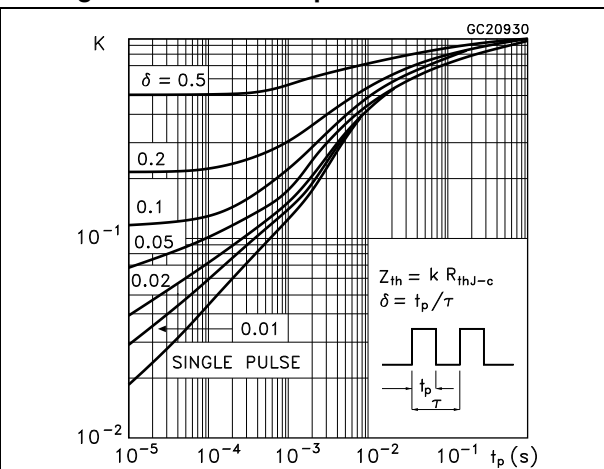


Figure 8. Output characteristics

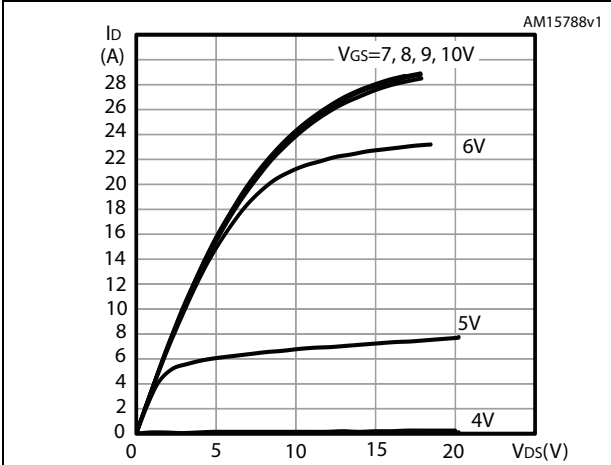


Figure 9. Transfer characteristics

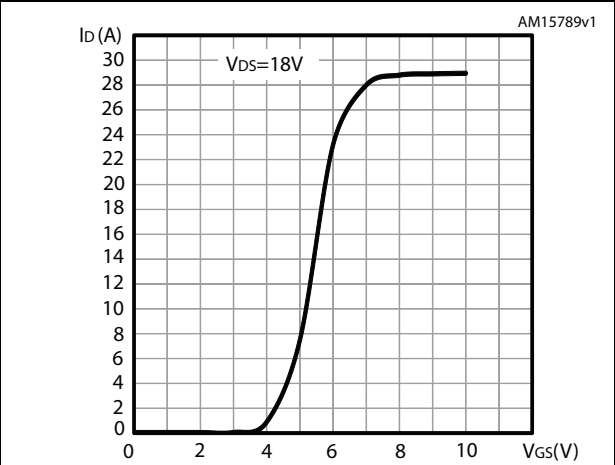


Figure 10. Gate charge vs gate-source voltage

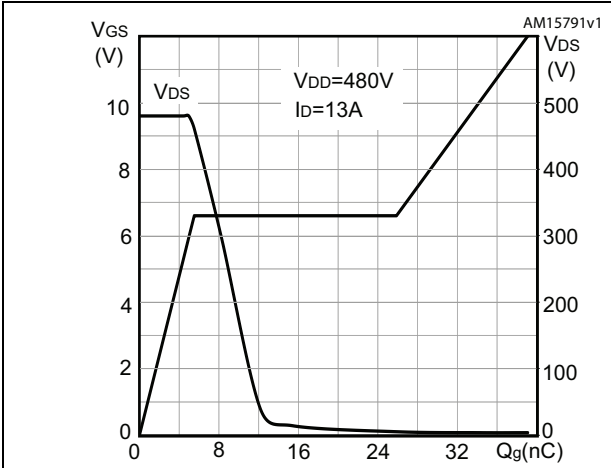


Figure 11. Static drain-source on-resistance

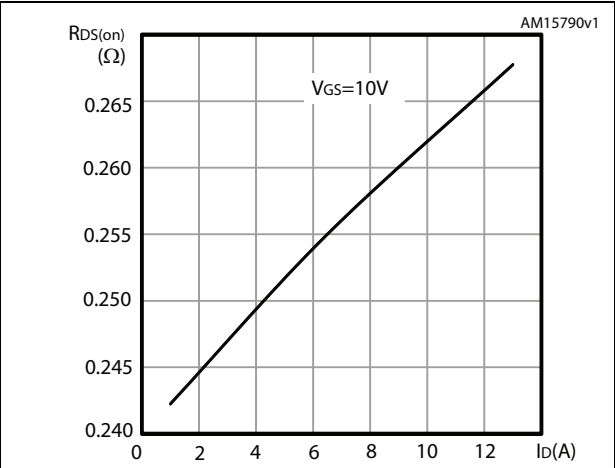


Figure 12. Capacitance variations

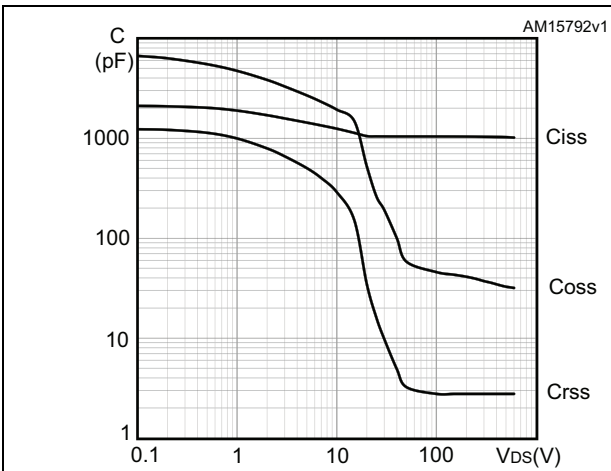


Figure 13. Normalized gate threshold voltage vs. temperature

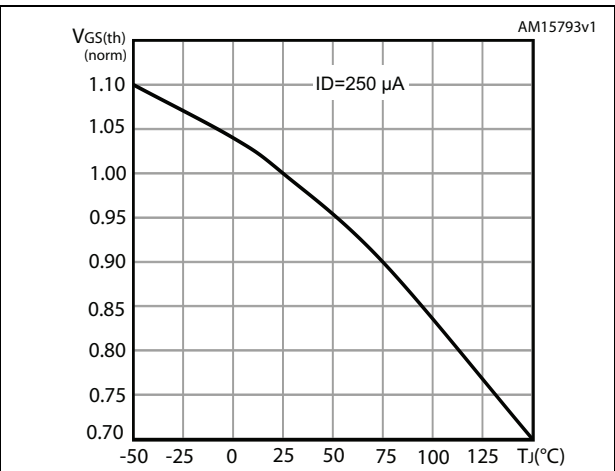


Figure 14. Normalized on-resistance vs temperature

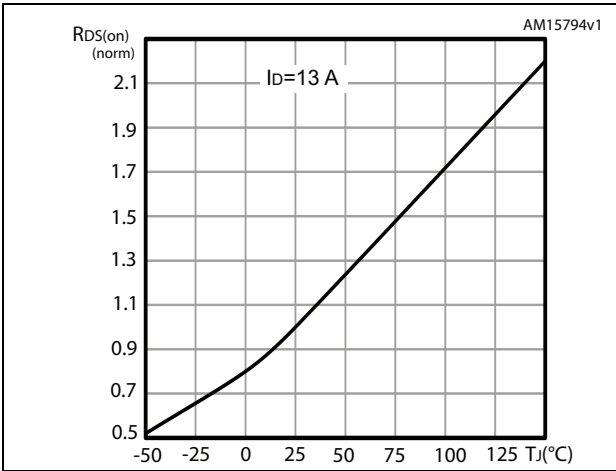


Figure 15. Source-drain diode forward characteristics

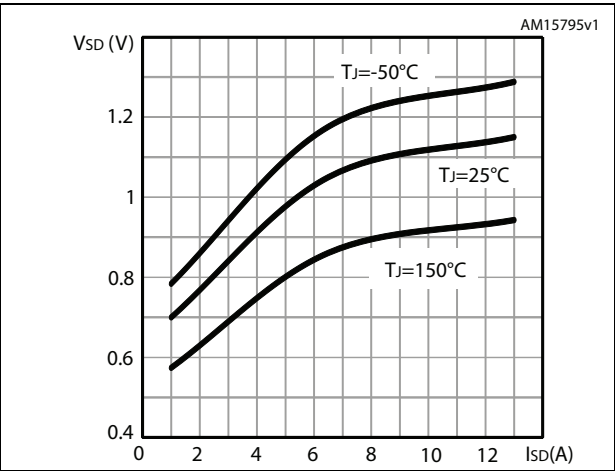
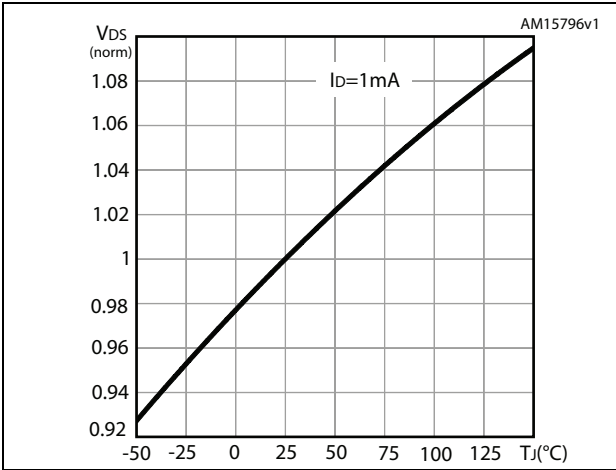


Figure 16. Normalized V_{DS} vs temperature



3 Test circuit

Figure 17. Switching times test circuit for resistive load

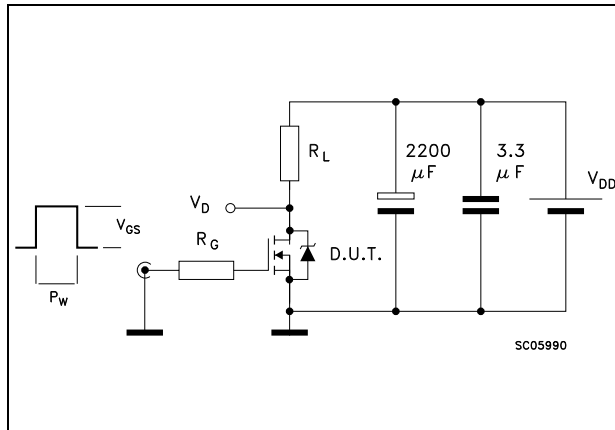


Figure 18. Gate charge test circuit

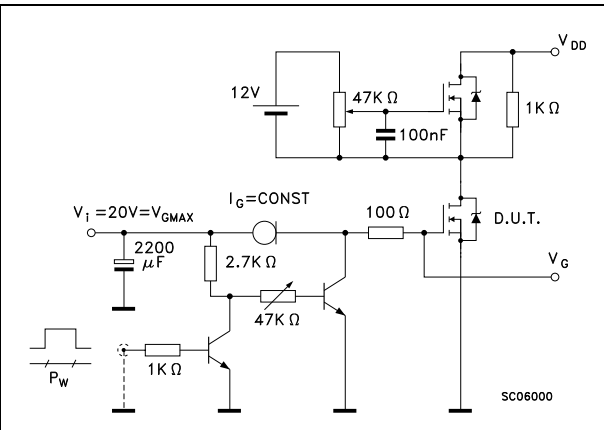


Figure 19. Test circuit for inductive load switching and diode recovery times

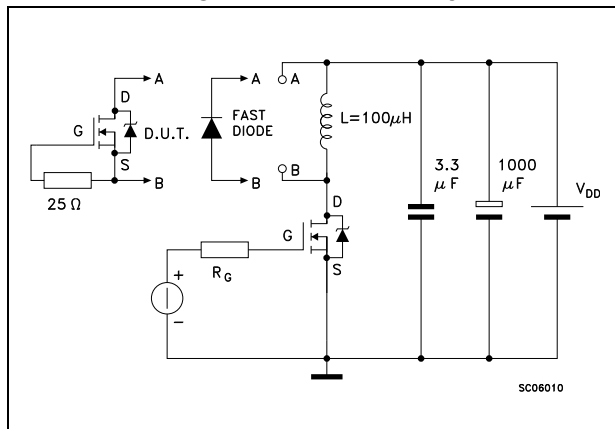


Figure 20. Unclamped Inductive load test circuit

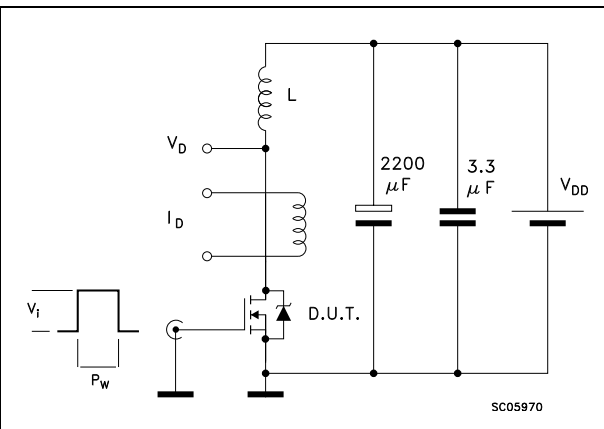


Figure 21. Unclamped inductive waveform

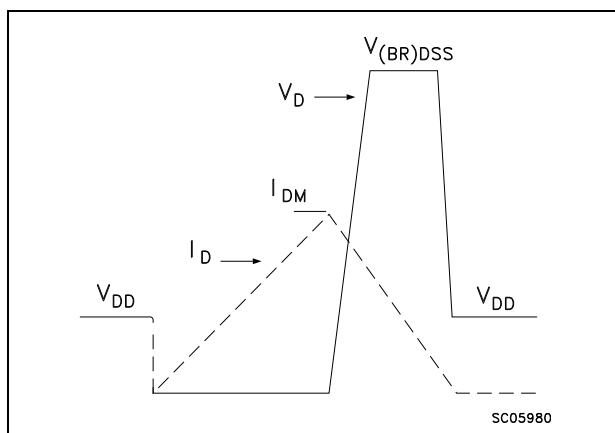
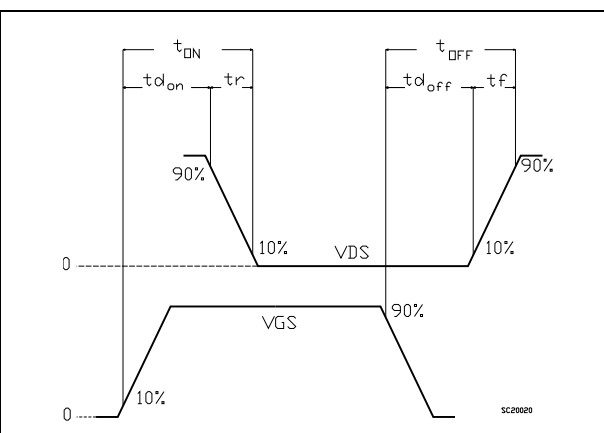


Figure 22. Switching time waveform

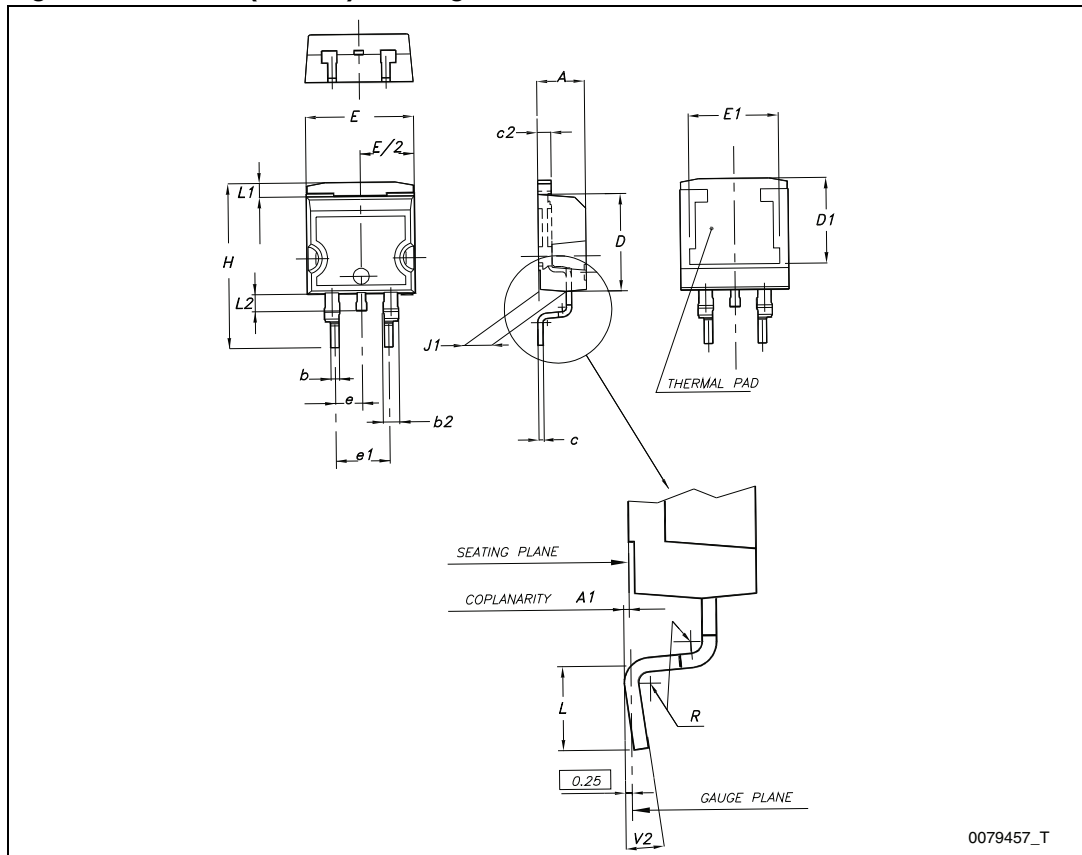
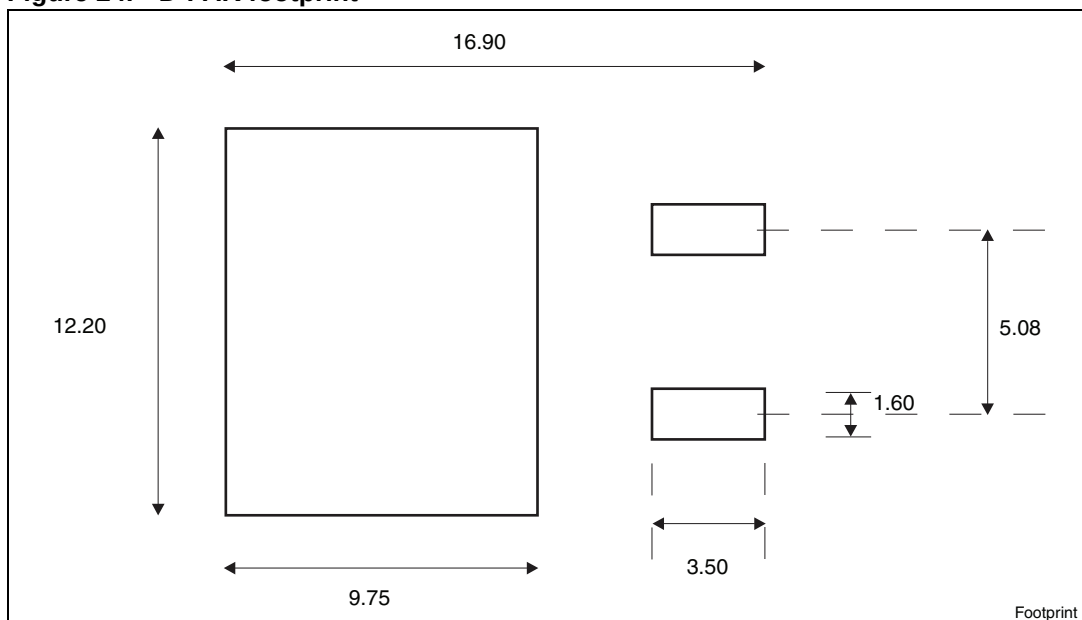


4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 9. D²PAK (TO-263) mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.40		4.60
A1	0.03		0.23
b	0.70		0.93
b2	1.14		1.70
c	0.45		0.60
c2	1.23		1.36
D	8.95		9.35
D1	7.50		
E	10		10.40
E1	8.50		
e		2.54	
e1	4.88		5.28
H	15		15.85
J1	2.49		2.69
L	2.29		2.79
L1	1.27		1.40
L2	1.30		1.75
R		0.4	
V2	0°		8°

Figure 23. D²PAK (TO-263) drawingFigure 24. D²PAK footprint^(a)

a. All dimension are in millimeters

Table 10. TO-220FP mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.4		4.6
B	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
H	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

Figure 25. TO-220FP drawing

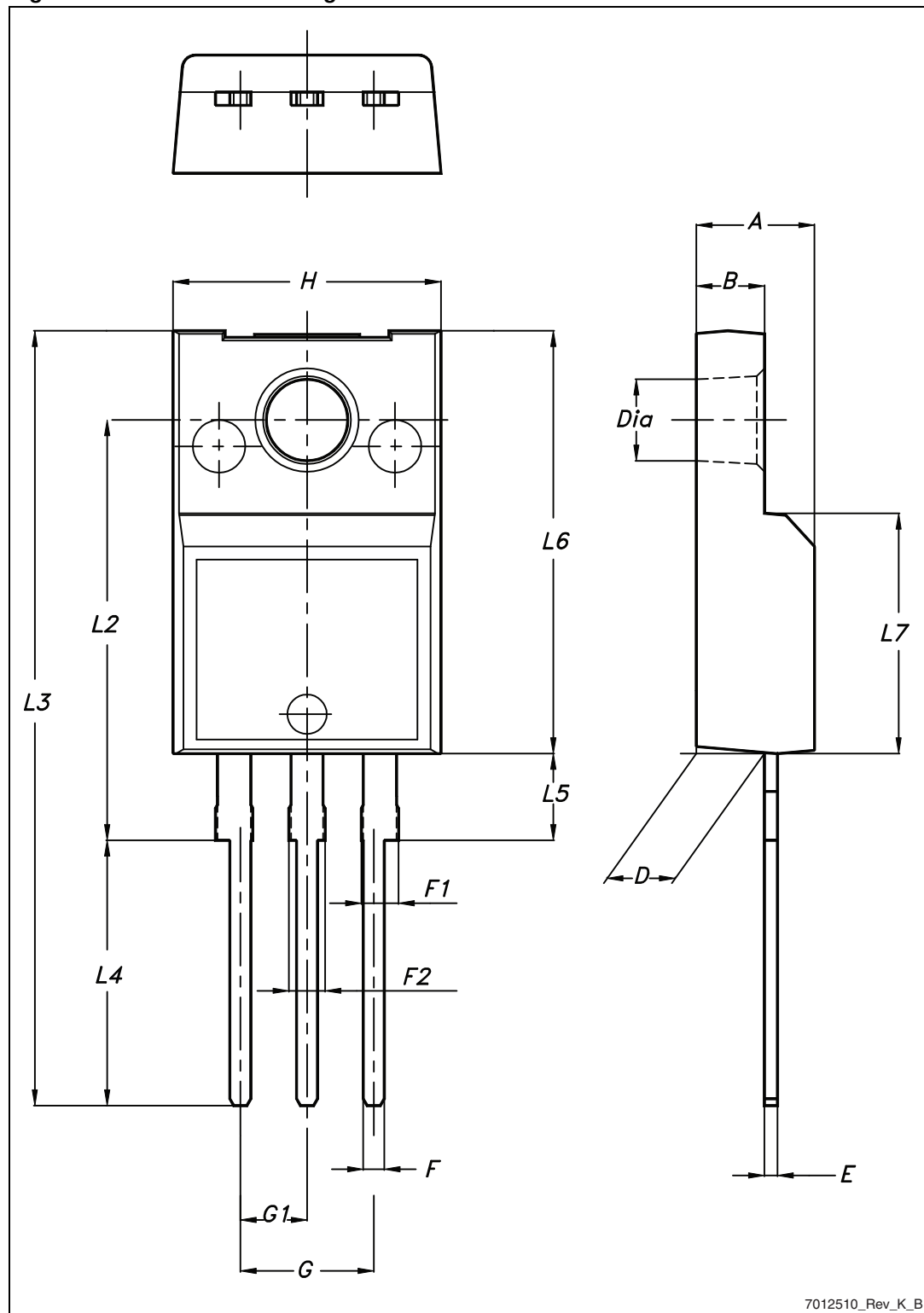


Table 11. TO-220 type A mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
c	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95

Technical drawing of a mechanical component, showing three views: a top view, a front view, and a side view.

Top View: Shows a rectangular base with three vertical pins. The pins are labeled with dimensions $b1(X3)$ and e . The base width is labeled E .

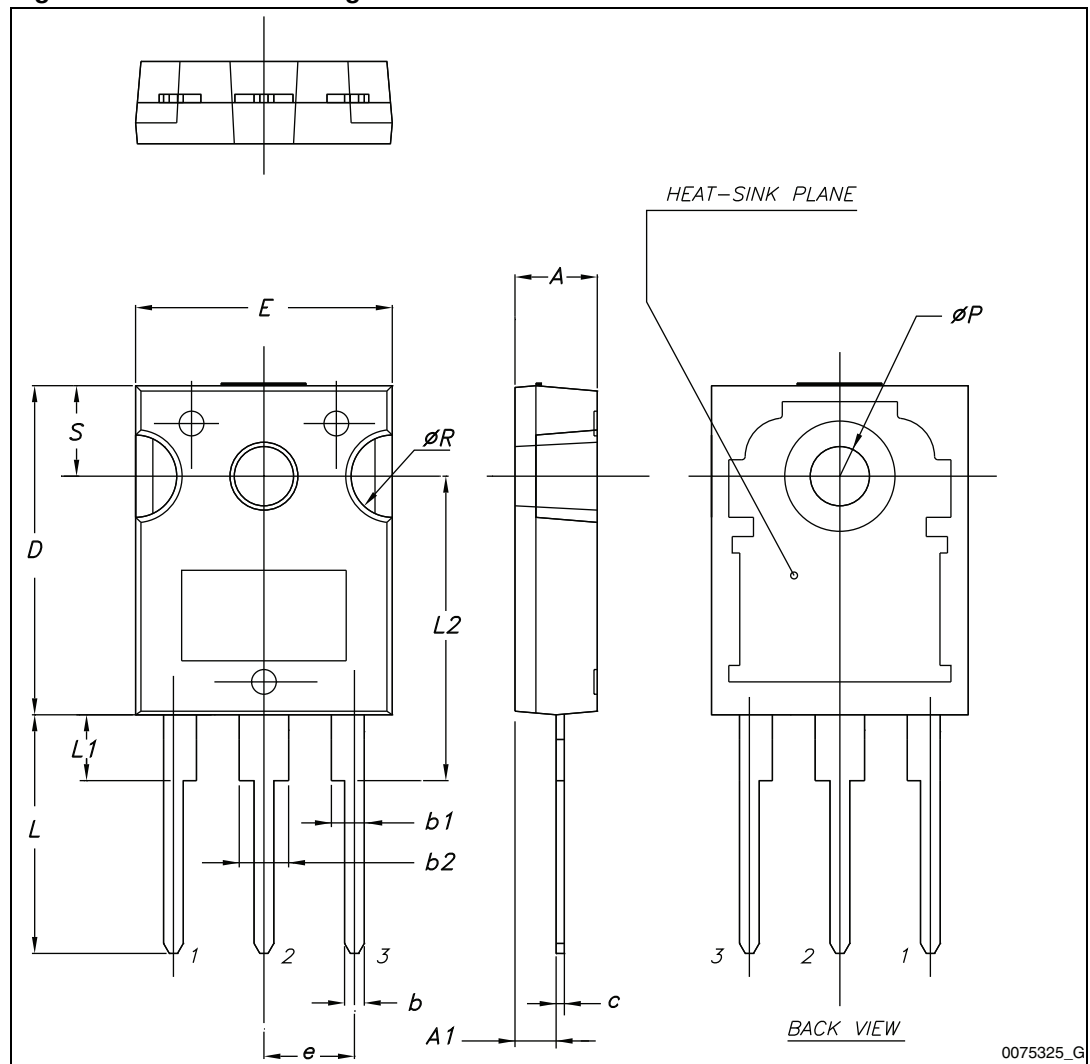
Front View: Shows a rectangular block with a circular feature on top. The top surface is labeled ϕP . The circular feature has a diameter of Q . The block height is labeled $L20$ and $L30$. The total height is labeled L . The bottom surface is labeled $L1$. The base width is labeled $b(X3)$ and $\phi 1$.

Side View: Shows a vertical profile with dimensions D , $H1$, $D1$, $J1$, and c .

Table 12. TO-247 mechanical data

Dim.	mm.		
	Min.	Typ.	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
c	0.40		0.80
D	19.85		20.15
E	15.45		15.75
e	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

Figure 27. TO-247 drawing



5 Packaging mechanical data

Table 13. D²PAK (TO-263) tape and reel mechanical data

Tape			Reel		
Dim.	mm		Dim.	mm	
	Min.	Max.		Min.	Max.
A0	10.5	10.7	A		330
B0	15.7	15.9	B	1.5	
D	1.5	1.6	C	12.8	13.2
D1	1.59	1.61	D	20.2	
E	1.65	1.85	G	24.4	26.4
F	11.4	11.6	N	100	
K0	4.8	5.0	T		30.4
P0	3.9	4.1			
P1	11.9	12.1	Base qty		1000
P2	1.9	2.1	Bulk qty		1000
R	50				
T	0.25	0.35			
W	23.7	24.3			

Figure 28. Tape

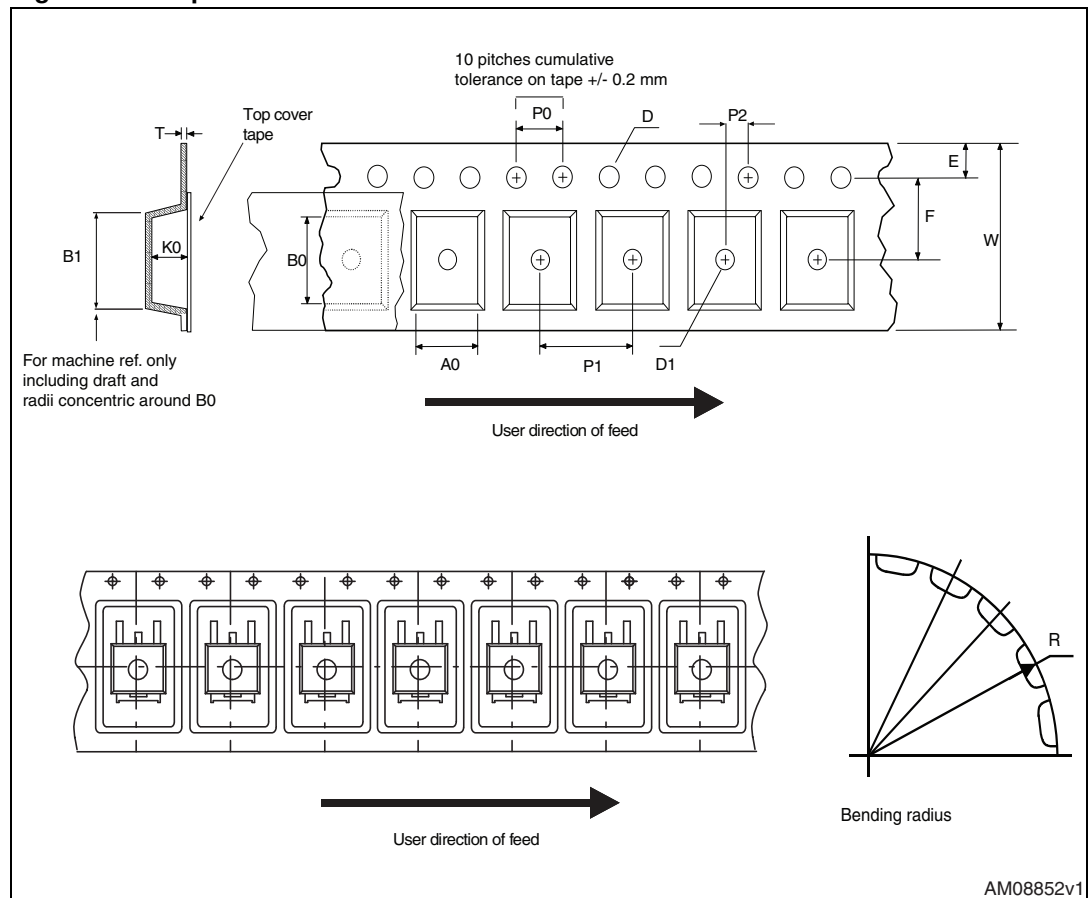
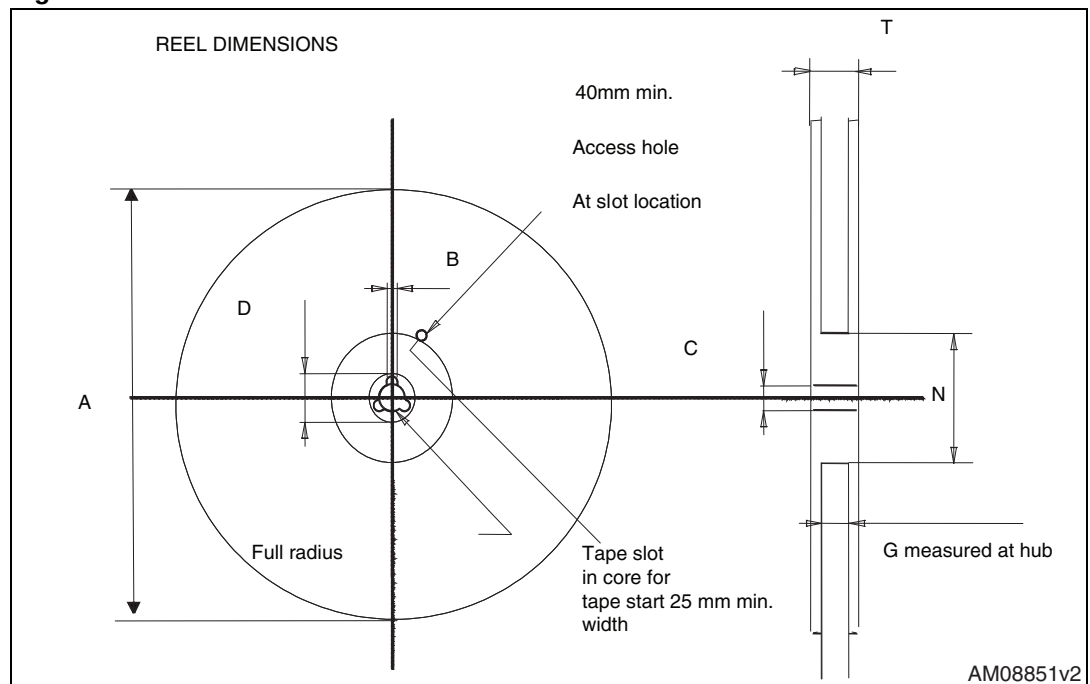


Figure 29. Reel



6 Revision history

Table 14. Document revision history

Date	Revision	Changes
15-May-2013	1	First release

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