Pch -60V -2.0A Small Signal MOSFET

Outline
SOT-457T

SC-95

TSMT6

V <sub>DSS</sub>	-60V
R <sub>DS(on)</sub> (Max.)	210mΩ
I <sub>D</sub>	±2A
P <sub>D</sub>	1.25W

# ●Inner circuit

- 1) Low on resistance
- 2) High Power small mold Package (TSMT6)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

Features

# (1) Drain (2) Drain (3) Gate (4) Source (5) Drain (6) Drain \*1 ESD Protection Diode \*2 Body Diode (1) (2) (3)

Packaging specifications

Packing

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TCR
	Marking	ZV

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{DSS}$	-60	V
Continuous drain current	I <sub>D</sub>	±2	Α
Pulsed drain current	I <sub>DP</sub> *1	±8	Α
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Down discipation	P <sub>D</sub> *2	1.25	W
Power dissipation	P <sub>D</sub> *3	0.95	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

# Application

Switching

# ●Thermal resistance

Parameter	Symbol	Values			Lloit
Parameter		Min.	Тур.	Max.	Unit
The result resistance is resting, ambient	R <sub>thJA</sub> *2	-	-	100	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	-	-	132	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Cymah al	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-60	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -60V, V <sub>GS</sub> = 0V	-	-	-1	μА	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	1	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-	-3.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	3.0	-	mV/°C	
		$V_{GS} = -10V, I_D = -2A$	-	150	210		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	$V_{GS} = -4.5V, I_D = -2A$	-	180	252	mΩ	
		$V_{GS} = -4.0V, I_D = -2A$	-	190	266		
Gate resistance	$R_G$	R <sub>G</sub> f = 1MHz, open drain		8.7	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = -10V, I <sub>D</sub> = -2A	2.5	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Curanh a l	Conditions	Values			l le:4	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	750	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	85	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	45	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -30V, V_{GS} = -10V$	-	9	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -1A	-	12	-	no	
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 30\Omega$	-	45	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	10	-		

# ●Gate charge characteristics (T<sub>a</sub> = 25°C)

	\ a	/				
Darameter	Symbol	Conditions		Values		Unit
Parameter	Symbol Conditions	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	Vnn ≃ <b>-</b> 30V.	-	7.2	-	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \simeq -30V$ , $I_D = -2A$ , $V_{GS} = -5V$	-	2.0	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4		-	2.0	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol Conditions		Values			l leit
Parameter			Min.	Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub>	T - 25°0	-	-	-1	Α
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-8	Α
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

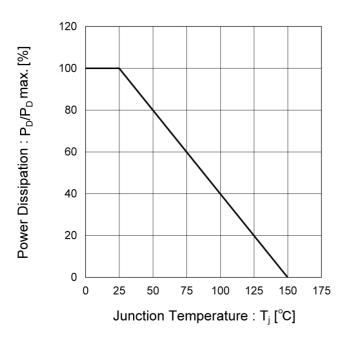
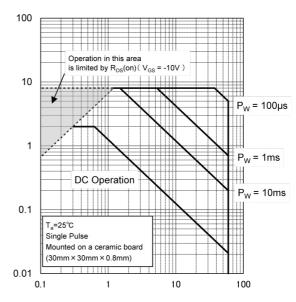


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

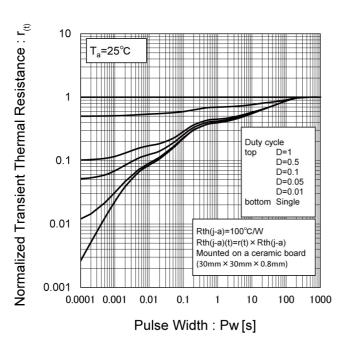
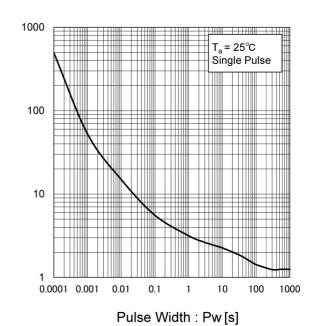


Fig.4 Single Pulse Maximum Power dissipation



Peak Transient Power : P(W)

Fig.5 Typical Output Characteristics(I)

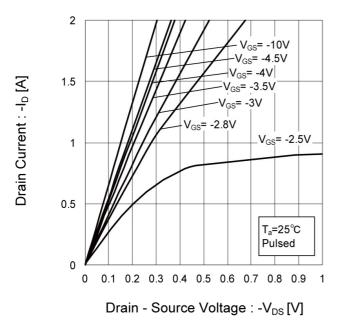
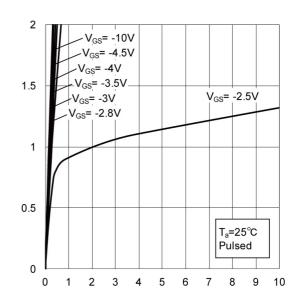


Fig.6 Typical Output Characteristics(II)



Drain Current : -I<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.

Junction Temperature

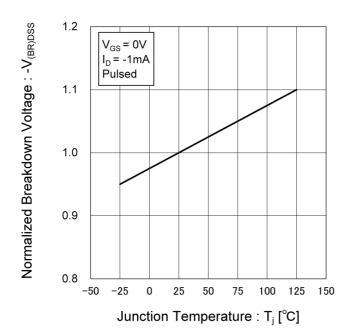


Fig.8 Typical Transfer Characteristics

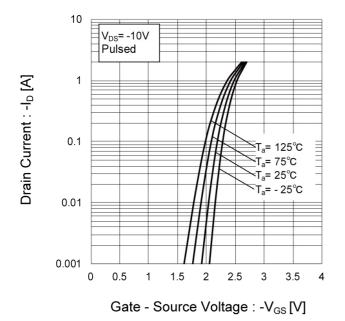


Fig.9 Gate Threshold Voltage vs. **Junction Temperature** 

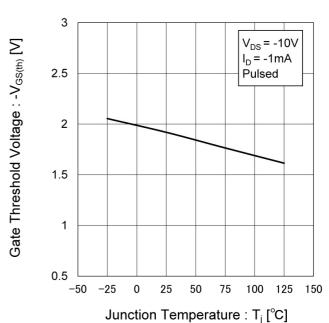
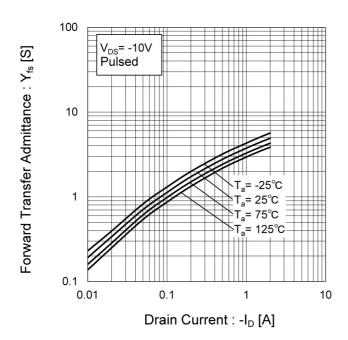


Fig.10 Forward Transfer Admittance vs. **Drain Current** 



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Fig.11 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

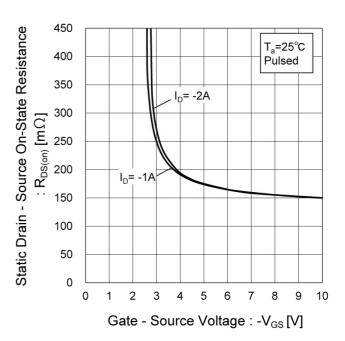


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

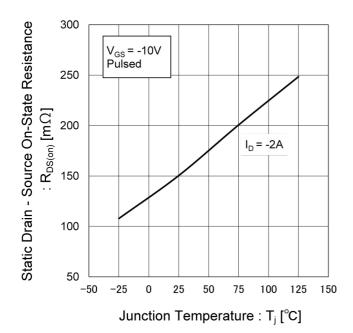


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

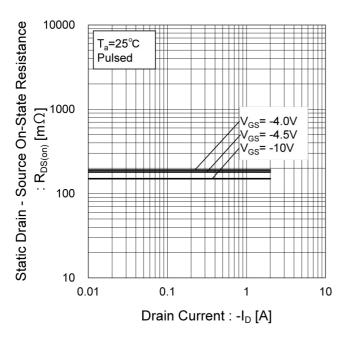


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

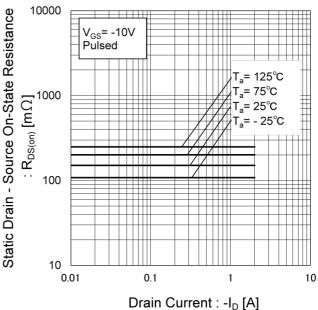


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

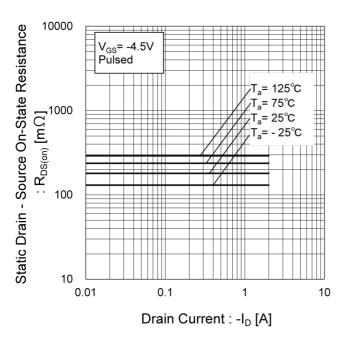
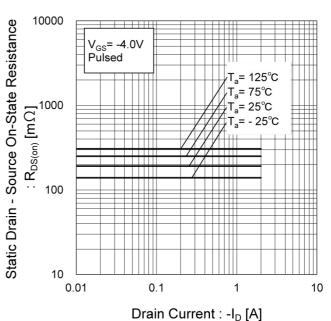


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)



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Fig.18 Typical Capacitance vs.

Drain - Source Voltage

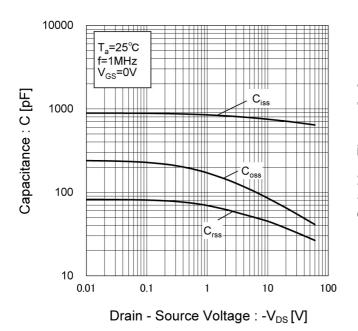


Fig.19 Switching Characteristics

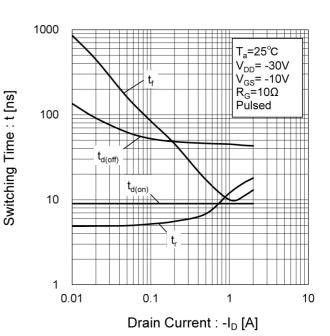


Fig.20 Dynamic Input Characteristics

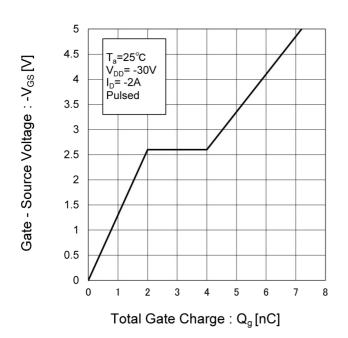
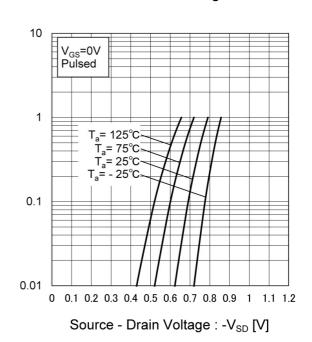


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : -I<sub>s</sub> [A]

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# Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

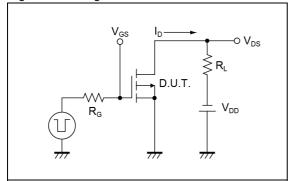


Fig.2-1 Gate Charge Measurement Circuit

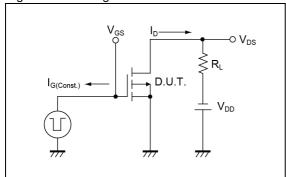


Fig.1-2 Switching Waveforms

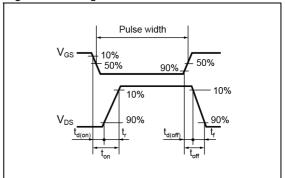
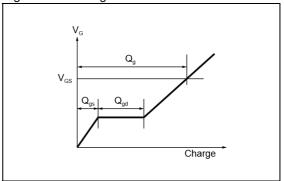


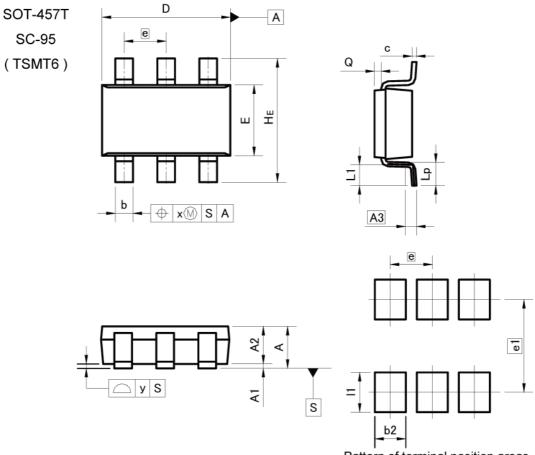
Fig.2-2 Gate Charge Waveform



## Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

# Dimensions



Pattern of	f terminal	l position	areas
[Not a pa	attern of s	soldering	pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	1-	1.00	ı	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.:	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	95	0.0	37
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
х	_	0.20	-	0.008
У	-	0.10	_	0.004

DIM MILIME		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2		0.70	_	0.028
e1	2.10		0.0	83
11	0 <del></del>	0.90	-	0.035

Dimension in mm/inches



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CLASSⅢ	CI ACCIII	CLASS II b	CI VCCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSⅢ

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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
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## **Precautions Regarding Application Examples and External Circuits**

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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

## **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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# RQ6L020SP - Web Page

**Distribution Inventory** 

Part Number	RQ6L020SP
Package	TSMT6
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes