TOSHIBA Transistor Silicon PNP Triple Diffused Type

2SA2184

High Voltage Switching Applications

• High voltage: VCEO = -550 V

• High speed: $t_f = 40 \text{ ns (typ.)} (I_C = -0.5\text{A})$

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit | |
|-----------------------------|-----------|------------------|------------|------|--|
| Collector-base voltage | | V_{CBO} | -550 | V | |
| Collector-emitter voltage | | V _{CEO} | -550 | V | |
| Emitter-base voltage | | V _{EBO} | -7 | V | |
| Collector current | DC | IC | -1 | Α | |
| | Pulse | I _{CP} | -2 | | |
| Base current | | ΙΒ | -1 | Α | |
| Collector power dissipation | Ta = 25°C | Pc | 1 | W | |
| | Tc = 25°C | FC | 20 | | |
| Junction temperature | | Tj | 150 | °C | |
| Storage temperature range | | T _{stg} | -55 to 150 | °C | |

Note:1 Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

Unit: mm 1.5±0.2 5.2±0.2 0.6MAX 5.5±0.2 9.5 ± 0.3 1.1±0.2 0.8MAX 0.6±0.15 2.3±0.15 2.3±0.15 1. Base 2. Collector (heatsink) 3. Emitter **JEDEC** JEITA **TOSHIBA** 2-7J1A

Weight: 0.36 g (typ.)

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

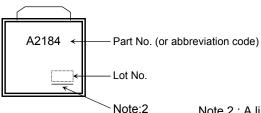
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

| Characteristics | | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------------|--------------|-----------------------|---|------|------|------|--------|
| Collector cut-off current | | I _{CBO} | V _{CB} = -550 V, I _E = 0 | _ | _ | -10 | μΑ |
| Emitter cut-off current | | I _{EBO} | V _{EB} = -7 V, I _C = 0 | _ | _ | -1 | μΑ |
| Collector-emitter breakdown voltage | | V (BR) CEO | I _C = -10 mA, I _B = 0 | -550 | _ | _ | V |
| DC current gain | | h _{FE (1)} | V _{CE} = -5 V, I _C = -100 mA | 80 | _ | 300 | |
| | | h _{FE (2)} | V _{CE} = -5 V, I _C = -500 mA | 5 | _ | _ | |
| Collector emitter saturation voltage | | V _{CE} (sat) | I _C = -300 mA, I _B = -60 mA | _ | _ | -0.7 | V |
| Base-emitter saturation voltage | | V _{BE (sat)} | I _C = -300 mA, I _B = -60 mA | _ | _ | -1.2 | ٧ |
| Transition frequency | | f _T | V _{CE} = -5 V, I _C = -50 mA | _ | 27 | _ | MH_Z |
| Collector output capacitance | | C _{ob} | V _{CB} = -10 V, I _E = 0, f = 1 MHz | _ | 30 | _ | pF |
| Switching time | Rise time | t _r | 20 μs Input ← G IB2 HB2 HB2 VCC = -300 V | _ | 0.1 | _ | μs |
| | Storage time | t _{stg} | | _ | 1.6 | _ | |
| | Fall time | t _f | I _{B1} = 100 mA, I _{B2} = 200 mA Duty cycle ≤ 1% | _ | 40 | _ | ns |

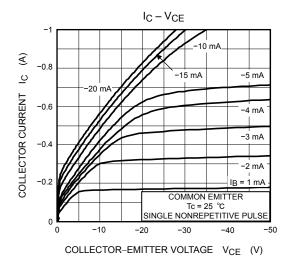
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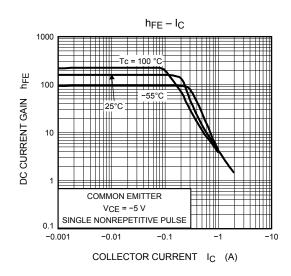
Marking

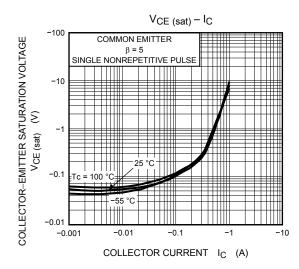


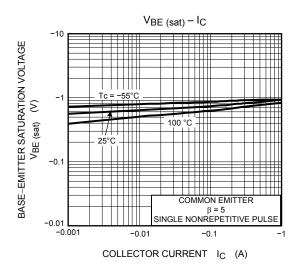
Note 2 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

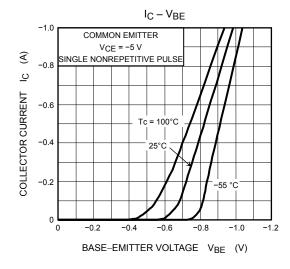
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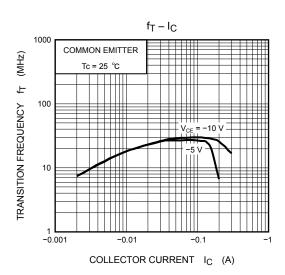




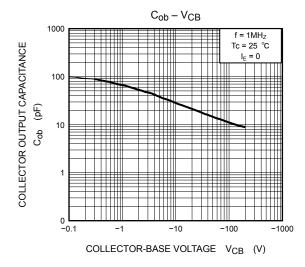


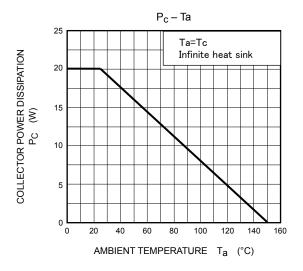


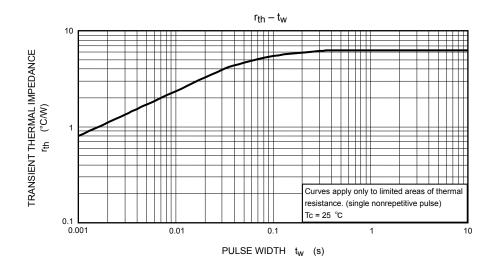


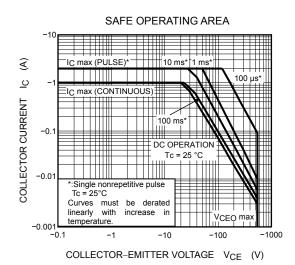


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