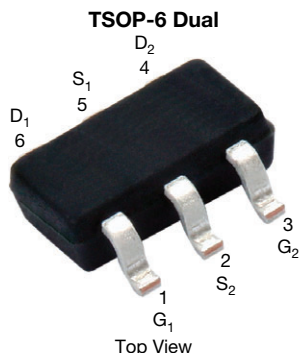


N- and P-Channel 30 V (D-S) MOSFET



Marking Code: ED

PRODUCT SUMMARY		
	N-CHANNEL	P-CHANNEL
V_{DS} (V)	30	-30
$R_{DS(on)}$ (Ω) at $V_{GS} = \pm 10$ V	0.058	0.165
$R_{DS(on)}$ (Ω) at $V_{GS} = \pm 4.5$ V	0.073	0.276
Q_g typ. (nC)	1.8	2.4
I_D (A) ^c	3.7	-2.1
Configuration	N- and p-pair	

FEATURES

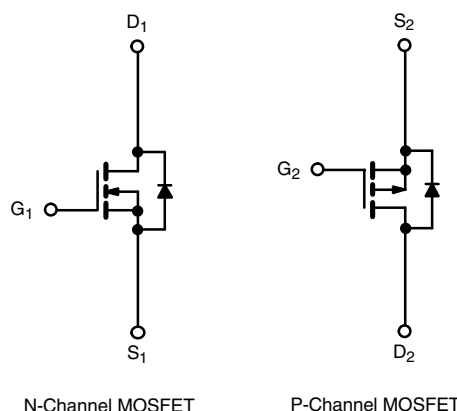
- TrenchFET® power MOSFET
- 100 % R_g tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912

APPLICATIONS

- Load switch
- DC/DC converter
- Drivers: motor, solenoid, relay



RoHS
COMPLIANT
HALOGEN
FREE
Available



N-Channel MOSFET

P-Channel MOSFET

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	Si3552BDV-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	N-CHANNEL	P-CHANNEL	UNIT
Drain-source voltage	V_{DS}	30	-30	V
Gate-source voltage	V_{GS}	± 20	± 20	
Continuous drain current ($T_J = 150$ °C) ^{a, b}	I_D	3.4	-1.9	A
		2.7	-1.5	
Pulsed drain current	I_{DM}	15	-6	
Continuous source current (diode conduction) ^{a, b}	I_S	0.95	-0.91	W
maximum power dissipation ^{a, b}	P_D	1.1	1.1	
		0.7	0.7	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +150		°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^a	$t \leq 5$ s	R_{thJA}	97	115	°C/W
	Steady state		132	155	
Maximum junction-to-lead	Steady state	R_{thJL}	78	95	

Notes

- Surface mounted on FR4 board
- $t \leq 5$ s
- $T_C = 25$ °C



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	N-Ch	1.2	-	2.2	V	
		V _{DS} = V _{GS} , I _D = -250 μA	P-Ch	-1.2	-	-2.5		
Gate-body leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	N-Ch	-	-	± 100	nA	
			P-Ch	-	-	± 100		
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	N-Ch	-	-	1	μA	
		V _{DS} = -30 V, V _{GS} = 0 V	P-Ch	-	-	-1		
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C	N-Ch	-	-	10		
		V _{DS} = -30 V, V _{GS} = 0 V, T _J = 55 °C	P-Ch	-	-	-10		
On-state drain current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	N-Ch	10	-	-	A	
		V _{DS} = -5 V, V _{GS} = -10 V	P-Ch	-6	-	-		
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 3.4 A	N-Ch	-	0.047	0.058	Ω	
		V _{GS} = -10 V, I _D = -1.9 A	P-Ch	-	0.137	0.165		
		V _{GS} = 4.5 V, I _D = 3 A	N-Ch	-	0.058	0.073		
		V _{GS} = -4.5 V, I _D = -1.2 A	P-Ch	-	0.230	0.267		
Forward transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 3.4 A	N-Ch	-	10	-	S	
		V _{DS} = -15 V, I _D = -1.9 A	P-Ch	-	3	-		
Diode forward voltage ^a	V _{SD}	I _S = 1 A, V _{GS} = 0 V	N-Ch	-	0.85	1.2	V	
		I _S = -1 A, V _{GS} = 0 V	P-Ch	-	-0.84	-1.2		
Dynamic ^b								
Total gate charge	Q _g	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 3.4 A	N-Ch	-	1.8	3	nC	
		V _{DS} = -15 V, V _{GS} = -4.5 V, I _D = -1.9 A	P-Ch	-	2.4	3.6		
Gate-source charge	Q _{gs}	N-channel V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 3.4 A	N-Ch	-	0.74	-		
			P-Ch	-	1.2	-		
Gate-drain charge	Q _{gd}	P-channel V _{DS} = -15 V, V _{GS} = -4.5 V, I _D = -1.9 A	N-Ch	-	0.42	-		
			P-Ch	-	0.8	-		
Gate resistance	R _g	f = 1 MHz	N-Ch	1	5	10	Ω	
			P-Ch	1.5	7.3	14.6		
Turn-on delay time	t _{d(on)}	N-channel V _{DD} = 15 V, R _L = 5.6 Ω, I _D ≡ 2.7 A, V _{GEN} = 10 V, R _g = 1 Ω P-channel V _{DD} = -15 V, R _L = 10 Ω, I _D ≡ -1.5 A, V _{GEN} = -10 V, R _g = 1 Ω	N-Ch	-	5	10	ns	
Rise time	t _r		P-Ch	-	4	8		
			N-Ch	-	15	30		
			P-Ch	-	9	18		
Turn-off delay time	t _{d(off)}		N-Ch	-	10	20		
			P-Ch	-	11	18		
Fall time	t _f		N-Ch	-	10	20		
			P-Ch	-	7	14		
Source-drain reverse recovery time	t _{rr}	I _F = 1 A, di/dt = 100 A/μs	N-Ch	-	10	20		
		I _F = -1 A, di/dt = 100 A/μs	P-Ch	-	14	21		

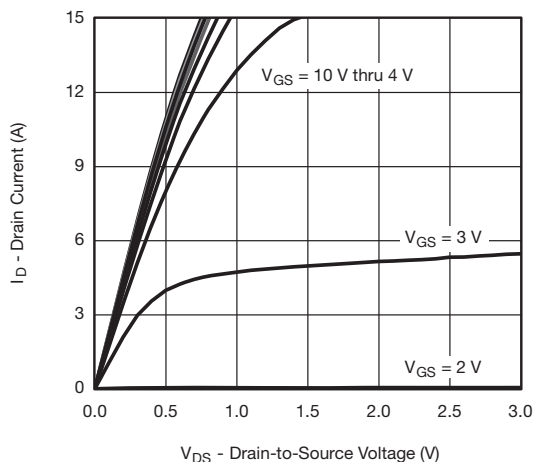
Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
b. Guaranteed by design, not subject to production testing

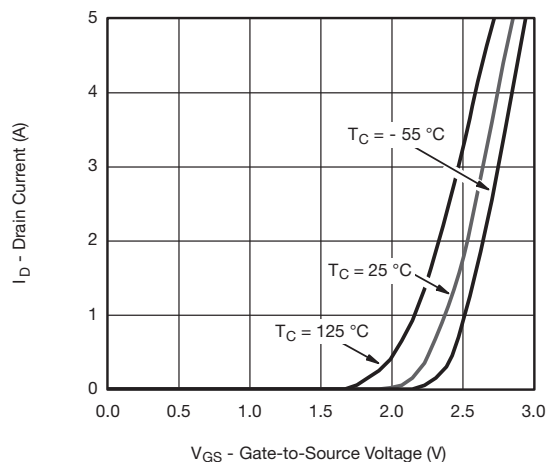
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



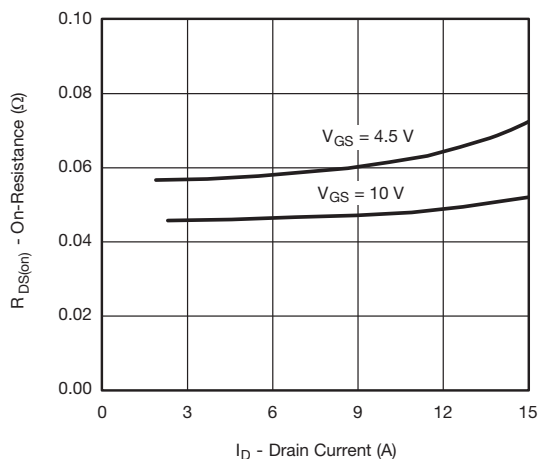
N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



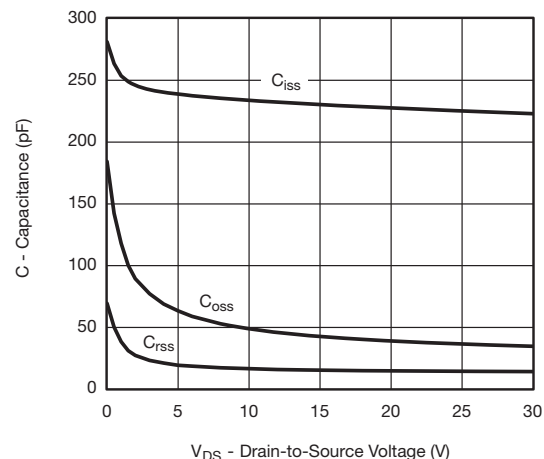
Output Characteristics



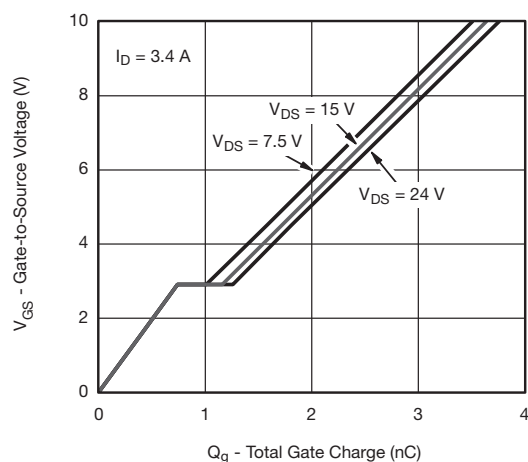
Transfer Characteristics



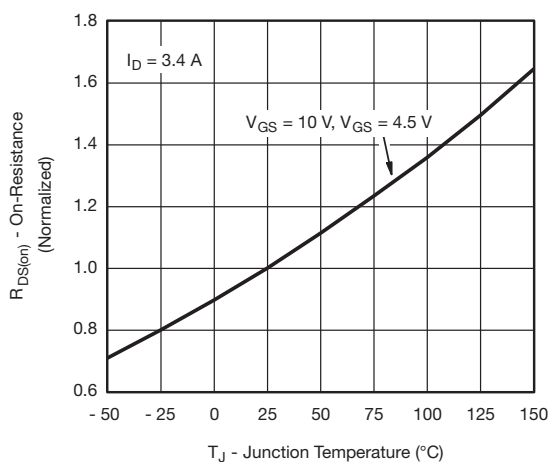
On-Resistance vs. Drain Current



Capacitance



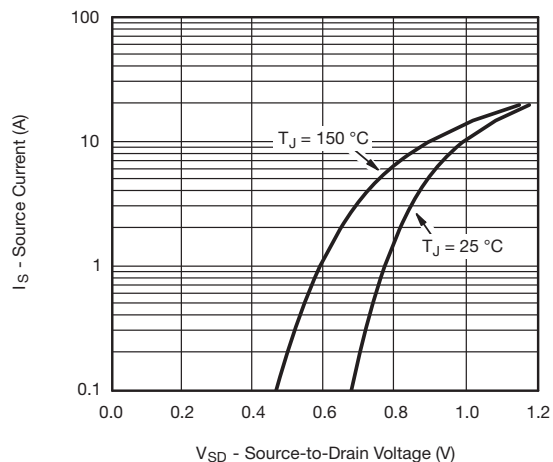
Gate Charge



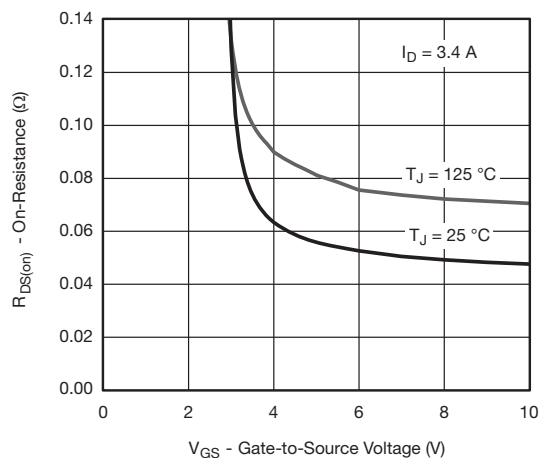
On-Resistance vs. Junction Temperature



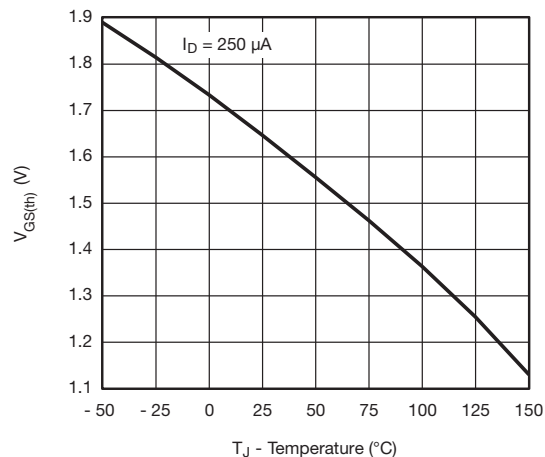
N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



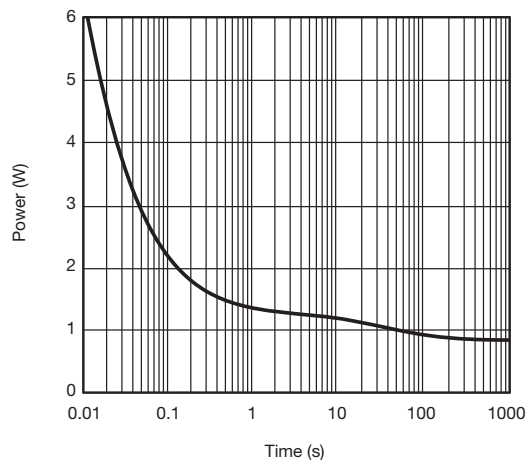
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



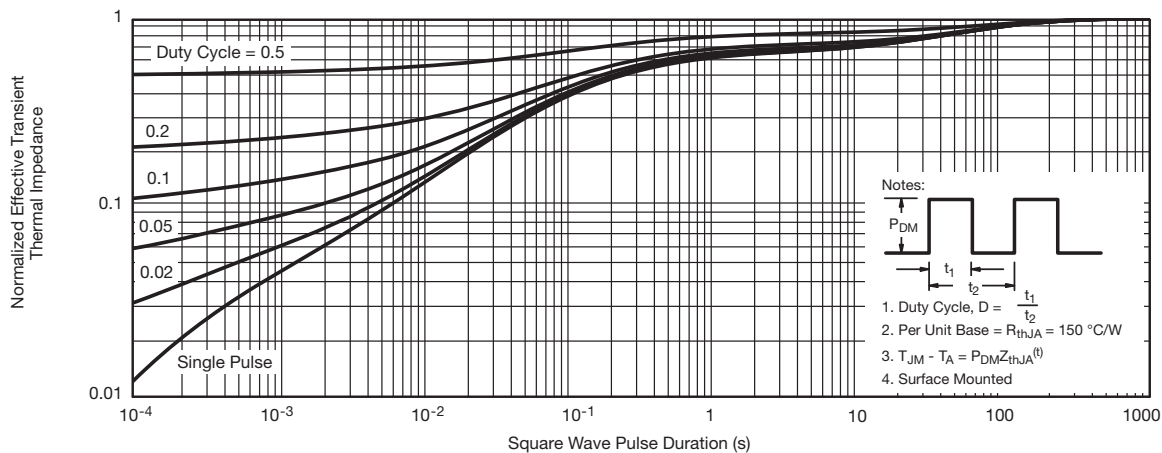
Threshold Voltage



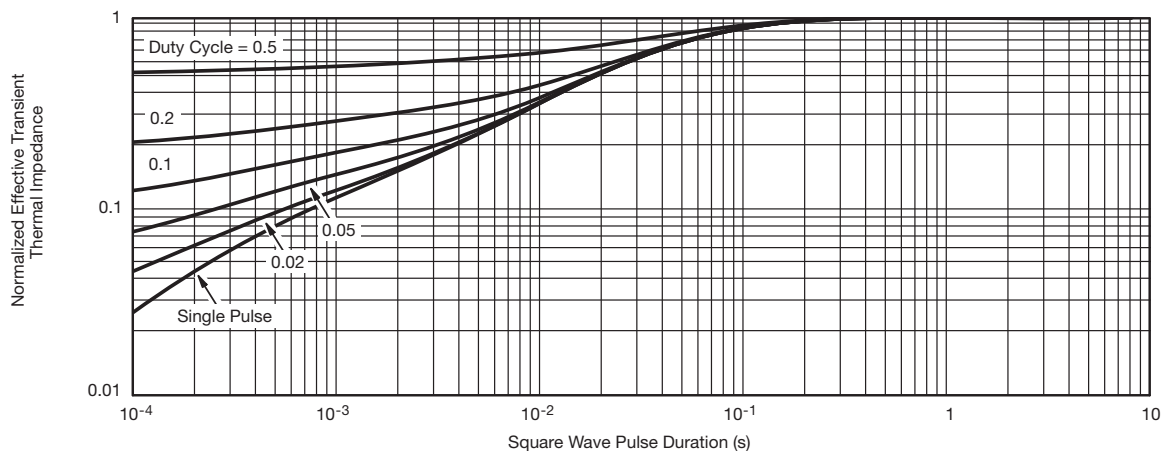
Single Pulse Power (Junction-to-Ambient)



N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



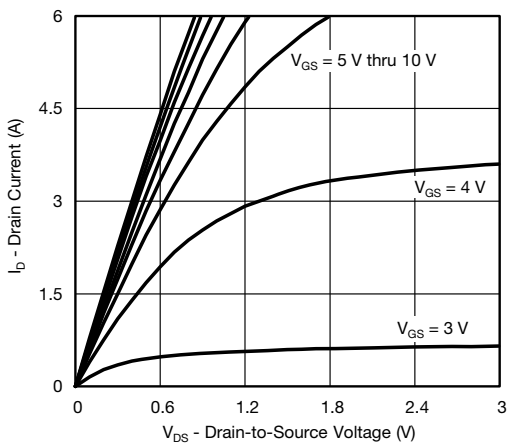
Normalized Thermal Transient Impedance, Junction-to-Ambient



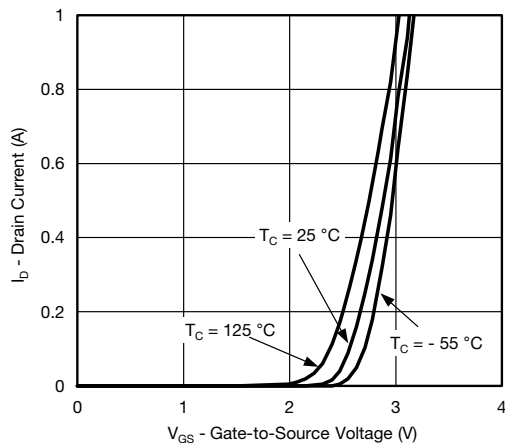
Normalized Thermal Transient Impedance, Junction-to-Foot



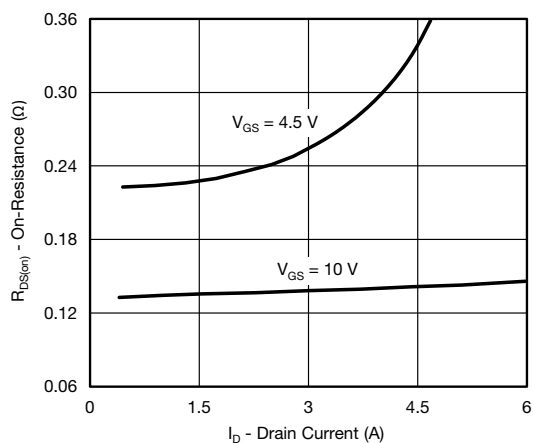
P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



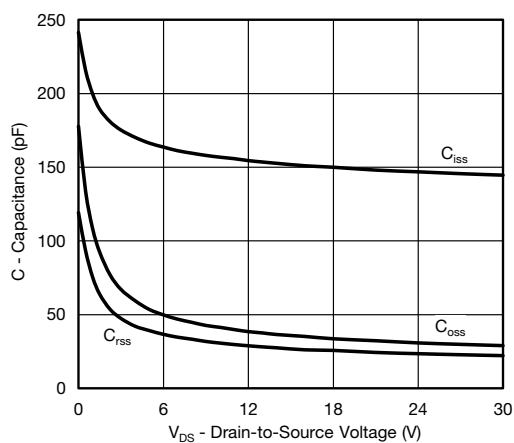
Output Characteristics



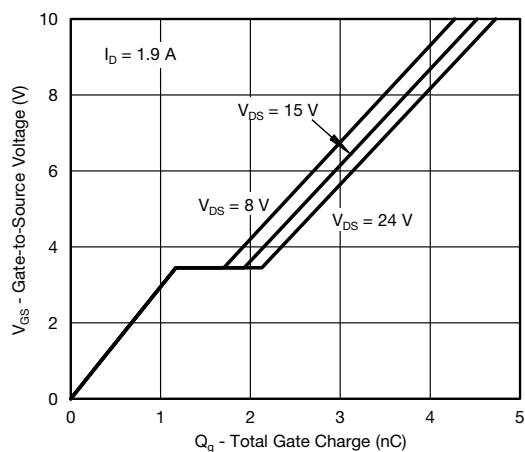
Transfer Characteristics



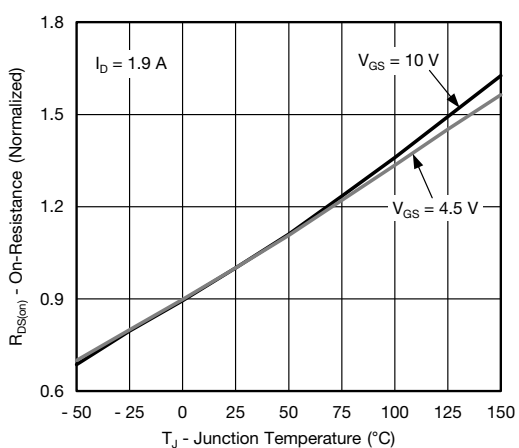
On-Resistance vs. Drain Current



Capacitance



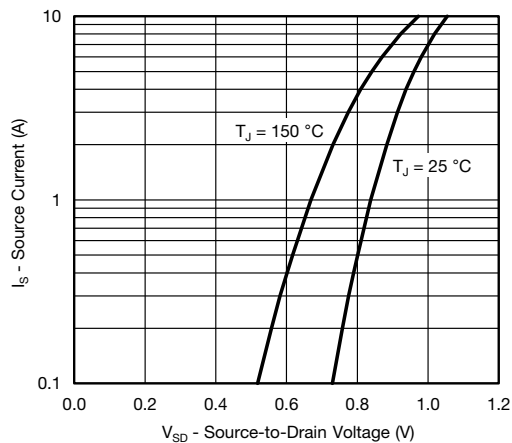
Gate Charge



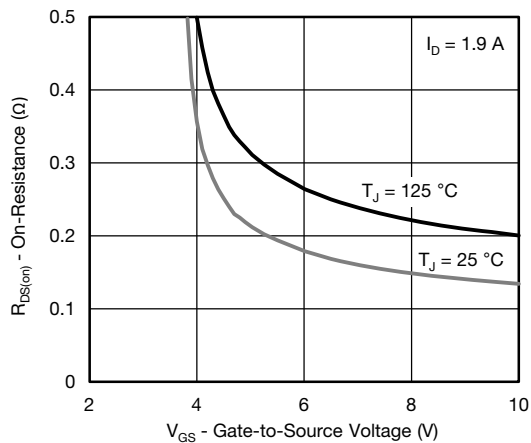
On-Resistance vs. Junction Temperature



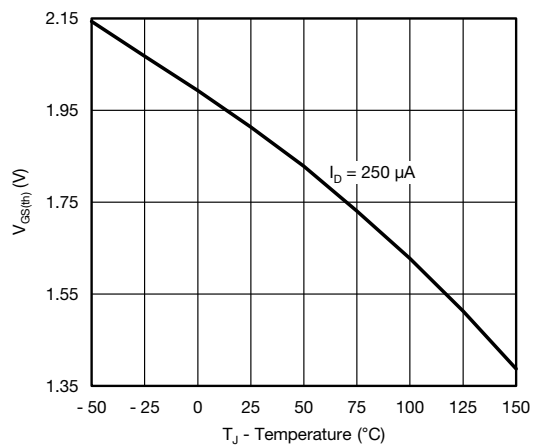
P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



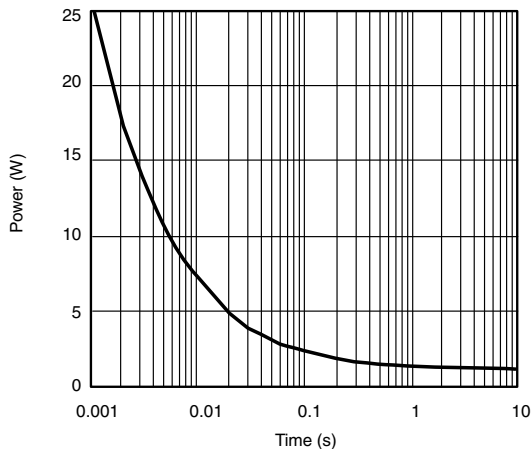
Source-Drain Diode Forward Voltage



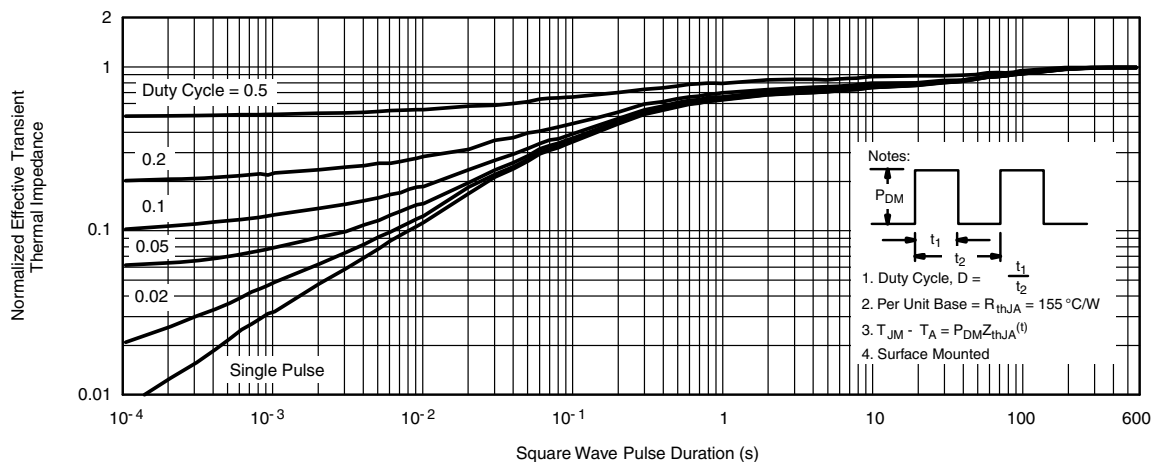
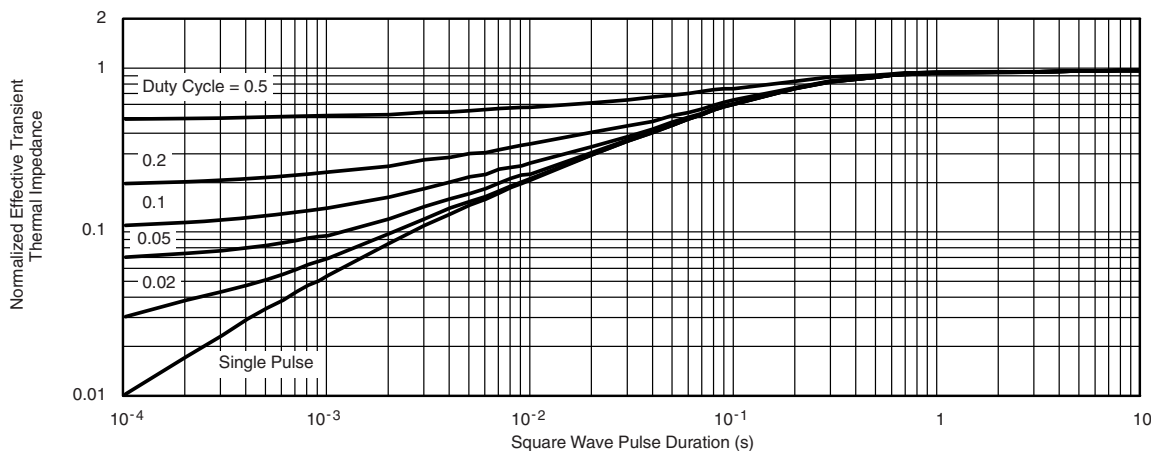
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



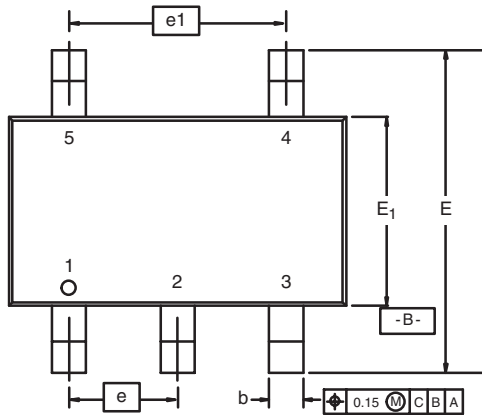
Single Pulse Power (Junction-to-Ambient)

P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

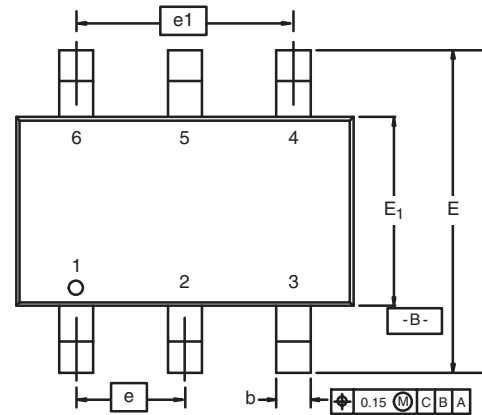
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?61827.

TSOP: 5/6-LEAD

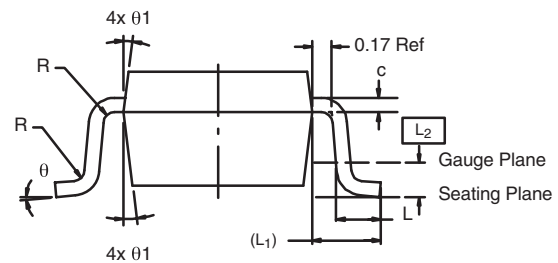
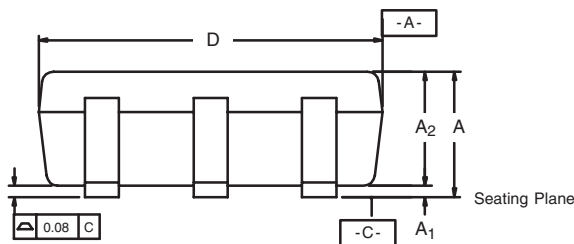
JEDEC Part Number: MO-193C



5-LEAD TSOP

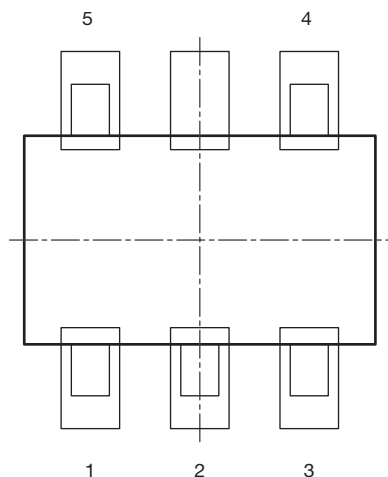


6-LEAD TSOP

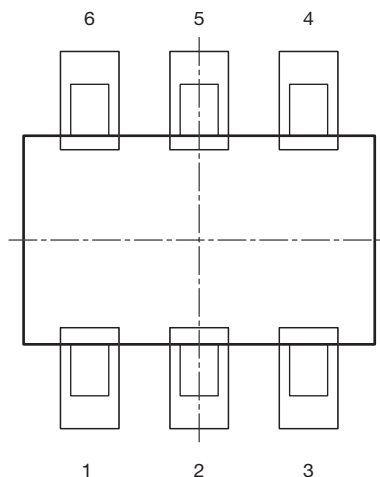


Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
A	0.91	-	1.10	0.036	-	0.043
A ₁	0.01	-	0.10	0.0004	-	0.004
A ₂	0.90	-	1.00	0.035	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E ₁	1.55	1.65	1.70	0.061	0.065	0.067
e	0.95 BSC			0.0374 BSC		
e ₁	1.80	1.90	2.00	0.071	0.075	0.079
L	0.32	-	0.50	0.012	-	0.020
L ₁	0.60 Ref			0.024 Ref		
L ₂	0.25 BSC			0.010 BSC		
R	0.10	-	-	0.004	-	-
θ	0°	4°	8°	0°	4°	8°
θ ₁	7° Nom			7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06						
DWG: 5540						

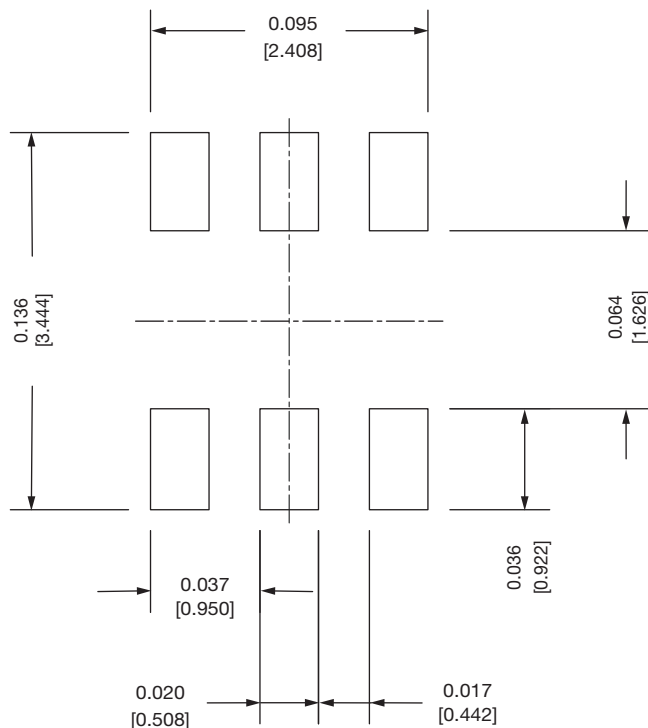
Recommended Land Pattern For TSOP-5L / TSOP-6L



TSOP 5L



TSOP 6L


Note

- All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022
DWG: 3010



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.