

Reference Specification

High Voltage Ceramic Capacitor DHRE4AD222Z4QB Issued Date: July 17, 2007

Product specifications in this drawing are subject to change or our products described in this drawing may be discontinued without advance notice. The parts numbers and specifications listed in this drawing are for information only. You are requested to transact the "Approval Sheet Product Specification", before your ordering.

PRODUCT ENGINEERING SECTION CAPACITOR GROUP

IZUMO MURATA MANUFACTURING. Co.,LTD

1.OPERATING VOLTAGE

When DC- rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage(1)	Pulse Voltage(2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings.

Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.(Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

3. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. The capacitor is designed to be used in the insulating media, such as epoxy resin, silicone oil, etc.. There must be 3mm or more insulating media for each direction of the capacitor. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85 %. Use capacitors within 6 months after delivered.

4. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

5. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 °C max. Soldering iron wattage: 50W max. Soldering time: 3.5s max.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion

when the product is used.

6. Limitation of Applications

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

①Aircraft equipment②Aerospace equipment③Undersea equipment⑤Medical equipment

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NOTICE

Cleaning

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

Capacitance change of capacitor

· Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

· Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

⚠ NOTE

- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from the agreed specifications.
- 3. We consider it not appropriate to include any terms and conditions with regard to the business transaction in the product specifications, drawings or other technical documents. Therefore, if your technical documents as above include such terms and conditions such as warranty clause, product liability clause, or intellectual property infringement liability clause, they will be deemed to be invalid.

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1. Application

This specification is applied to ceramic capacitor DHR series used for in electric equipment. DHR series is high voltage disc ceramic capacitor

2. Rating

2.1 Operating temperature

-30°C to +85°C

2.2 Part number configuration

<u>DHR</u>	<u>E4</u>	<u>AD</u>	<u>222</u>	<u>Z</u>	<u>4Q</u>	<u>B</u>	
Series	Temperature	Rated	Capacitance	Capacitance	Lead	Packing	Individual
	characteristic	voltage		tolerance	code	style code	specification

• Temperature characteristic

Code	Temperature	characteristic
E4	Z	5U

Please confirm detailed specification on [5. Specification and test methods].

Rated voltage

Code	Rated voltage
AD	DC7.5kV

Capacitance

The first two digits denote significant figures ; the last digit denotes the multiplier of 10 in pF. ex.) In case of 222.

$$22 \times 10^2 = 2200 pF$$

• Capacitance tolerance

Please refer to [4. Part number list].

· Lead code

Code	Lead style							
4Q	Straight long							

Please refer to [4. Part number list].

Solder coated copper wire is applied for termination.

· Packing code

<u> </u>	
Code	Packing type
В	Bulk type

• Individual specification

In case part number cannot be identified without 'individual specification', it is added at the end of part number.

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3. Marking

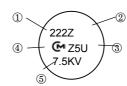
①. Capacitance : marked with 3 figures.

② Cap. tolerance : marked with code.

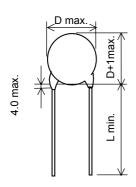
③ Temperarure Characteristic : marked with code.

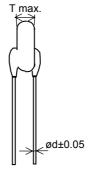
Manufacturer's identification: marked with code.

⑤ Rated Voltage: marked with code.



4. Part number List







Unit:mm

Тетр. Сар.	Cap. Customer part		Murata part number	DC Rated.		Dime	nsions (mm)		Lead	Pack	
Char.	Char. (pF) (%) number	volt. (kV)		D	T	F	L	d	code	qty.		
Z5U	2200	+80/-20		DHRE4AD222Z4QB	7.5	15.0	7.0	9.5	25.0	0.8	4Q	50

5.TEST

5-1VISUAL EXAMINATION CHECK OF DIMENSIONS

No.	ITF		SPECIFICATION	TESTING METHOD			
1	Appearance dimensions		See 3,4.	Shall be visually examined or Venire			
'	Appearance dimensions		See 5,4.	Calipers.			
2	Marking		To be easily legible.	Shall be visually examined.			
_	iviaikiiig		To be easily legible.	Shall be visually examined.			
5-2E	5-2ELECTRICAL PERFORMANCE TESTS						
1	Dielectric	Between	No failure	The capacitors shall not be damage			
	Strength	Lead wires		when DC voltage of 150% of the rated			
				voltage are applied between the lead			
				wires for 60 s in insulate liquid or gas.			
				(Charge/discharge current: 50mA max.)			
		Body		The capacitors is placed in the			
		insulation		container with metal balls of diameter			
				1mm so that each lead wires, Short-			
				circuited, is kept approximately 2mm			
				off the balls as shown in the figure, and DC voltage of 3kV is applied for 10 s			
				between capacitor lead wires and			
				small metals.			
				(Charge/discharge current: 50mA max.)			
2	Insulation	Between	10,000M Ω min.	The insulation resistance shall be			
	Resistance	Lead wires		measured with DC 1kV within 60±5 s of			
	(I.R.)			charging.			
3	Capacitance		Within the specified tolerance.	The capacitance shall be measured at			
				25°C with 1±0.2kHz and AC5V(r.m.s.)			
				max			
4	Dissipation Factor		2.5% max.	Same condition as capacitance.			
_	(D.F.)			The considered was a way and the line			
5	Temperature Characteristic		Within +22~ -56%	The capacitance measurement shall be made at each step specified in table.			
				Capacitance change from the value of step			
				3 shall not exceed the limit specified.			
				Step			
				Z5U 25±2°C 10±2°C 25±2°C 85±2°C 25±2°C			
6	Charge	Appearance	No marked defect.	Charge discharge test shall be measured			
	Discharge	Capacitance	Within ±10%	in the following test circuit and cycle.			
	Test	Change	VVIIIII1 ± 10 /6	Applied voltage: rated voltage			
		D.F.	4.0% max.	Cycle numbers: 20,000 cycles			
		I.R.	1,000MΩ min.	Post-treatment: Capacitor shall be stored			
		Dielectric	No failure	for 4 h at room condition.			
		Strength					
		(Between		6 s E:Direct-current			
		lead wires)		voltage source			
				Co:Suppled energy for Cx.			
				(Co≠10Cx)			
				Cx Cx: Specimen R1:Circuit protective			
				$\not\equiv$ \perp \downarrow resistor (300k Ω)			
				T E T S R2:Current limiting			
				Co R2 Resistor (E/10Ω)			
N.L. 4 - 1	. T4- (Di-14	air ataras atta Ola	- mailion of Director and a section of the set. I have a	dity. Life and Temperature cycling shall			

Note) Tests for Dielectric strength ,Charging/Discharging test, Humidity , Life and Temperature cycling shall be performed with specimens having molded resin (MR1023C:made by Murata) extending over 3mm on all the surface.

Room condition

Temperature:15~35°C Humidity:45~75%

Atmospheric pressure:86~106kPa

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5-3 MECHANICAL PERFORMANCE TESTS

Ν	o. ITE	M	SPECIFICATION	TESTING METHOD
	1 Strength of Lead	Pull	Lead wire shall not cut off. Capacitor shall not be broken.	Fix the body of capacitor, apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N, and keep it for 10±1 s.
		Bending		Each lead wire shall be subjected to 5N weight and then a 90° to bend, at the point of egress, in one direction, return to original position, and then a 90° bend in the opposite direction at the rate of one bend in 2 to 3 s.
2	Solderability of L	_eads	with uniformly coated on the axial direction over 3/4	The lead wire shall be dipped into a 25% methanol solution of rosin and then into molten solder of 235±5°C for 2±0.5 s. In both cases the depth of dipping is up to about 1.5 to 2.0mm from the root of lead wires.
	Soldering Effect	Appearance Capacitance Change Dielectric Strength (Between lead wires)	No marked defect. Within±10% No failure	The lead wires shall be immersed into the melted solder of 350±10°C up to about 1.5 to 2.0mm from the main body for 3.5±0.5 s. Post-treatment:Capacitor shall be stored for 24±2 h at room condition.

5-4 CLIMATIC TESTS

No.	ITE	N/I	SPECIFICATION	TESTING METHOD
1	Humidity	•••	No marked defect.	
'	,	Appearance		Set the capacitor for 240±8 h at 40±2°C
	(under steady state)	Capacitance	Within±10%	in 90 to 95% humidity.
	state)	Change	4.00/	Post-treatment: Capacitor shall be stored for 1 to 2 h at room condition.
		D.F.	4.0% max.	
		I.R.	1,000M Ω min.	(Charge/discharge current: 50mA max.)
		Dielectric	No failure	(Charge/discharge current, 50m/Amax.)
		Strength		
		(Between		
		lead wires)		
2	Life	Appearance	No marked defect.	Apply a DC voltage of 125% of the rated
	(high temperature	Capacitance	Within±10%	voltage for 1000+48/-0 h in silicon oil at
	load)	Change		85±2°C.
		D.F.	4.0% max.	Post-treatment: Capacitor shall be stored
		I.R.	1,000M Ω min.	for 24±2 h at room condition.
		Dielectric	No failure	(Charge/discharge current:50mA max.)
		Strength		
		(Between		
		lead wires)		
3	Temperature	Appearance	No marked defect.	+85 °C
	Cycling	Capacitance	Within±10%	
		Change		
		D.F.	4.0% max.	
		I.R.	1,000M Ω min.	0000
		Dielectric	No failure	-30°C
		Strength		0.0 0.0 11
		(Between		Temperature cycling shall be measured
		lead wires)		in the following test.
		•		Cycle numbers: 5 cycles
				Post-treatment: Capacitor shall be stored
				for 4 h at room condition.

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6. Packing Specification

Packaging Styles: Bulk type

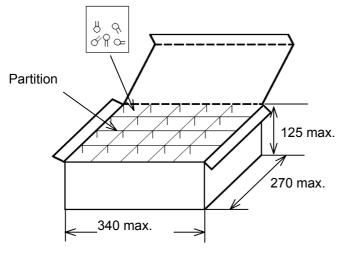
(Packing style code : B)

*2

The number of packing = Packing quantity \times n

The size of packing case and packing way

Polyethylene bag



*1 : Please refer to [Part number list]. *2 : Standard n = 20 (bag)

Note)

The outer package and the number of outer packing be changed by the order getting amount.

Unit: mm

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