molex

Part Number: 387206203

Product Description: 9.53mm Pitch Beau PCB

Terminal Strip, PC Tail, 3 Circuits

Series Number: 38720

Status: Obsolete

Product Category: Terminal Blocks and Barrier

Strip

Engineering Number: 72503



Documents & Resources

Drawings

387206203 sd.pdf

3D Models and Design Files

STEP AP242

SOLIDWORKS

Creo

Specifications

PS-38710-001-001.pdf



Compliance

GADSL/IMDS	Not Relevant
China RoHS	⊚ per SJ/T 11365-2006
EU ELV	Not Relevant
Low-Halogen Status	Not Low-Halogen per IEC 61249-2-21
REACH SVHC	Not Contained per D(2025)4165-DC (25 June 2025)
EU RoHS	Compliant per EU 2015/863

Compliance Statements

- EU RoHS
- REACH SVHC



• Low-Halogen

Industry Documents

- IPC 1752A Class C
- IPC 1752A Class D
- Molex Product Compliance Declaration
- IEC-62474
- chemSHERPA (xml)

Substances of Interest

PFAS

EU RoHS Certificate of Compliance

<u>Additional Product Compliance Information</u>

Part Details

General

Status	Obsolete
Category	Terminal Blocks and Barrier Strip
Series	38720
Description	9.53mm Pitch Beau PCB Terminal Strip, PC Tail, 3 Circuits
Application	Wire-to-Board
Component Type	One Piece
Product Name	Fixed Mount Barrier
Туре	Barrier Strips
UPC	800756332512

Electrical

Cur <mark>ren</mark> t - Maxim <mark>um</mark> per Contact	15.0A
Voltage - Maximum	300V

Physical

Circuits (Loaded)	3
Circuits (maximum)	3
Color - Resin	Black
Entry Angle	Horizontal
Lock to Mating Part	None
Material - Metal	Brass

Material - Plating Mating	Tin
Material - Plating Termination	Tin
Material - Resin	Polyester
Number of Rows	1
Orientation	Horizontal
Panel Mount	No
PCB Retention	Yes
PCB Thickness - Recommended	3.18mm
PC Tail Length	4.80mm
Pitch - Mating Interface	N/A
Pitch - Termination Interface	9.53mm
Plating min - Mating	3.810µm
Plating min - Termination	3.810µm
Polarized to Mating Part	No
Shrouded	Dual-Barrier
Stackable	No
Temperature Range - Operating	-40° to +130°C
Termination Interface Style	Screw or Lug
Wire Size (AWG)	14, 16, 18, 20, 22
Wire Size mm ²	0.33-2.08

Solder Process Data

Lead-Free Process Capability	WAVE