

# MMBT2222AT

## **NPN Epitaxial Silicon Transistor**

### **Features**

- General purpose amplifier transistor.
- Ultra-Small Surface Mount Package for all types.
- · General purpose switching & amplification application



September 2008

## Absolute Maximum Ratings T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Unit	
$V_{CBO}$	Collector-Base Voltage	75	V	
V <sub>CEO</sub> Collector-Emitter Voltage		40	V	
$V_{EBO}$	Emitter-Base Voltage	6	V	
I <sub>C</sub>	Collector Current	600	mA	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature Range	-55 ~ 150	°C	

## Thermal Characteristics\* Ta=25°C unless otherwise noted

Symbol	Parameter	Max	Unit
P <sub>C</sub>	Collector Power Dissipation, by $R_{\theta JA}$	250	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	500	°C/W

<sup>\*</sup> Minimum land pad.

## Electrical Characteristics\* Ta=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10\mu A, I_E = 0$	75		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 1 \text{mA}, I_B = 0$	40		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10\mu A, I_C = 0$	6		V
I <sub>CEX</sub>	Collector Cut-off Current	$V_{CE} = 60V$ , $V_{EB(OFF)} = 3V$		10	nA
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 1V, I_{C} = 0.1 \text{mA}$ $V_{CF} = 1V, I_{C} = 1 \text{mA}$	35 50		
		V <sub>CE</sub> = 1V, I <sub>C</sub> = 10mA V <sub>CE</sub> = 1V, I <sub>C</sub> = 150mA	75 100		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA		0.3 1.0	V V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA	0.6	1.2 2.0	V V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 20V, I_{C} = 20mA, f = 100MHz$	300		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$		8	pF
C <sub>ib</sub>	Input Capacitance	$V_{EB} = 0.5V, I_{C} = 0, f = 1MHz$		30	pF
t <sub>d</sub>	Delay Time	$V_{CC} = 30V, I_{C} = 150mA$		10	ns
t <sub>r</sub>	Rise Time	I <sub>B1</sub> =- I <sub>B2</sub> = 15mA		25	ns
t <sub>s</sub>	Storage Time	7		225	ns
t <sub>f</sub>	Fall Time			60	ns

<sup>\*</sup> DC Item are tested by Pulse Test : Pulse Width≤300us, Duty Cycle≤2%

<sup>\* 1.</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## **Typical Performance Characteristics**

Figure 1. DC Current Gain

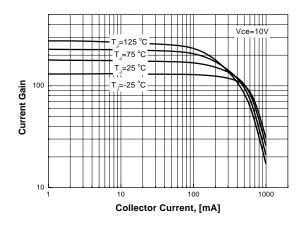


Figure 2. DC Current Gain

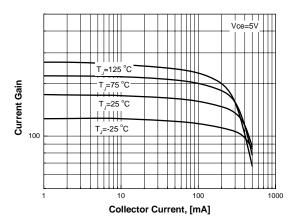


Figure 3. Collector-Emitter Saturation Voltage

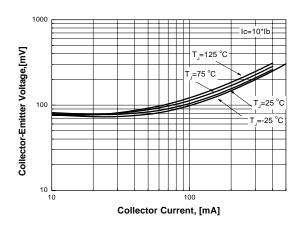


Figure 4. Base-Emitter Saturation voltage

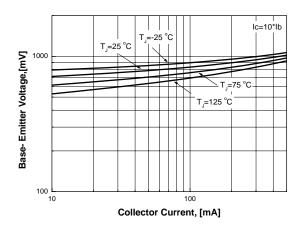


Figure 5. Collector- Base Leakage Current

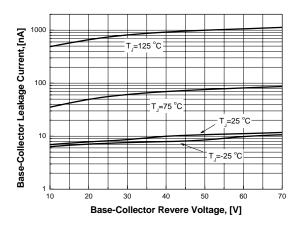
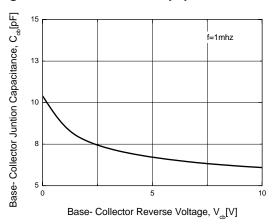
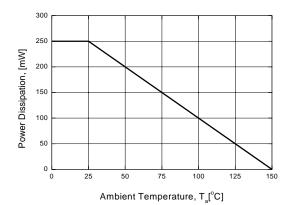


Figure 6. Collector-Base Capapcitance



# **Typical Performance Characteristics**

## Figure 7. Power Derating



# **Package Dimensions**

### **SOT-523F**

Case: SOT-523F

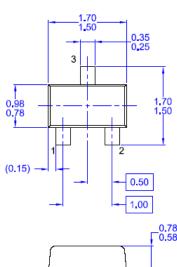
Case Material(Molded Plastic): KTMC1060SC

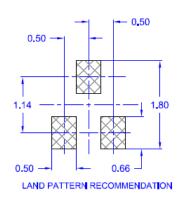
UL Flammability classification rating: "V0"

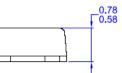
Moisture Sensitivity level per JESD22-A1113B : MSL 1

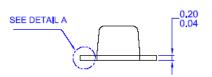
• Lead terminals solderable per MIL-STD7502026 /JESD22A121

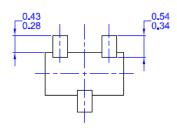
• Lead Free Plating : Pure Tin(Matte)











**Dimensions in Millimeters** 





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# PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition	
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Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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