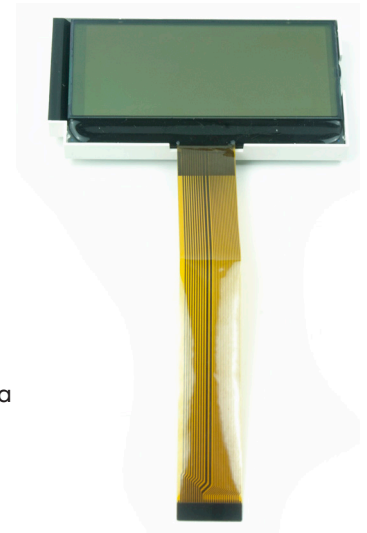


IDB-CI064-4073-XX-01

Overview

IDS have designed a break-out board for our CI064-4073-xx range of standard COG displays.

The IDB-CI064-4073-xx-01. enables our CI064-4073-xx COG displays to be used with a pin interface, and on the standard 0.1" header. The IDB-CI064-4073-xx-01. also has a built-in connector for a resistive touch screen option.

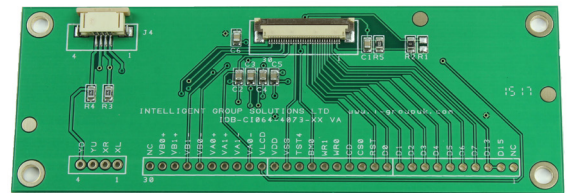


LCD Connection

- Insert the LCD Flexi with the white side up
- Push the brown Flexi Clamp Back in place

Touch Connector

Pin Number	Symbol	Function
1	YD	Y axis down electrode
2	YU	Y axis up electrode
3	XR	X axis right electrode
4	XL	X axis left electrode



Pin Description

Pin no	Symbol	Function									
30	NC	No connection									
29	VBO +	LCD Bias Voltages. These are the voltage sources to provide SEG driving currents. These voltages are generated internally. Connect capacitor of 2.2uF value between: i) VA0+ & VA0- ii) VA1+ & VA1+ iii) VBO+ & VBO- iv) VB1+ & VB1+									
28	VB1 +										
27	VB1 -										
26	VBO -										
25	VA0 +										
24	VA1+										
23	VA1 -	High voltage LCD Power Supply. Connect these pins together. A bypass capacitor CL of 1uF should be connected between VLCD and VSS.									
22	VA0 -										
21	VLCD	Power supply, +3.3V. VSS supplies for Display Data RAM and digital logic.									
20	VDD										
19	VSS	Power supply, 0V. Connect VSS and VSS2 to the shared GND pin.									
18	TST4	TST4 control test mode and is also used to supply one of the high voltage required for MTP Program operation. Leave TST4 open during normal LCD operation.									
17	BMO	Bus mode: The interface bus mode is determined by BMO and [D15 D13] by the following relationship:									
		<table border="1"> <thead> <tr> <th>BMO</th> <th>{D15, D13}</th> <th>Mode</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Data</td> <td>6800/16-bit</td> </tr> <tr> <td>0</td> <td>Data</td> <td>8080/16-bit</td> </tr> </tbody> </table>	BMO	{D15, D13}	Mode	1	Data	6800/16-bit	0	Data	8080/16-bit
		BMO	{D15, D13}	Mode							
1	Data	6800/16-bit									
0	Data	8080/16-bit									

Pin no	Symbol	Function
16	WR1	WR[1:0] control the read/write operation of the interface. See Host interface section for details. In Parallel mode, the meaning of WRR[1:0] depends on which interface is it in, 6800 or 8080 mode. In serial interface modes these two pins are not used, connect them to VSS.
15	WR0	
14	CD	Display/control data select "H":display data; "L":control data
13	CS0	Chip select pin. Chip is selected when CS0=L (CS1=H hardwired internally)
12	RST	External reset pin, low active. If RST not used, connect to VDD.
11-4	D7-D0	Bi-directional bus for parallel host interface
3	D13	
2	D15	
1	NC	No Connection