



Integrated Device Technology, Inc.
6024 Silver Creek Valley Road, San Jose, CA 95138
Phone #: (408) 284-8200

Errata Notification

EN #: KEN-12-08

Errata Revision #: N/A

Issue Date: November 7, 2012

Effective Date: November 7, 2012

Product Affected: 9DB403D devices (Refer Attachment 2 for the affected parts)

Contact: IDT Clock Marketing Team

E-mail: clocks@idt.com

ERRATA DETAILS:

ATTACHMENT

☒ Yes ☐ No

☒ Data Sheet Errata

☐ Device Errata

☐ Device Manual Errata

☐ Application Notes Errata

☐ Other (Please Specify)

Updated Input-to-Output Skew max value (Bypass Mode condition only) on page 7 of datasheet from 4500ps to 5000ps, per latest characterization data. Refer to Attachment 1 for additional details.

Please visit IDT web site (www.IDT.com) or contact your local IDT sales representative for additional details.

CUSTOMER NOTIFICATION:

As a result of Errata listed above, IDT has decided to notify you through this Errata Notification.

We recommend you to review this notification in details prior to the use of affected product in your application.

Please use acknowledgement below or E-Mail to request additional information.

If you have any questions, please feel free to contact your local IDT distributor or sales representative.

Customer: _____

E-Mail Address: _____

Name/Date: _____

Phone #: _____

Title: _____

Fax #: _____

CUSTOMER COMMENTS: _____



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ATTACHMENT 1 - EN # : KEN-12-08

Datasheet updates as follows:

Updated Input-to-Output Skew max value (Bypass Mode condition only) on page 7 of datasheet from 4500ps to 5000ps, per latest characterization data.

From:

Skew, Input to Output	t_{pdBYP}	Bypass Mode, $V_T = 50\%$	2500	4500	ps	1
	t_{pdPLL}	PLL Mode $V_T = 50\%$	-250	250	ps	1

To:

Skew, Input to Output	t_{pdBYP}	Bypass Mode, $V_T = 50\%$	2500	5000	ps	1
	t_{pdPLL}	PLL Mode $V_T = 50\%$	-250	250	ps	1



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Affected Part Numbers

Part Number	Part Number	Part Number	Part Number
9DB403DFILF	9DB403DFLF	9DB403DGILF	9DB403DGLF
9DB403DFILFT	9DB403DFLFT	9DB403DGILFT	9DB403DGLFT