

8 7 6 5 4 3 2 1

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JUMPER TABLE

JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	17MAR09	A.MOORE
B	ENGINEERING UPDATE	04MAY09	A.MOORE
C	ENGINEERING UPDATE	29JUN09	D.ROWE

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

SCHEMATIC

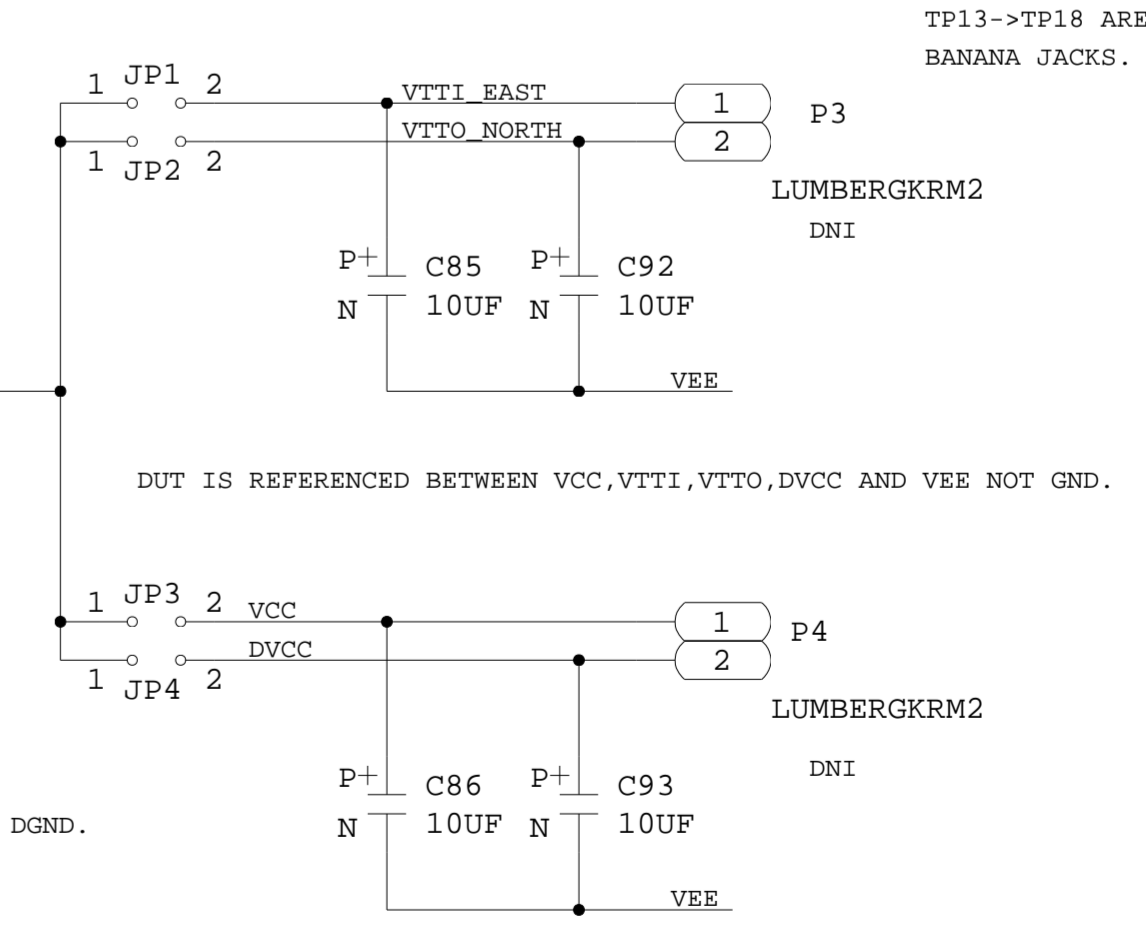
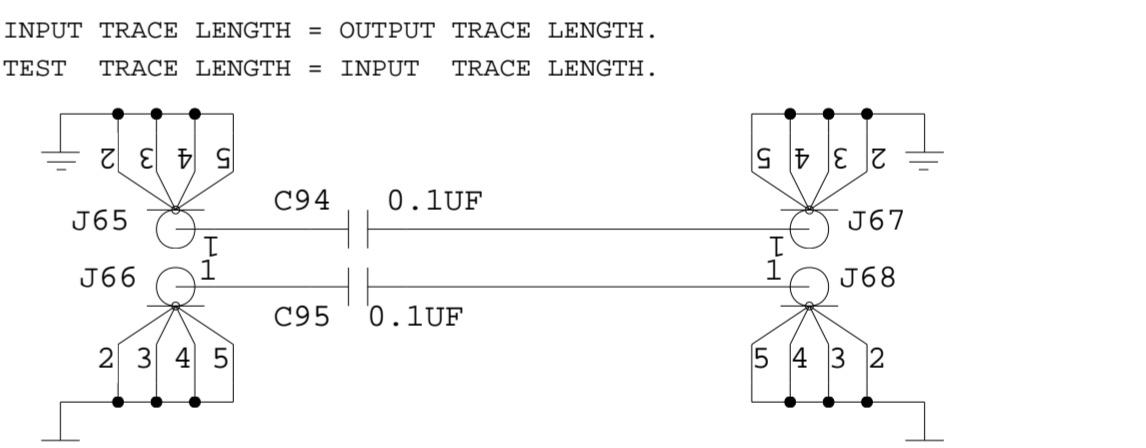
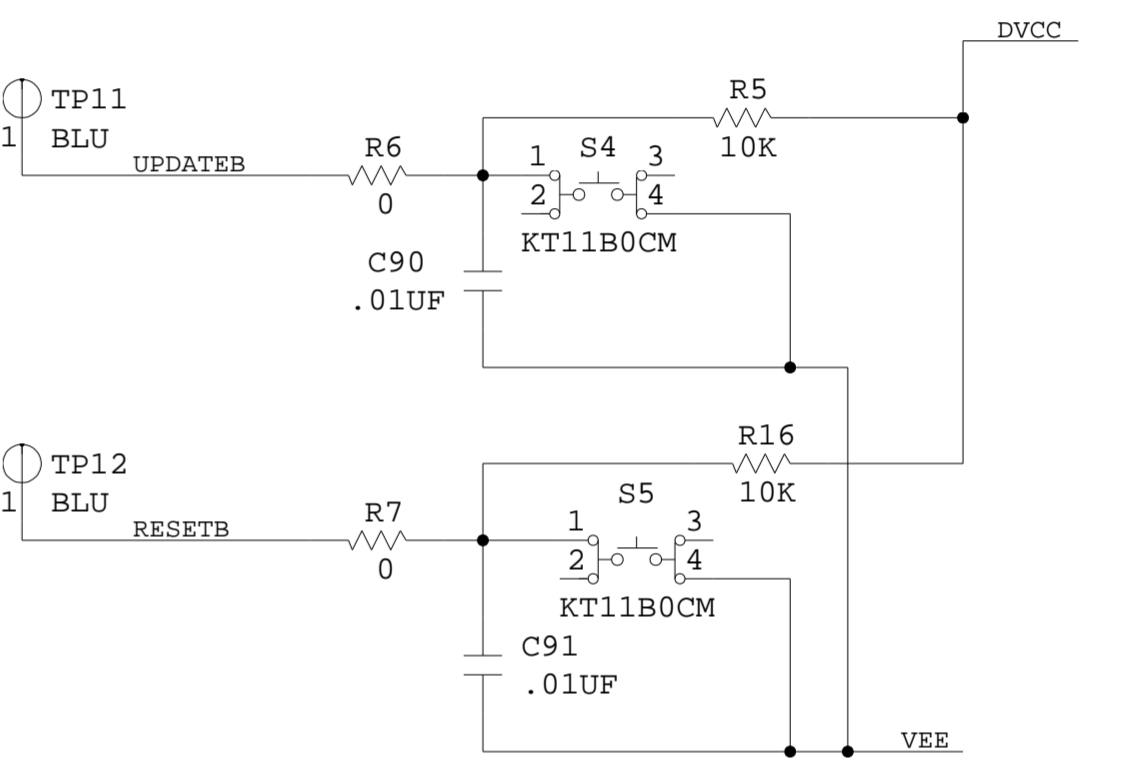
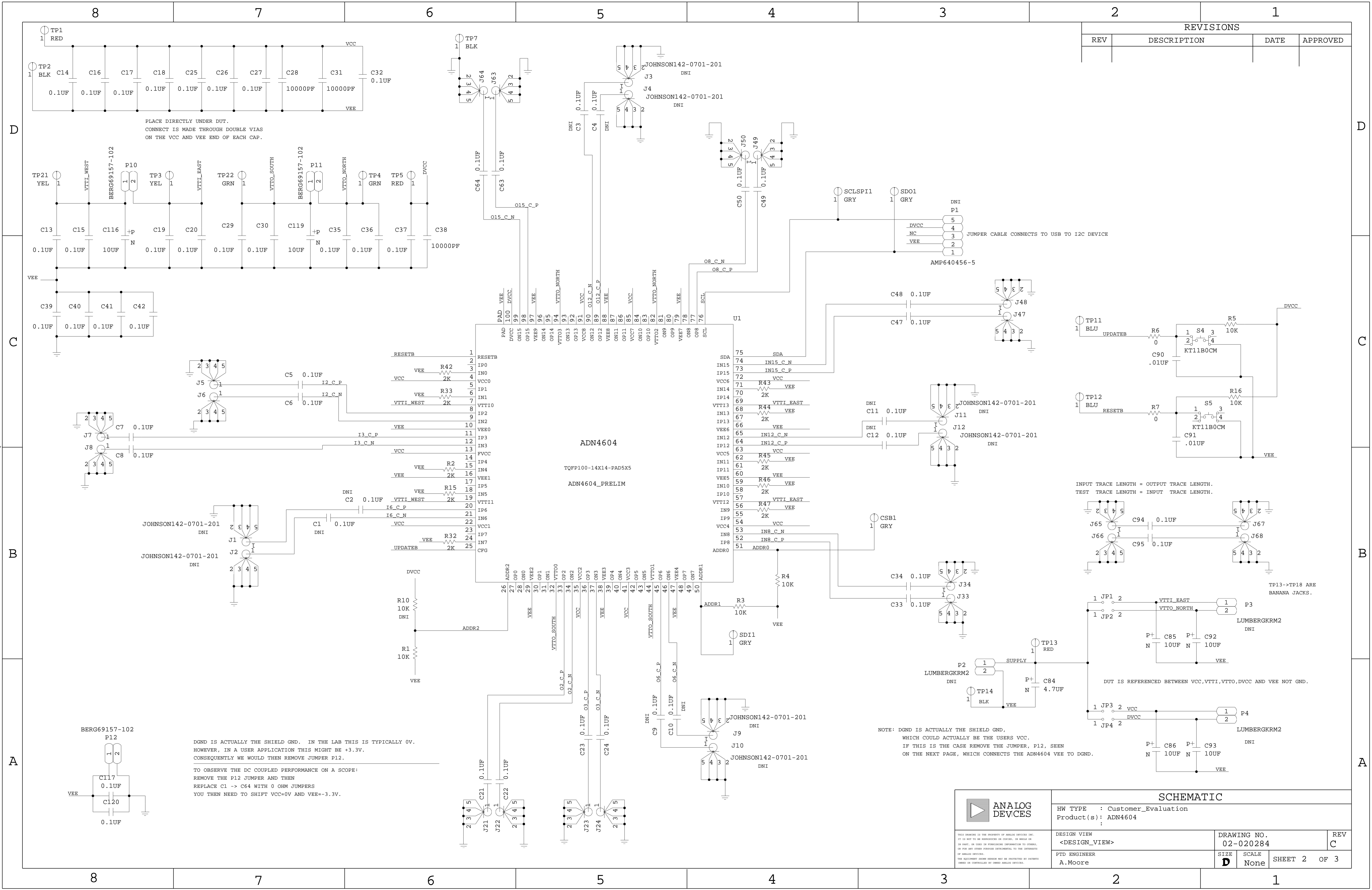


TEMPLATE ENGINEER *	DATE	HW TYPE : Customer_Evaluation Product(s) : ADN4604 : PACKAGE : 100-lead 14X14_MM TQFP_EP-family : 0.50-pitch AMKOR Singulated TESTER : N/A CONTACTOR : N/A HANDLER : N/A					
HARDWARE SERVICES *							
HARDWARE SYSTEMS *							
TEST ENGINEER *							
COMPONENT ENGINEER *							
TEST PROCESS *							
HARDWARE RELEASE *		MASTER PROJECT TEMPLATE N/A	TESTER TEMPLATE N/A	DRAWING NO. 02-020284	REV. C		
DESIGNER M.Lewin	17MAR09	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		SIZE D	SCALE None	CODE ID NO. N/A	SHEET 1 OF 3
PTD ENGINEER A.Moore	17MAR09	TOLERANCES		DECIMALS X.XX +-0.010	FRACTIONS +-1/32	ANGLES +-2	
CHECKER *							

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

8 7 6 5 4 3 2 1

REVISIONS			
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DGND IS ACTUALLY THE SHIELD GND. IN THE LAB THIS IS TYPICALLY 0V. HOWEVER, IN A USER APPLICATION THIS MIGHT BE +3.3V. CONSEQUENTLY WE WOULD THEN REMOVE JUMPER P12.

TO OBSERVE THE DC COUPLED PERFORMANCE ON A SCOPE: REMOVE THE P12 JUMPER AND THEN REPLACE C1 -> C64 WITH 0 OHM JUMPERS YOU THEN NEED TO SHIFT VCC=0V AND VEE=-3.3V.

SCHEMATIC			
ANALOG DEVICES		HW TYPE : Customer Evaluation	
DESIGN VIEW		Product(s): ADN4604	
<DESIGN_VIEW>		DRAWING NO. 02-020284	
PTD ENGINEER		REV C	
A.Moore		SIZE D	
SCALE None		SHEET 2 OF 3	

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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

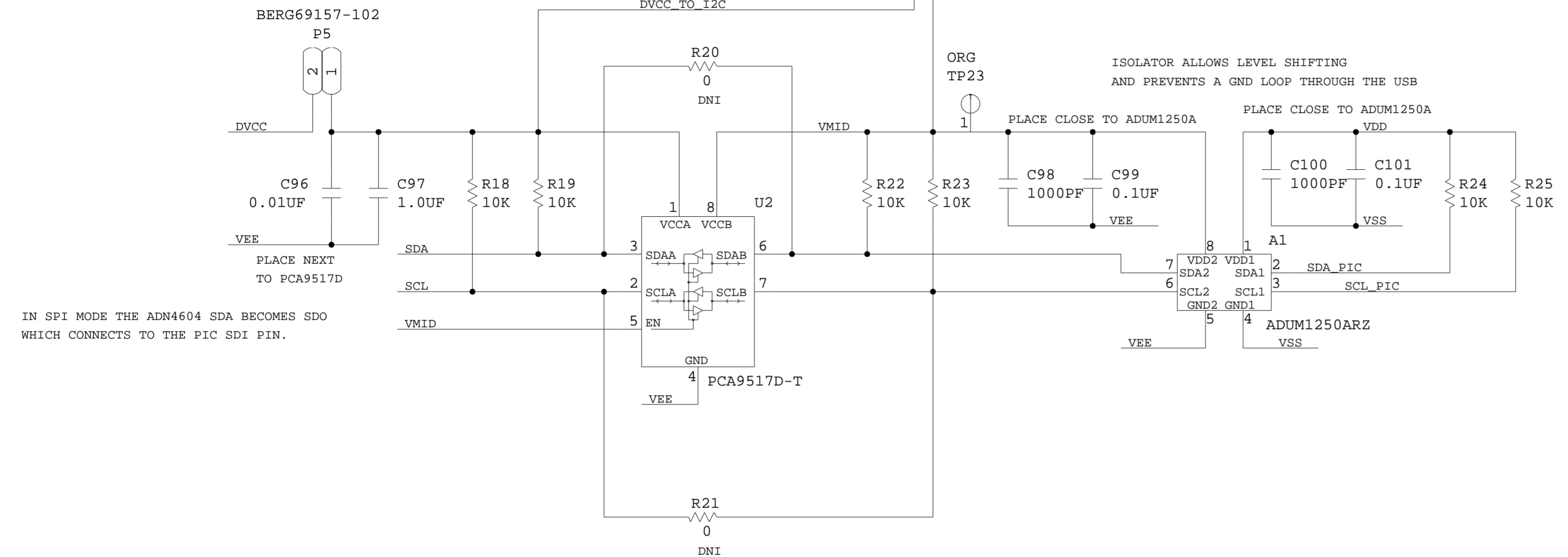
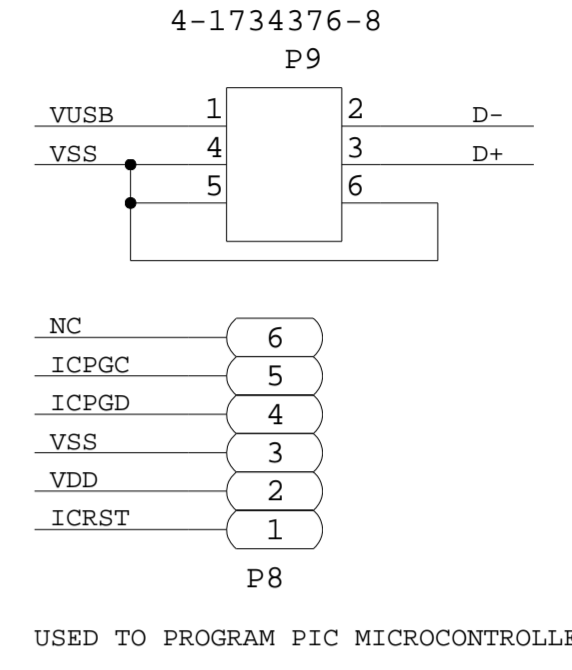
WHEN PCA9517D IS INSTALLED AND DVCC IS BELOW 2.7V
 YOU NEED TO DISCONNECT THE P6 JUMPER AND CONNECT PIN 1
 (VMID) TO A VOLTAGE BETWEEN +2.7V AND +5.5V.
 WHICH MAY NOT BE THE DUT VCC. OTHERWISE KEEP P6 CONNECTED AT ALL TIMES.

!! WARNING: DO NOT CONNECT THE USB CABLE TO P9 WHILE THE PIC CABLE IS CONNECTED TO P8 !!!
 !! WARNING: YOU MUST FIRST REMOVE THE PIC CABLE FROM P8 !!!

THIS ALLOWS THE USER TO SEPARATE THE I2C CIRCUIT FROM
 THE DUT DVCC, SO THIS SUPPLY CURRENT CAN BE MEASURED.
 THIS IS A SINGLE DISCONNECT, SCL AND SDA BECOME HIGH IMPEDANCE
 BY THIS WHEN POWER IS REMOVED (OR OFF).

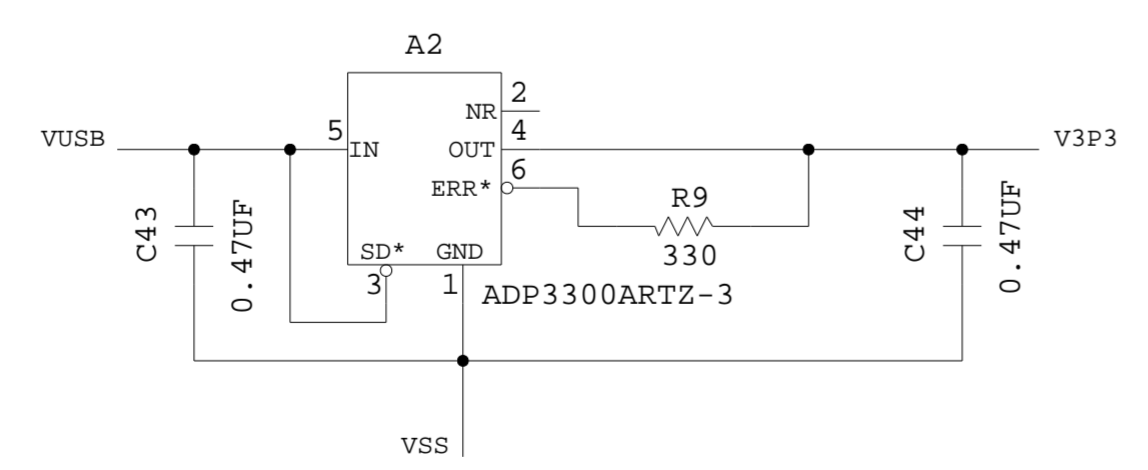
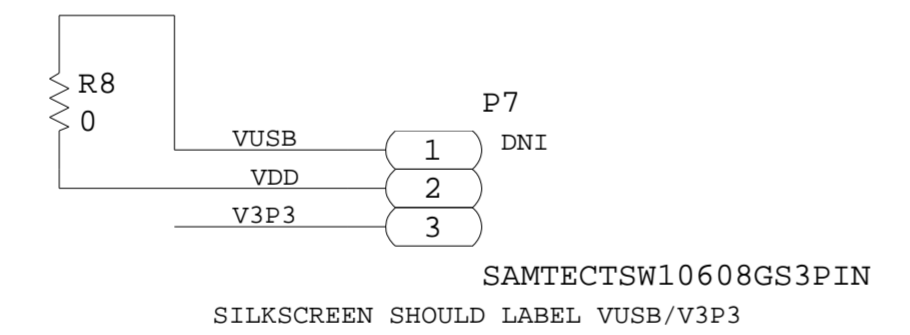
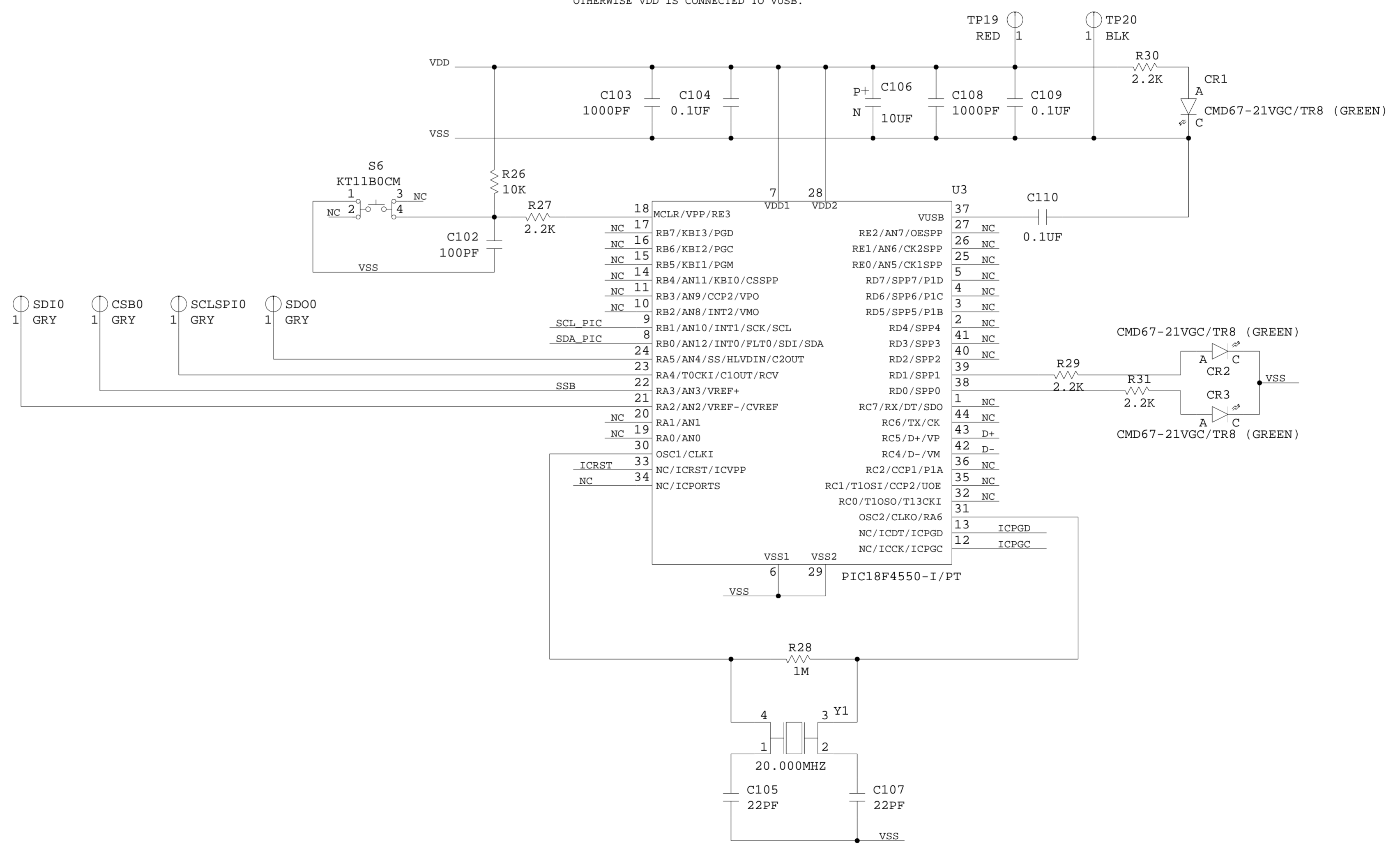
THIS IS DVCC THROUGH A JUMPER.
 DVCC TO I2C

USB CONNECTOR TO PC.



IN SPI MODE THE ADN4604 SDA BECOMES SDO
 WHICH CONNECTS TO THE PIC SDI PIN.

THIS PREVENTS ACCIDENTLY CONNECTING VUSB TO VFGM.
 WHEN PROGRAMMING THE PIC18F4550, VDD IS CONNECTED TO VFGM ONLY.
 OTHERWISE VDD IS CONNECTED TO VUSB.



	SCHEMATIC		
	HW TYPE : Customer Evaluation Product(s): ADN4604		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-020284	REV C
	PTD ENGINEER A.Moore	SIZE D	SCALE None
		SHEET 3 OF 3	

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