# RENESAS

# HD74HC123A

## Dual Retriggerable Monostable Multivibrators (with Clear)

REJ03D0564-0200 (Previous ADE-205-438) Rev.2.00 Oct 11, 2005

### Description

This multivibrator features both a negative, A, and a positive, B, transition triggered input, either of which can be used as an inhibit input. Also included is a clear input that when taken low resets the one shot. The HD74HC123A can be triggered on the positive transition of the clear while A is held low and B is held high.

The HD74HC123A is retriggerable. That is it may be triggered repeatedly while their outputs are generating a pulse and the pulse will be extended.

Pulse width stability over a wide range of temperature. The output pulse equation is simply: tw = (Rext) (Cext).

### Features

- High Speed Operation
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage:  $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC123AP	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Ρ	_
HD74HC123AFPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

### **Function Table**

	Inputs	Outputs			
Clear	Α	В	Q	Q	
L	Х	Х	L	Н	
Х	Н	Х	L	Н	
Х	Х	L	L	Н	
Н	L			T	
Н		Н		T	
	L	Н		T	

Note: External timing capacitance connects between Cext and Rext/Cext.

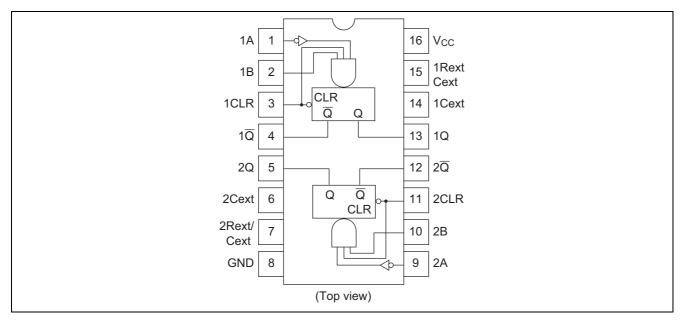
H: High level

L: Low level

X: Irrelevant



### **Pin Arrangement**



### **Absolute Maximum Ratings**

ltem	Symbol	Ratings	Unit	
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V	
Input / Output voltage	Vin, Vout	-0.5 to V <sub>CC</sub> +0.5	V	
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA	
Output current	lo	±25	mA	
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	
Power dissipation	PT	500	mW	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

### **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions	
Supply voltage	V <sub>CC</sub>	2 to 6	V		
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V		
Operating temperature	Та	-40 to 85	°C		
		0 to 1000		V <sub>CC</sub> = 2.0 V	
Input rise / fall time <sup>*1</sup>	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns	$V_{CC} = 4.5 V$	
		0 to 400		$V_{CC} = 6.0 V$	

Note: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.



## **Electrical Characteristics**

				Т	a = 25°	С	Ta = -40	to+85°C			
l I	tem	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Input voltag	je	V <sub>IH</sub>	2.0	1.5	_	_	1.5	—	V		
			4.5	3.15	_	—	3.15	—			
			6.0	4.2		_	4.2	_			
			2.0			0.5	_	0.5	V		
			4.5			1.35	_	1.35			
			6.0			1.8		1.8			
Output volta	age	V <sub>OH</sub>	2.0	1.9	2.0	—	1.9	—	V		I <sub>OH</sub> = -20 μA
			4.5	4.4	4.5	—	4.4	—		or V <sub>IL</sub>	
			6.0	5.9	6.0	—	5.9	—			
			4.5	4.18		_	4.13	_			$I_{OH} = -4 \text{ mA}$
			6.0	5.68		—	5.63	—			$I_{OH} = -5.2 \text{ mA}$
		V <sub>OL</sub>	2.0		0.0	0.1		0.1	V	$Vin = V_{IH}$	$I_{OL} = 20 \ \mu A$
			4.5		0.0	0.1		0.1		or V <sub>IL</sub>	
			6.0		0.0	0.1		0.1			
			4.5			0.26	_	0.33			$I_{OL} = 4 \text{ mA}$
			6.0			0.26		0.33			$I_{OL} = 5.2 \text{ mA}$
Input current		lin	6.0			±0.1	—	±1.0	μΑ	$Vin=V_{CC}$	or GND
Quiescent	Standby state	I <sub>CC</sub>	6.0			130	_	220	μΑ	$Vin = V_{CC}$	lout = 0 $\mu$ A
supply current	Active state		6.0	_	_	130	—	220		or GND	Rext/Cext = 0.5 V <sub>CC</sub>

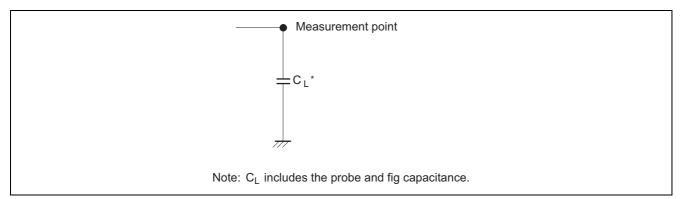


			-								
			Т	a = 25°	C	Ta = –40	to +85°C				
ltem	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions		
Propagation delay	t <sub>PLH</sub>	2.0	_	_	210	_	265	ns	A, B or Clear to Q		
time		4.5	_	22	42		53				
		6.0		—	36		45				
	t <sub>PHL</sub>	2.0	_	—	240		300	ns	A, B or Clear to	Q	
		4.5	_	23	48		60				
		6.0	_	—	41		51				
	t <sub>PHL</sub>	2.0	_	—	170		215	ns	Clear to Q		
		4.5		18	34		43				
		6.0		—	29		37				
	t <sub>PLH</sub>	2.0	_	—	180		225	ns	Clear to Q		
		4.5	_	16	36		45				
		6.0	_	—	31		38				
Output rise/fall	t <sub>TLH</sub> , t <sub>THL</sub>	2.0	_	—	75		95	ns			
time		4.5	_	5	15		19				
		6.0	_	—	13		16				
Pulse width	t <sub>w</sub>	2.0	150	—	—	190	—	ns	A, B, Clear		
		4.5	30	6	—	38	—				
		6.0	26	—	—	33	—				
Minimum output	t <sub>wQ(min)</sub>	2.0		1.5	—			μs	Cext = 28 pF	Rext = $6 \text{ k}\Omega$	
pulse width		4.5		450	—		—	ns		Rext = $2 k\Omega$	
		6.0		380	—		—				
Output pulse width	t <sub>wQ</sub>	4.5		1.0	—		—	ms	Cext = 0.1 $\mu$ F, Rext = 10 k $\Omega$		
Input capacitance	Cin	_	_	5	10		10	pF			

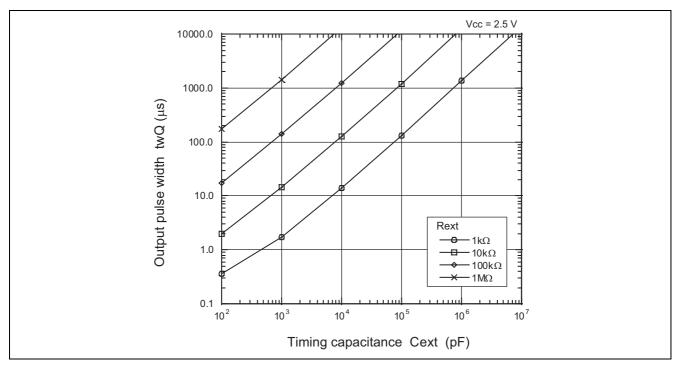
### Switching Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

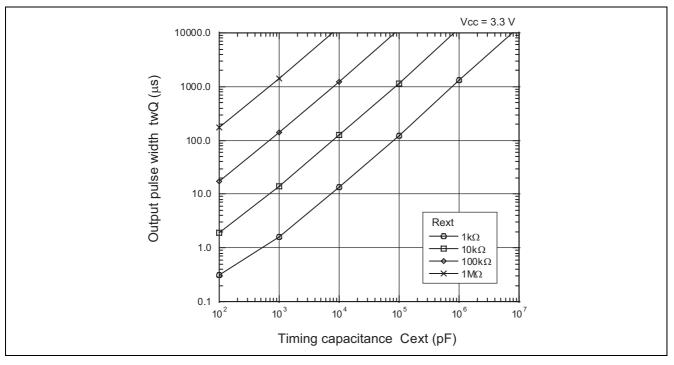
Caution in use: In order to prevent any malfunctions due to noise, connect a high-frequency performance capacitor between V<sub>CC</sub> and GND, and keep the wiring between the External components and Cext, Rext/Cext pins as short as possible.

### **Test Circuit**

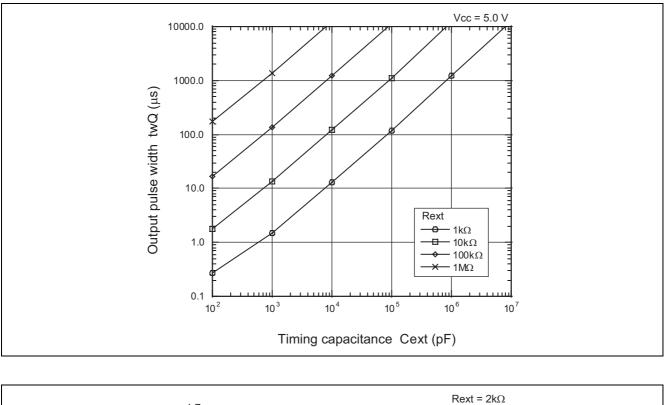


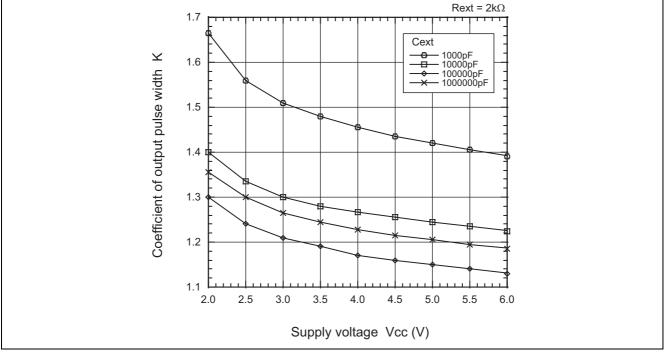
### **Application Data**



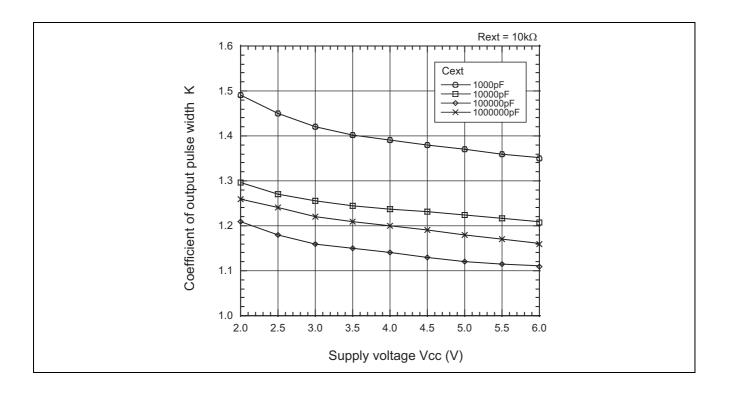






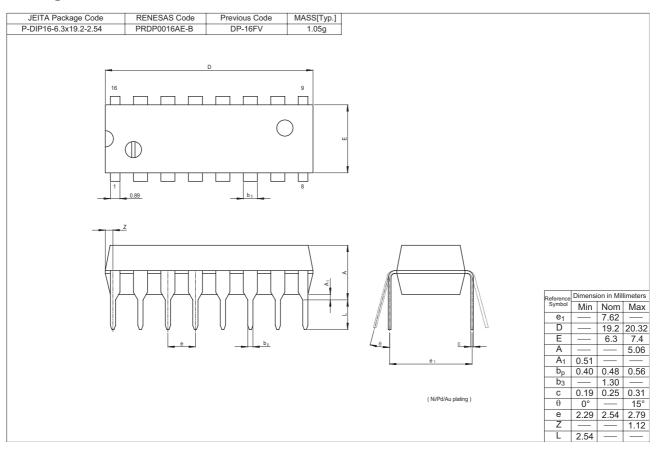


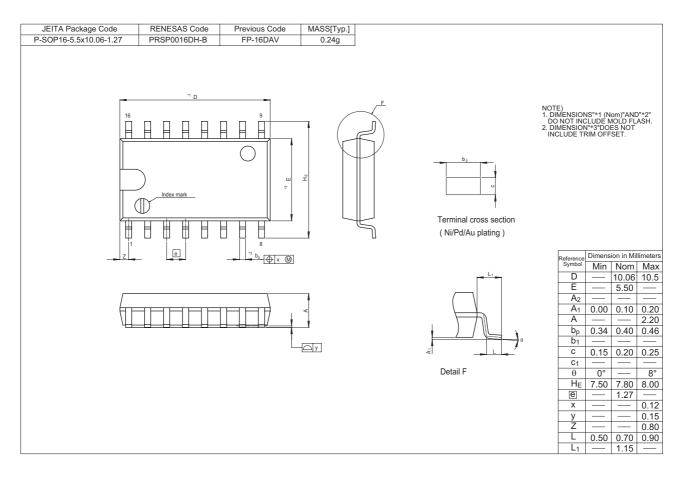






### **Package Dimensions**







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