

Product Overview

MC14050B: Hex Buffer

For complete documentation, see the data sheet.

The MC14049B Hex Inverter/Buffer and MC14050B Noninverting Hex Buffer are constructed with MOS P-Channel and N-Channel enhancement mode devices in a single monolithic structure. These complementary MOS devices find primary use where low power dissipation and/or high noise immunity is desired. These devices provide logic level conversion using only one supply voltage, V_{DD} .

The input-signal high level (V_{IH}) can exceed the V_{DD} supply voltage for logic level conversions. Two TTL/DTL loads can be driven when the devices are used as a CMOS-to-TTL/DTL converter ($V_{DD} = 5.0\text{ V}$, $V_{OL} \leq 0.4\text{ V}$, $I_{OL} \geq 3.2\text{ mA}$).

Note that pins 13 and 16 are not connected internally on these devices; consequently connections to these terminals will not affect circuit operation.

Features

- High Source and Sink Currents
- High-to-Low Level Converter
- Supply Voltage Range = 3.0 V to 18 V
- V_{IN} can exceed V_{DD}
- Meets JEDEC B Specifications
- Improved ESD Protection On All Inputs
- Pb-Free Packages are Available*

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_o Max (mA)	Package Type
MC14050BDG	Pb-free	Active	6	CMOS	3	18	80	16	SOIC-16
	Halide free								
MC14050BDR2G	Pb-free	Active	6	CMOS	3	18	80	16	SOIC-16
	Halide free								
MC14050BDTG	Pb-free	Active	6	CMOS	3	18	80	16	TSSOP-16
	Halide free								
MC14050BDTR2G	Pb-free	Active	6	CMOS	3	18	80	16	TSSOP-16
	Halide free								
NLV14050BDG	AEC Qualified	Active	6	CMOS	3	18	80	16	SOIC-16
	PPAP Capable								
	Pb-free								
	Halide free								
NLV14050BDR2G	AEC Qualified	Active	6	CMOS	3	18	80	16	SOIC-16
	PPAP Capable								
	Pb-free								
	Halide free								
NLV14050BDTG	AEC Qualified	Active	6	CMOS	3	18	80	16	TSSOP-16
	PPAP Capable								
	Pb-free								
	Halide free								

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