

Speaker /Headphone Amplifiers Series

1.1W to1.5W





Monaural Speaker Amplifiers

BH7824FVM, BH7826FVM

Description

The BH7824FVM and BH7826FVM are speaker amplifier ICs for low-voltage drives and low power consumption audio, specialized for mobile telephones and other mobile audio devices.

Features

- 1) BTL monaural speaker amplifier
- 2) Capable of high power 500mW/8Ω/BTL output
- 3) Wide power supply voltage range
- 4) Supports active/shutdown modes
- 5) Built-in anti-pop circuit
- Built-in thermal shutdown circuit

Applications

Mobile telephones, PDAs, notebook computers, DSC, DVC, and other mobile audio devices.

Product lineup

Part No.	BH7824FVM	BH7826FVM
Input type	Unbalanced input	Balanced input
Supply voltage(V)	2.4~5.5	2.6~5.5

• Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	Vccmax.	6.0	V
Power dissipation	Pd	470(*1)	mW
Operating temperature	Topr	-30 ~ +85(*2)	°C
Storage temperature	Tstg	-55~+125	°C

^{*1} Reduced by 4.7 mW/°C at 25°C or higher, when mounted on a 70mm×70mm×1.6mm PCB board.

^{*2} Topr=70°C~85°C is the range for performing basic operations and does not guarantee characteristics or rated output.

Moreover, TSD (Thermal Shutdown) may become operable if input signals occurring in this range are excessive.

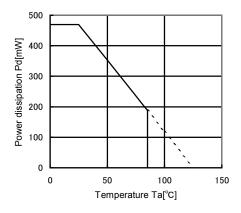


Fig.1 Ta-Pd (when mounted on a PCB board)

•Operating range (Ta=25°C)

Part No.	Part No. BH7824FVM	
Supply voltage	2.4V~5.5V	2.6~5.5

^{*} Note: This IC is not designed to be radiation-resistant.

•Electrical characteristics (Unless otherwise noted Ta=25°C, Vcc=3.6V, f=1kHz, RL=8Ω)

Devementer	Curahal	Тур.		l lmit	Conditions	
Parameter	Symbol	BA7824FVM	BA7826FVM	Unit	Conditions	
Circuit current 1	ICC1	3.5	3.5	mA	No signal Active mode	
Circuit current 2	ICC2	0	0	μΑ	No signal Suspend mode	
Voltage gain 1	GV1	+11.5	+11.5	dB	VIN=-20dBV,1st Opamp gain	
Voltage gain 2	GV2	0		dB	2nd Opamp gain	
Maximum output voltage1	Vom1	+6.0	+6.0	dBV	DSTN=1% BTL *1	
Maximum output voltage2	Vom2	_	+5.1	dBV	Vcc=3.4V,DSTN=1%,BTL *1	
Output distortion	Dstn	0.07	0.2	%	VIN=-20dBV SE *1	
Output noise level	Vno	-94	-94	dBV	No signal, SE Active mode *2	
Suspend attenuation	Gs	-107	-107	dBV	VIN=-20dBV BTL *2	
Bias setting voltage	VBIAS	1.8	1.8	V	3pin DC voltage	
Suspend hold voltage/H	VsH1	Vcc×0.8~Vcc	2.0~Vcc	V	Active mode, Hold voltage	
Suspend hold voltage/L	VsH2	0~0.5	0~0.5	V	Suspend mode, Hold voltage	

^{*1 :} B.W.=0.4~30kHz

^{*2 :} DIN AUDIO

•Measurement circuit

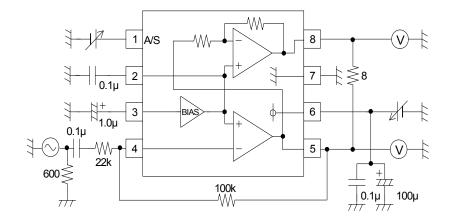
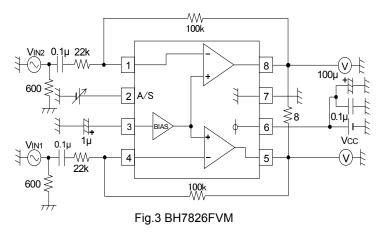


Fig.2 BH7824FVM



•Block diagram

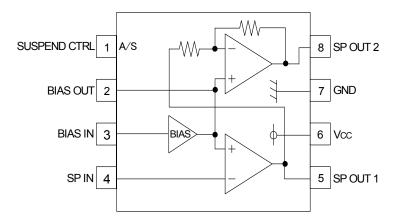


Fig.4 BH7824FVM

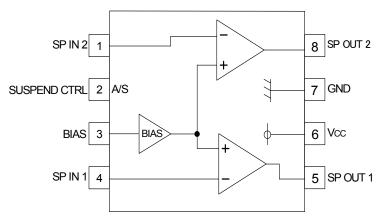


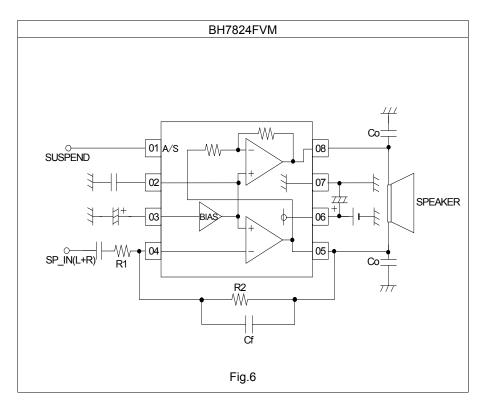
Fig.5 BH7826FVM

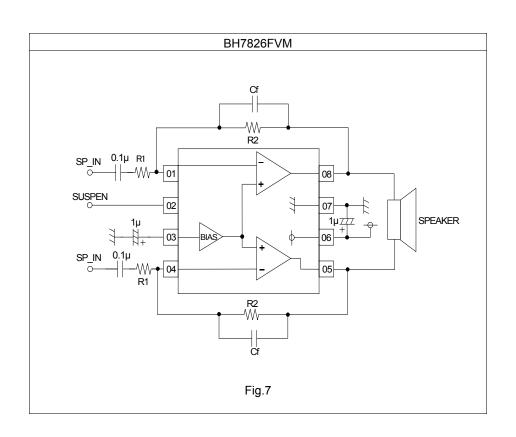
•Equivalent circuit

Pin	BH7824FVM		BH7826FVM	
No.	Pin name	Equivalent circuit	Pin name	Equivalent circuit
1	SUSPEND CTRL	01 \$50k	SUSPEND CTRL	
2	BIAS OUT	30k 487k 02	BIAS OUT	02
3	BIAS IN	30k 57k 03	BIAS IN	30k 150k 03
4	SP IN		SP IN	——————————————————————————————————————
5	SP OUT1	05	SP OUT1	05
6	Vcc	-	Vcc	_
7	GND	_	GND	_
8	SP OUT2		SP OUT2	1E 08

Note: Numerical values in figures are design values, and do not guarantee ratings.

Application circuit





Cautions on use

1) Set within the ranges below.

Item	8, 12Ωspeaker	16Ωspeaker	
item	BH7824FVM/26FVM	BH7824FVM	
R1	10kΩ ≦ R1 ≦120kΩ	←	
R2	$47kΩ \le R1 \le 120kΩ$	←	
Cf	Cf≦560pF (G>+6dB)	,	
	Cf≦270pF(0 <g≦+6db)< td=""><td>_</td></g≦+6db)<>	_	
Co	Co ≦ 100pF	Co ≦ 47pF	
fcutoff	fcutoff ≤ 4kHz	_	
	fcutoff = $1/(2 \pi R_2 Cf)$ [Hz]	_	

- 2) Do not use this IC without a load.
- 3) Do not connect a capacitive load greater than 100 pF between an output pin (pin 5 or 8) and GND.

 Because of the multiple feedback configuration, if a large capacitive load were connected, the amplifier might oscillate.
- 4) This IC is compatible with dynamic speaker loads (8/12/16 Ω : BH7824FVM, 8/12 Ω : BH7826FVM) and is not compatible with loads other than these mentioned.
- 5) A soft mute setting becomes effective on connecting a resistor and capacitor to the SUSPEND pin (1pin: BH7824FVM, 2pin: BH7826FVM).

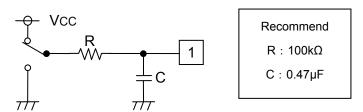


Fig.8 BH7824FVM Soft mute

•Reference data

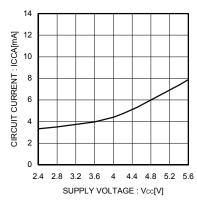


Fig.9 Circuit current (ACTIVE) vs Supply voltage

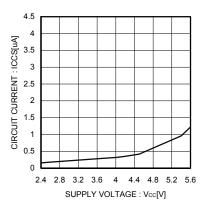


Fig.10 Circuit current (SUSPEND) vs Supply voltage

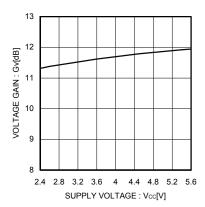


Fig.11 Voltage gain vs Supply voltage

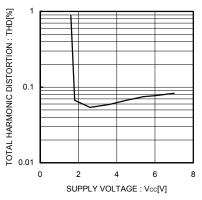


Fig.12 Total harmonic distortion vs Supply voltage

Operation Notes

- 1. Numbers and data in entries are representative design values and are not guaranteed values of the items.
- 2. Although ROHM is confident that the example application circuit reflects the best possible recommendations, be sure to verify circuit characteristics for your particular application. Modification of constants for other externally connected circuits may cause variations in both static and transient characteristics for external components as well as this Rohm IC. Allow for sufficient margins when determining circuit constants.
- 3. Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings, such as the applied voltage or operating temperature range (Topr), may result in IC damage. Assumptions should not be made regarding the state of the IC (short mode or open mode) when such damage is suffered. A physical safety measure, such as a fuse, should be implemented when using the IC at times where the absolute maximum ratings may be exceeded.

4. GND potential

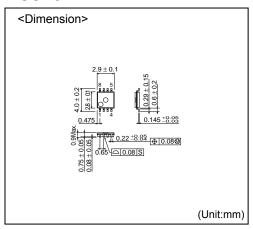
Ensure a minimum GND pin potential in all operating conditions. Make sure that no pins are at a voltage below the GND at any time, regardless of whether it is a transient signal or not.

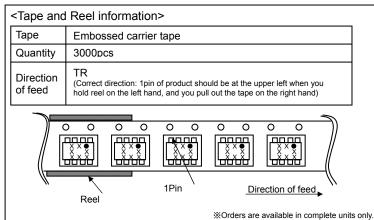
- 5. Thermal design
 - Perform thermal design, in which there are adequate margins, by taking into account the permissible dissipation (Pd) in actual states of use.
- 6. Short circuit between terminals and erroneous mounting Pay attention to the assembly direction of the ICs. Wrong mounting direction or shorts between terminals, GND, or other components on the circuits, can damage the IC.
- 7. Operation in strong electromagnetic field
 Using the ICs in a strong electromagnetic field can cause operation malfunction.

Selection of order type



MSOP8





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