# QUICK START GUIDE

EVAL\_AUDIO\_MA12070

Class D Audio Team March 2019



### Product overview and features



#### Overview

The demonstration board <u>EVAL\_AUDIO\_MA12070</u> is an evaluation and demonstration board for MERUS™ audio <u>MA12070</u>. It contains analog inputs and a variety of output and setup/selection features. It also contains two on-board power supply generators (5 V and 3.3 V buck-converted) so only one external power supply (PVDD) is necessary. The board can be used for evaluating or demonstrating key features/advantages of the MERUS™ technology:

- > Energy efficiency: Power losses at typical audio listening levels / Idle power loss
- > Adaptive power management system
- > Minimum output filter components: Significant cost and size reduction.
- > THD performance and audio quality

## General features and audio performance

Number of audio channels	2xBTL, 1xPBTL, 1xBTL+2xSE
Audio input format	Analog
Amplifier gain	20 dB / configurable 26dB
Supply voltage	26 V
Output noise level	<100 uVrms(AW)
Dynamic range	>100 dB
Idle consumption @ PVDD=18V	<16 mA
Crosstalk	<-85 dB
Efficiency, full-scale, 8 ohm	91%

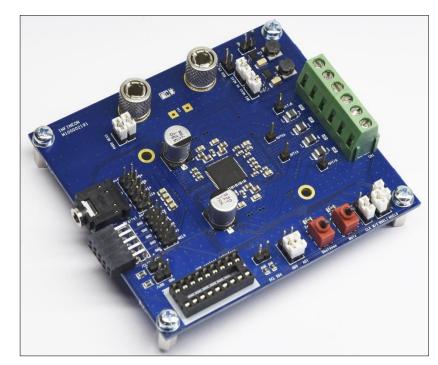


Figure 1. Overview of EVAL\_AUDIO\_MA12070 evaluation board

# **Board description**



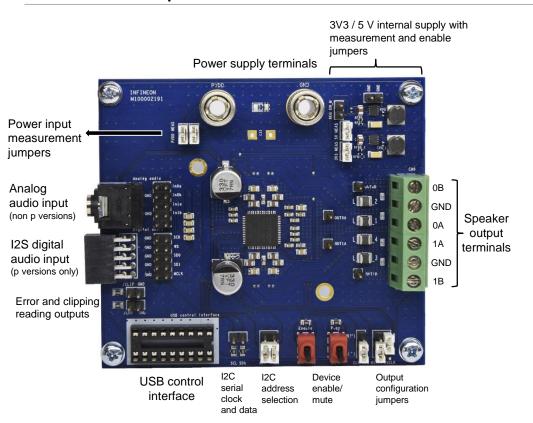


Figure 2. Top board view of EVAL AUDIO MA12040

#### Recommended operation conditions

Parameter	Part Nr	Minimum	Nomina I	Maximum	Unit
PVDD	MA12040	5.5		18	V
Output peak current	MA12040			6.0	Α
AC Analog input level IN0A, IN0B, IN1A, IN1B	MA12040			6.0	Vpk

#### Typical audio and electrical specifications

(BTL default configuration; Power Mode Profile = 0)

Parameter	Conditions	Тур	Unit
Output power p/channel (peak)	THD+N = 10%, RL = $4\Omega$ , f = 1kHz	40	W
Output power p/channel (peak)	THD+N = 10%, RL = $8\Omega$ , f = 1kHz	20	W
Total harmonic distortion + noise	1kHz, POUT = 1W, RL = $4\Omega$	0.008	%
Total harmonic distortion + noise	1kHz, POUT = 20W, RL = $4\Omega$	0.010	%
Efficiency*	POUT = $2 \times 40$ W, $4\Omega$ , PMP = $0$	87	%
Efficiency*	POUT = $2 \times 20$ W, $8\Omega$ , PMP = $0$	91	%

<sup>\*</sup> Efficiency values do not take into account the 5 V and 3V3 board power supplies' consumption.

# Default configuration for a quick start



Slides 4 and 5 describe the start up and quick start operation procedures with the following configuration:

# Single ended audio sources Volto Relation Relat

Figure 3. Bridge tied load (BTL) configuration with single ended inputs for MA12070

**Note:** Please refer to the manual for other input/output configurations. Typically balanced/differential inputs should be used to obtain optimum audio performance.

#### Verify that the jumpers are set in the following positions:

Jumper	State	Picture
MSELO	Н	
MSEL1	L	
CLK M/S	H(M)	CER A/J MJEEL MJEEU
AD0 & AD1	L	A00 A01
Analog audio	Jack input = all jumpered Balanced input = use individual pints.	
Digital audio	Do not Jumper	70 00 00 00 00 00 00 00 00 00 00 00 00 0
PVDD MEAS	Jumpered	PVDD MEAS
3V3 & 5V MEAS	Jumpered	SA S
REG EN_N	Do not Jumper	N AS SYDIN EACH AND

# Power and start-up procedure



Although it is not necessary to configure the MA12070 for quick start purposes, the following slides describe also the installation process of its software tool. This will allow to monitor and configure the power mode profiles. Power mode profile 0 is configured by default.

- Before connecting any source or load (speaker) make sure all of them are turned off.
- Open your web browser and download the USB control interface drivers from the following <u>Link</u>. Press "setup executable" to download the automatic installation. Run the downloaded file "CDM21228\_Setup" and follow the instructions to install the driver. Do not connect the USB cable while the installing process in running.

Comments

WHQL Certified. Includes VCP and D2XX

Available as setup executable. Please read the Release Notes and Installation Guides.

Figure 4. File download for USB control interface drivers



Figure 5. File download for GUI software

 Make sure toggle buttons are in "shutdown" and "mute" positions.



Figure 6. Toggle buttons set to "mute" and "shutdown"

- Connect all the sources and speaker/load cables:
  - a) Audio source to the analog audio jack input
  - b) Power source and its ground to PVDD and GND
  - c) Speaker/load to audio output terminals
  - d) USB control interface to the computer.

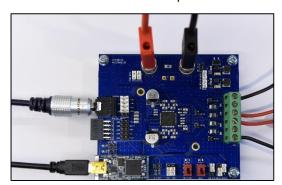


Figure 7. Complete evaluation board connections.

## Power and start-up procedure



- 6. Turn on the PVDD supply
- 7. Start board by setting toggle switch to "enable" position.



Figure 8. Left toggle switch set to "enable" position.

Open the "MA120xx\_GUI" file to run the monitoring interface.



Figure 9. MA120xx control software interface

If the connection was successful it will be indicated in the ID status line:

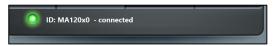


Figure 10. Control interface indicating a successful communication with the board.

10. Start playing music from your audio source and set the toggle switch to "play" position. You will be able to monitor the modulation index of both channels and the their current power modes. Also, the power mode profiles can be selected. For more information on power mode profiles please refer to the MA12070 data sheet and Infineon MERUS™ evaluation board user's guide.



Figure 11. Right toggle switch set to "play" position.

# Audio measurements set up



#### The need for an external filter:

MA120xx and MA120xxP are filterless amplifiers enabled by it's MERUS™ audio multilevel technique. However, in order to obtain reliable measurements results an external low-pass filter is required in front of the input stage. This is because fast transients in the switching output signal might stress the bandlimited input of measurement instruments. Therefore, slew rate limiting and other distortion artifacts may appear due to this stress if no external filtering is applied.

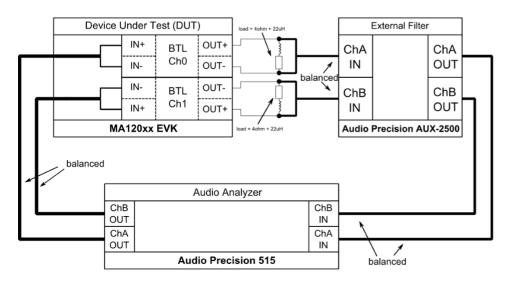


Figure 12. Measurement setup for MA120xx amplifiers.

# Support

## www.infineon.com/merus



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#### Technical material

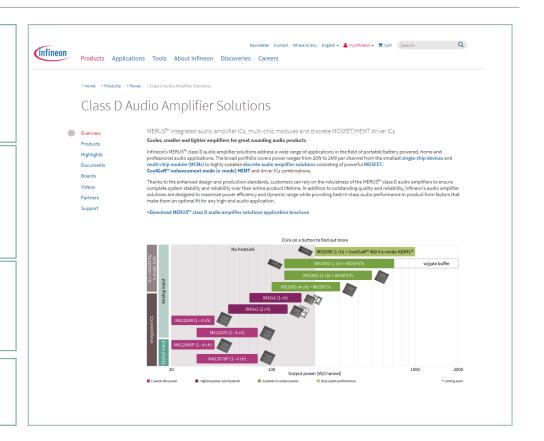
- Application notes
- Whitepapers
- Datasheets, MCDS files
- PCB design data

#### **Evaluation boards**

- Evaluation boards
- Demoboards
- Reference designs

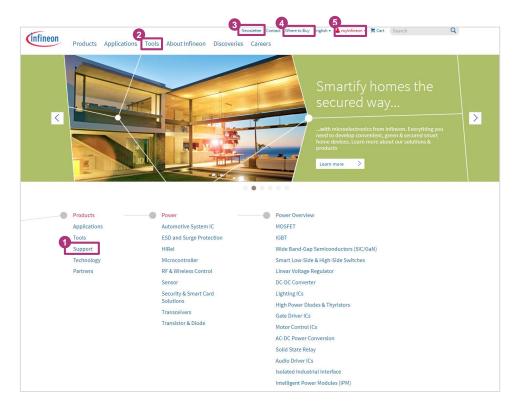
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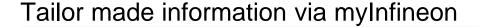


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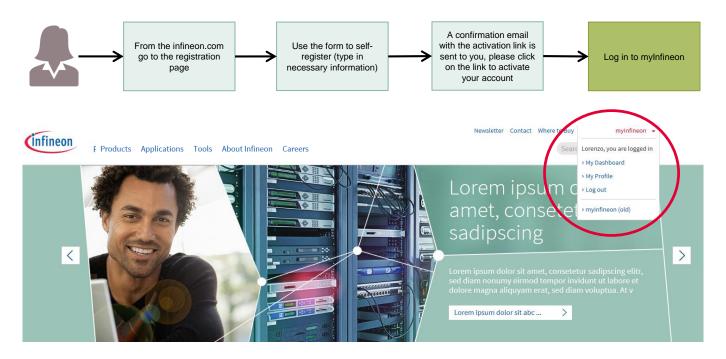












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