



SIMATIC ET 200SP HA, analog input module, safety-oriented, F-AI 8x1 2-wire/4-wire HART HA, 16-bit, 2-wire/4-wire, SIL3 (IEC 61508), up to PL e (ISO 13849-1), suitable for terminal block H1, F1, color code CC00, channel diagnostics

General information	
Product type designation	F-AI 8x1 2-/4-wire HART HA
Firmware version	V1.0
<ul style="list-style-type: none"> <li>FW update possible</li> </ul>	Yes
Color code for module-specific color-coded label	CC00
Product function	
<ul style="list-style-type: none"> <li>I&amp;M data</li> </ul>	Yes; I&M0 to I&M3
Engineering with	
<ul style="list-style-type: none"> <li>STEP 7 configurable/integrated from version</li> <li>PCS 7 configurable/integrated from version</li> <li>PROFINET from GSD version/GSD revision</li> </ul>	V5.6 SP2 (with S7 F Systems V6.4) V9.0 SP3 + UC04 (with F Systems V6.4) GSDML V2.42 2023.01
Redundancy	
<ul style="list-style-type: none"> <li>Redundancy capability</li> </ul>	Yes; with TB type F1
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Input current	
Current consumption (rated value)	90 mA; without sensor supply
Encoder supply	
Number of outputs	8
Short-circuit protection	Yes
24 V encoder supply	
<ul style="list-style-type: none"> <li>24 V</li> <li>Short-circuit protection</li> <li>Output current per channel, max.</li> </ul>	Yes Yes 30 mA
Power	
Power consumption from the backplane bus	90 mW
Power loss	
Power loss, typ.	2.8 W
Address area	
Address space per module	
<ul style="list-style-type: none"> <li>Inputs</li> <li>Outputs</li> </ul>	22 byte 5 byte
Analog inputs	

Number of analog inputs	
<ul style="list-style-type: none"> <li>For current measurement</li> </ul>	8
permissible input current for current input (destruction limit), max.	35 mA
<b>Input ranges (rated values), currents</b>	
<ul style="list-style-type: none"> <li>0 to 20 mA</li> <li>— Input resistance (0 to 20 mA)</li> </ul>	Yes 150 Ω
<ul style="list-style-type: none"> <li>4 mA to 20 mA</li> <li>— Input resistance (4 mA to 20 mA)</li> </ul>	Yes 150 Ω
<b>HART communication</b>	
<ul style="list-style-type: none"> <li>Primary Master</li> </ul>	Yes
<ul style="list-style-type: none"> <li>Secondary Master</li> </ul>	No
<ul style="list-style-type: none"> <li>input resistance (with HART communication)</li> </ul>	150 Ω; for operation with an external secondary master (e.g. communicator), an external load may be necessary to achieve a total impedance of 230 - 600 Ω.
<b>Cable length</b>	
<ul style="list-style-type: none"> <li>shielded, max.</li> </ul>	1 000 m; shielded, twisted pair
<b>Analog value generation for the inputs</b>	
Measurement principle	Sigma Delta
<b>Integration and conversion time/resolution per channel</b>	
<ul style="list-style-type: none"> <li>Resolution with overrange (bit including sign), max.</li> </ul>	16 bit
<ul style="list-style-type: none"> <li>Integration time, parameterizable</li> </ul>	Yes
<ul style="list-style-type: none"> <li>Integration time (ms)</li> </ul>	20 ms (at 50 Hz); 16.66 ms (at 60 Hz)
<ul style="list-style-type: none"> <li>Interference voltage suppression for interference frequency <math>f_1</math> in Hz</li> </ul>	50 / 60 Hz
<b>Smoothing of measured values</b>	
<ul style="list-style-type: none"> <li>parameterizable</li> </ul>	Yes; in 4 stages (1, 4, 16, 64 conversion cycles), channel-by-channel
<b>Encoder</b>	
<b>Connection of signal encoders</b>	
<ul style="list-style-type: none"> <li>for current measurement as 2-wire transducer</li> </ul>	Yes
<ul style="list-style-type: none"> <li>for current measurement as 4-wire transducer</li> </ul>	Yes
<b>Errors/accuracies</b>	
Crosstalk between the inputs, min.	-70 dB
Repeat accuracy in steady state at 25 °C (relative to input range), (+/-)	0.008 %
safety-relevant accuracy	
<ul style="list-style-type: none"> <li>up to 40 °C, max.</li> </ul>	0.6 %; (0.7% in vertical installation)
<ul style="list-style-type: none"> <li>up to 70 °C, max.</li> </ul>	0.9 %
note regarding accuracy	the safety-relevant accuracy consists of a basic error, a temperature-dependent drift, aging and internal safety measures
<b>Influence of a HART signal modulated on the input signal in relation to input range</b>	
<ul style="list-style-type: none"> <li>error at 16.6 ms integration time</li> </ul>	0.11 %
<ul style="list-style-type: none"> <li>error at 20 ms integration time</li> </ul>	0.11 %
<b>Interference voltage suppression for <math>f = n \times (f_1 \pm 1 \%)</math>, <math>f_1 =</math> interference frequency</b>	
<ul style="list-style-type: none"> <li>Series mode interference (peak value of interference &lt; rated value of input range), min.</li> </ul>	40 dB
<ul style="list-style-type: none"> <li>Common mode voltage, max.</li> </ul>	35 V
<ul style="list-style-type: none"> <li>Common mode interference, min.</li> </ul>	80 dB
<b>Protocols</b>	
HART protocol	Yes
<ul style="list-style-type: none"> <li>Protocol version</li> </ul>	up to Revision 7
<b>Interrupts/diagnostics/status information</b>	
<b>Alarms</b>	
<ul style="list-style-type: none"> <li>Diagnostic alarm</li> </ul>	Yes
<b>Diagnoses</b>	
<ul style="list-style-type: none"> <li>Monitoring the supply voltage</li> </ul>	Yes
<ul style="list-style-type: none"> <li>Wire-break</li> </ul>	Yes
<ul style="list-style-type: none"> <li>Short-circuit</li> </ul>	Yes
<ul style="list-style-type: none"> <li>Overflow/underflow</li> </ul>	Yes
<b>Diagnostics indication LED</b>	
<ul style="list-style-type: none"> <li>MAINT LED</li> </ul>	Yes; Yellow LED

- Monitoring of the supply voltage (PWR-LED) Yes; green PWR LED
- Channel status display Yes; green LED
- for channel diagnostics Yes; red LED
- for module diagnostics Yes; green/red DIAG LED

#### Potential separation

##### Potential separation channels

- between the channels No
- between the channels and backplane bus Yes
- Between the channels and load voltage L+ No

#### Permissible potential difference

between the inputs (UCM) 30 V DC / 25 V AC

#### Isolation

##### tested with

- between backplane bus and load voltage 1 500 V DC (load voltage L+ and channels I+n bridged)
- between the backplane bus and functional ground (FE) 1 500 V DC
- between load voltage and functional ground (FE) 1 500 V DC (load voltage L+ and channels I+n bridged)
- between the channels and load voltage 370 V AC
- between the potential groups of the channels 370 V AC

#### Standards, approvals, certificates

##### Highest safety class achievable in safety mode

- Performance level according to ISO 13849-1 PLd (PLe for 1oo2 voting on the F-CPU)
- Category according to ISO 13849-1 cat. 3 (cat. 4 for 1oo2 voting on the F-CPU)
- SIL acc. to IEC 61508 SIL 3

##### Probability of failure (for service life of 20 years and repair time of 100 hours)

- Low demand mode: PFDavg in accordance with SIL3 < 27E-05 (< 9E-05 for 1oo2 voting on the F-CPU)
- High demand/continuous mode: PFH in accordance with SIL3 < 4E-09 1/h (< 1E-09 1/h for 1oo2 voting on the F-CPU)

#### Ambient conditions

##### Ambient temperature during operation

- horizontal installation, min. -40 °C
- horizontal installation, max. 70 °C
- vertical installation, min. -40 °C
- vertical installation, max. 60 °C

#### Dimensions

Width 22.5 mm  
 Height 115 mm  
 Depth 138 mm

#### Weights

Weight, approx. 220 g

#### Classifications

	Version	Classification
eClass	14	27-24-26-01
eClass	12	27-24-26-01
eClass	9.1	27-24-26-01
eClass	9	27-24-26-01
eClass	8	27-24-26-01
eClass	7.1	27-24-26-01
eClass	6	27-24-26-01
ETIM	10	EC001596
ETIM	9	EC001596
ETIM	8	EC001596
ETIM	7	EC001596
IDEA	4	3562
UNSPSC	15	32-15-17-05

#### Approvals / Certificates

General Product Approval



For use in hazardous locations

[Declaration of Conformity](#)



[Miscellaneous](#)

Functional Safety

[TUEV](#)

[Type Examination Certificate](#)

[TUEV](#)

Maritime application



Maritime application



[NK / Nippon Kaiji Kyokai](#)



[CCS \(China Classification Society\)](#)

Environment



Industrial Communication

[PROIsafe](#)

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