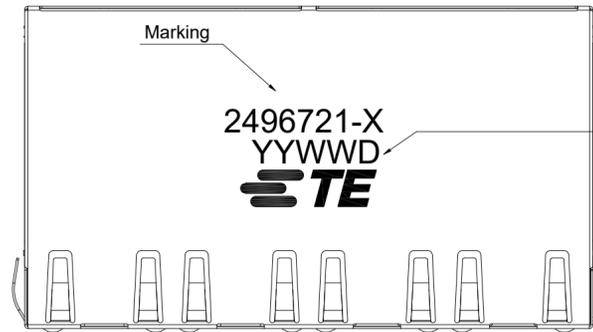


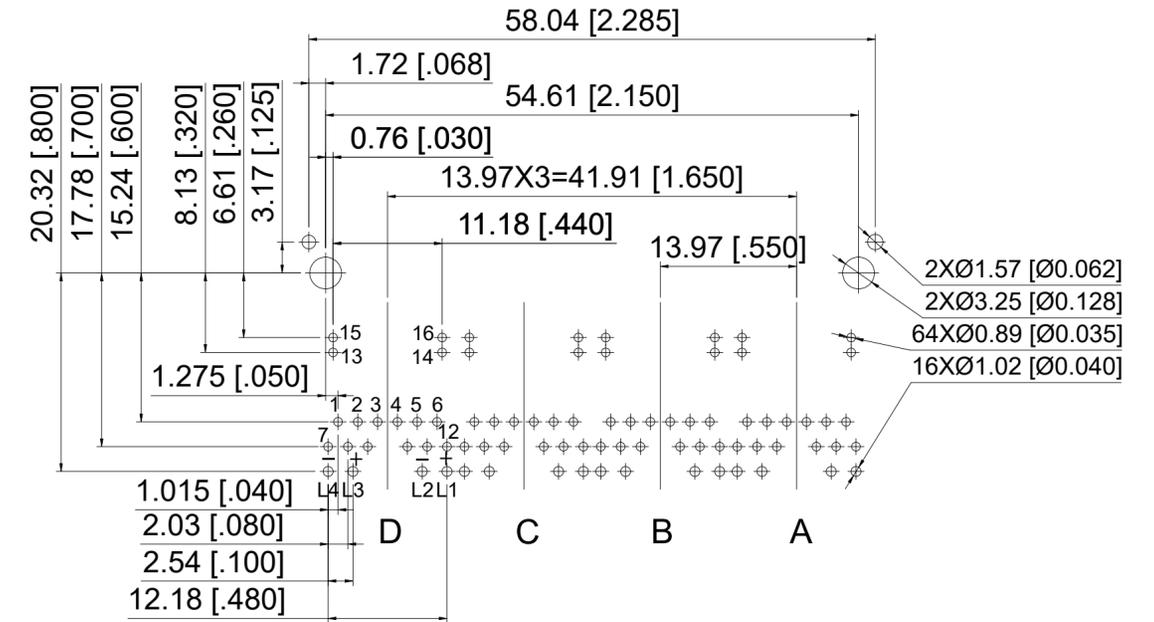
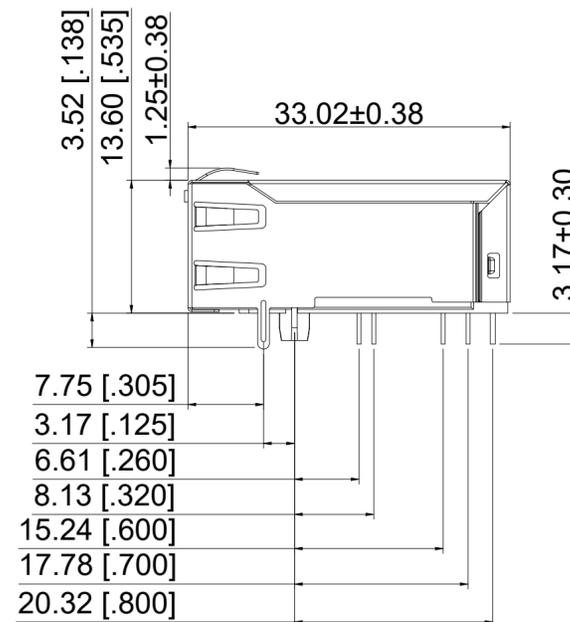
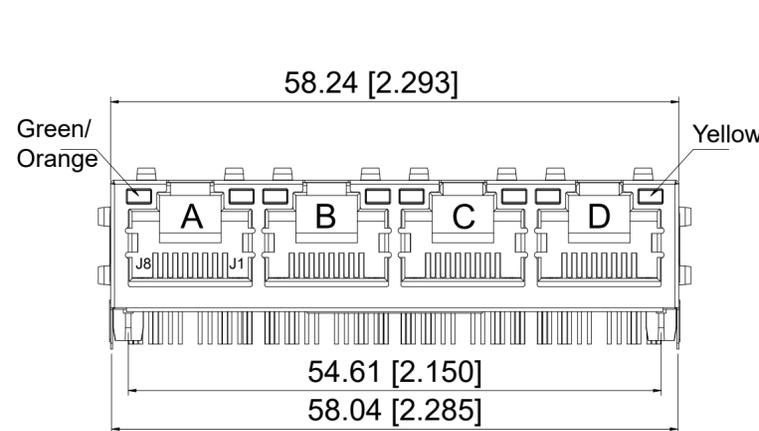
REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
A		REVISED PER ECN-25-301496	10JAN2025	TR	RCC



Note:
 X= 1,2,3,4,5,6
 YYWWD
 YY=YEAR
 WW=WEEK
 D=1 for Sunday - 7 for Saturday

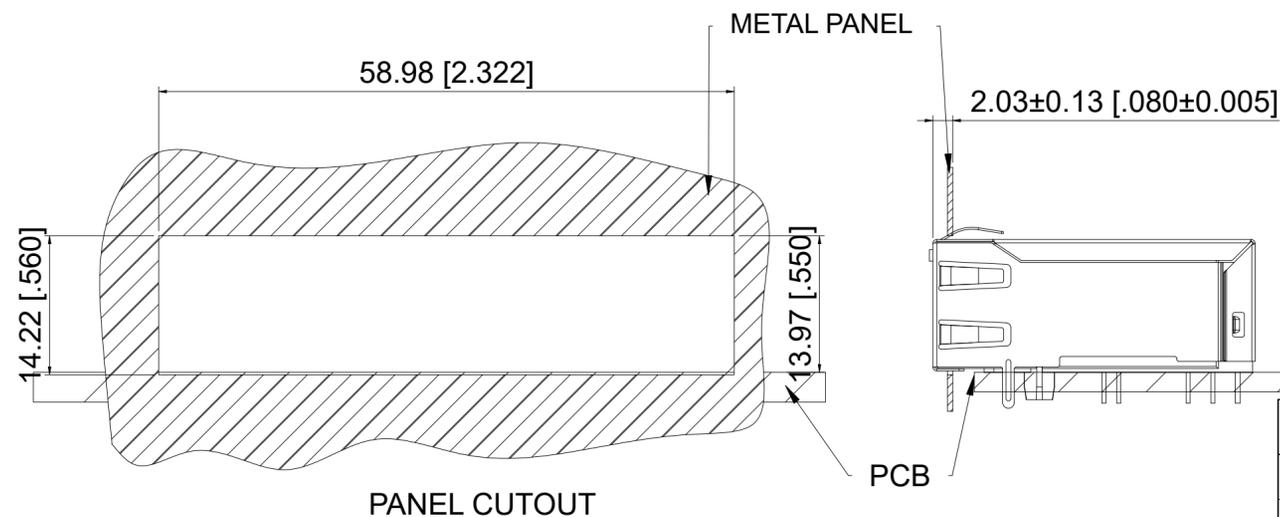
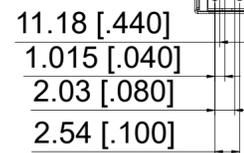
NOTE :

- 1.HOUSING:GLASS FILLED PA46(BLACK) UL94V-0(RoHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH (REFER PART NUMBER TABLE) GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE:-40°C TO +85°C.
STORAGE TEMPERATURE:-40°C TO +85°C.



RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

Dimension Tolerance:
PCB LAYOUT
Tolerances:±0.05[.002]

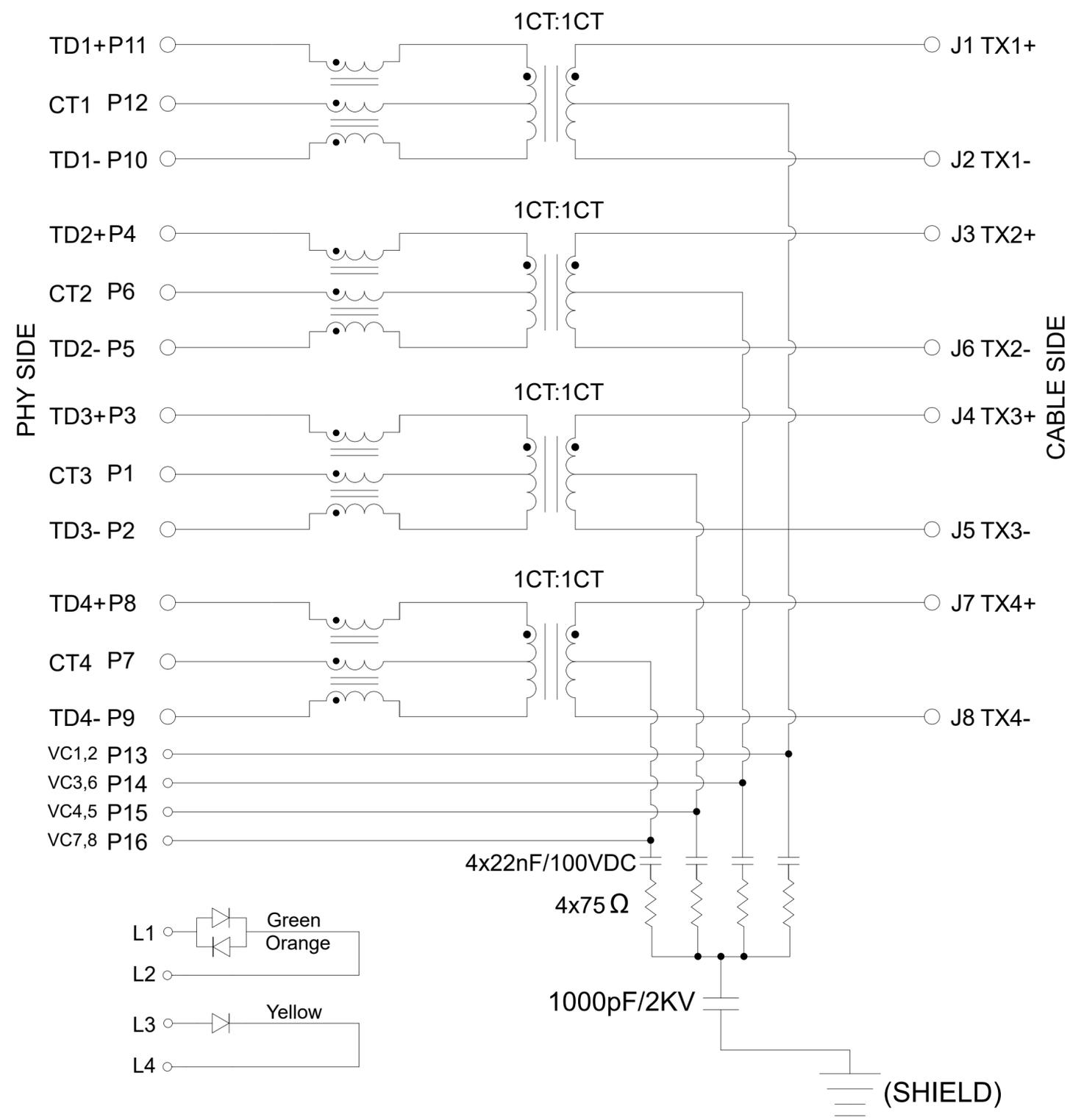


GOLD PLATING THICKNESS	PART NUMBER
GF	2496721-6
0.0762 μm [3μ"]	2496721-5
0.1524 μm [6μ"]	2496721-4
0.381 μm [15μ"]	2496721-3
0.762 μm [30μ"]	2496721-2
1.27 μm [50μ"]	2496721-1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TARUN, R 05NOV2024	TE Connectivity	
DIMENSIONS: mm [INCHES]		CHK RAMESH, KIVADER 05NOV2024	RJ45 W/MAGNET 1X4 1G POE 60W LED	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD RAGHAVAN, CG 05NOV2024	NAME	
0 PLC ± -	1 PLC ± 0.35	PRODUCT SPEC	108-161622	
2 PLC ± 0.25	3 PLC ± 0.15	APPLICATION SPEC	114-161219	
4 PLC ± -	ANGLES ± 2	WEIGHT	-	
MATERIAL	FINISH	SCALE	CAGE CODE	DRAWING NO
-	-	A2	00779	C=2496721
CUSTOMER DRAWING			RESTRICTED TO	REV
SCALE 1:1			SHEET 1 OF 2	A

REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
TURNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	220uH MIN@100KHz/100mV With 19mA DC Bias For(CHANNEL1,2,3,4) 350uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1,2,3,4)
INSERTION LOSS(IL)	-1.2dB MAX@0.3MHz-100MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz; -12dB MIN@60MHz-80MHz;-10dB MIN@80MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHz-100MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS)
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Farward V(max)	TYP
GREEN	570nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V
ORANGE	605nm	2.5V	2.1V

*WITH A FORWARD CURRENT OF 20mA

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TARUN, R 05NOV2024		
DIMENSIONS: mm [INCHES]		CHK RAMESH, KIVADER 05NOV2024		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD RAGHAVAN, CG 05NOV2024	NAME RJ45 W/MAGNET 1X4 1G POE 60W LED	
0 PLC ± -		PRODUCT SPEC 108-161622	RESTRICTED TO	
1 PLC ± 0.35		APPLICATION SPEC 114-161219	SIZE A2	CAGE CODE 00779
2 PLC ± 0.25		WEIGHT -	DRAWING NO C=2496721	SCALE 1:1
3 PLC ± 0.15		FINISH -	SHEET 2 OF 2	REV A
4 PLC ± -		CUSTOMER DRAWING		
ANGLES ± 2				

2496721