

Integrated Silicon Pressure Sensor for Manifold Absolute Pressure, Applications, On-Chip Signal Conditioned, Temperature Compensated and Calibrated

The Freescale MPX4200A series Manifold Absolute Pressure (MAP) sensor for turbo boost engine control is designed to sense absolute air pressure within the intake manifold. This measurement can be used to compute the amount of fuel required for each cylinder.

The MPX4200A series sensor integrates on-chip, bipolar op amp circuitry and thin film resistor networks to provide a high level analog output signal and temperature compensation. The small form factor and reliability of on-chip integration make the Freescale MAP sensor a logical and economical choice for automotive system designers.

Features

- Specifically Designed for Intake Manifold Absolute Pressure Sensing in Engine Control Systems
- Patented Silicon Shear Stress Strain Gauge
- Temperature Compensated Over -40° to $+125^{\circ}\text{C}$
- Offers Reduction in Weight and Volume Compared to Existing Hybrid Modules
- Durable Epoxy Unibody Element

MPX4200A Series

20 to 200 kPa (2.9 to 29 psi)
0.3 to 4.9 V Output

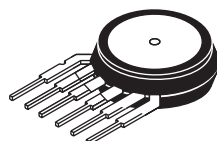
Application Examples

- Manifold Sensing for Automotive Systems
- Ideally suited for Microprocessor or Microcontroller-Based Systems
- Also ideal for Non-Automotive Applications

ORDERING INFORMATION

Device Name	Package Options	Case No.	# of Ports			Pressure Type			Device Marking
			None	Single	Dual	Gauge	Differential	Absolute	
MPX4200A	Tray	867	•					•	MPX4200A

UNIBODY PACKAGE



MPX4200A
CASE 867-08

Operating Characteristics

Table 1. Operating Characteristics ($V_S = 5.1$ Vdc, $T_A = 25^\circ\text{C}$ unless otherwise noted, $P1 > P2$. Decoupling circuit shown in [Figure 3](#) required to meet electrical specifications.)

Characteristic	Symbol	Min	Typ	Max	Unit
Pressure Range ⁽¹⁾	P_{OP}	20	—	200	kPa
Supply Voltage ⁽²⁾	V_S	4.85	5.1	5.35	Vdc
Supply Current	I_o	—	7.0	10	mAdc
Minimum Pressure Offset @ $V_S = 5.1$ Volts ⁽³⁾ (0 to 85°C)	V_{off}	0.199	0.306	0.413	Vdc
Full Scale Output @ $V_S = 5.1$ Volts ⁽⁴⁾ (0 to 85°C)	V_{FSO}	4.725	4.896	4.978	Vdc
Full Scale Span @ $V_S = 5.1$ Volts ⁽⁵⁾ (0 to 85°C)	V_{FSS}	—	4.590	—	Vdc
Accuracy ⁽⁶⁾ (0 to 85°C)	—	—	—	±1.5	% V_{FSS}
Sensitivity	V/P	—	25.5	—	mV/kPa
Response Time ⁽⁷⁾	t_R	—	1.0	—	ms
Output Source Current at Full Scale Output	I_{o+}	—	0.1	—	mAdc
Warm-Up Time ⁽⁸⁾	—	—	20	—	ms
Offset Stability ⁽⁹⁾	—	—	±0.5	—	% V_{FSS}

- 1.0 kPa (kiloPascal) equals 0.145 psi.
- Device is ratiometric within this specified excitation range.
- Offset (V_{off}) is defined as the output voltage at the minimum rated pressure.
- Full Scale Output (V_{FSO}) is defined as the output voltage at the maximum or full rated pressure.
- Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- Accuracy (error budget) consists of the following:
 - Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.
 - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
 - TcSpan: Output deviation over the temperature range of 0 to 85°C, relative to 25°C.
 - TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
 - Variation from Nominal: The variation from nominal values, for Offset or Full Scale Span, as a percent of V_{FSS} , at 25°C.
- Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- Warm-up Time is defined as the time required for the product to meet the specified output voltage after the Pressure has been stabilized.
- Offset Stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

Maximum Ratings

Table 2. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Unit
Maximum Pressure (P1 > P2)	P _{MAX}	800	kPa
Storage Temperature	T _{STG}	-40 to +125	°C
Operating Temperature	T _A	-40 to +125	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Table 3. Mechanical Characteristics

Characteristics	Typ	Unit
Weight, Basic Element (Case 867)	4.0	grams

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

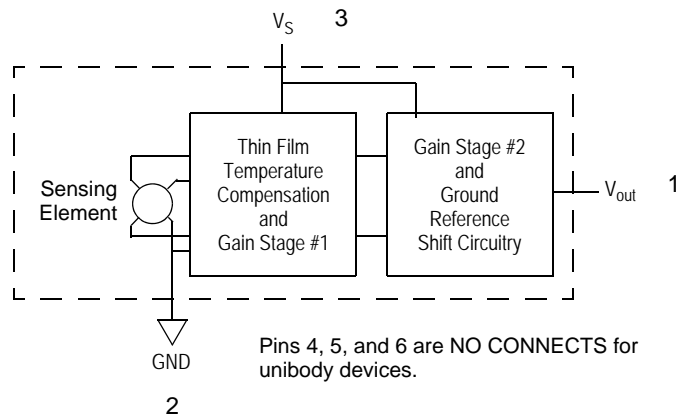


Figure 1. Fully Integrated Pressure Sensor Schematic

On-chip Temperature Compensation and Calibration

Figure 2 illustrates the absolute sensing chip in the basic chip carrier (Case 867). A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the sensor diaphragm. The MPX4200A series pressure sensor operating characteristics, and internal reliability and qualification tests are based on use of dry air as the pressure media. Media, other than dry air, may have adverse effects on sensor performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

Figure 3 shows the recommended decoupling circuit for interfacing the output of the integrated sensor to the A/D input of a microprocessor or microcontroller. Proper decoupling of the power supply is recommended.

Figure 4 shows the sensor output signal relative to pressure input. Typical minimum and maximum output curves are shown for operation over temperature range of 0° to 85°C. The output will saturate outside of the specified pressure range.

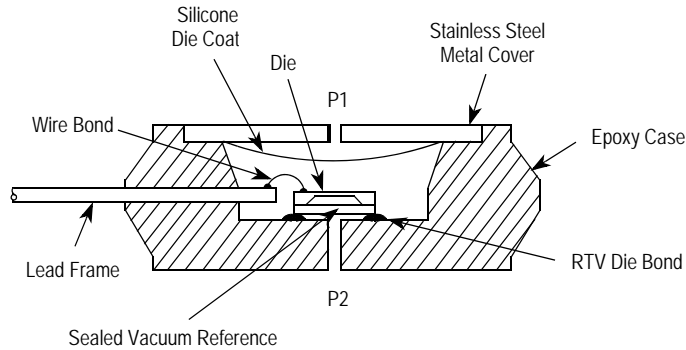


Figure 2. Cross Sectional Diagram (not to scale)

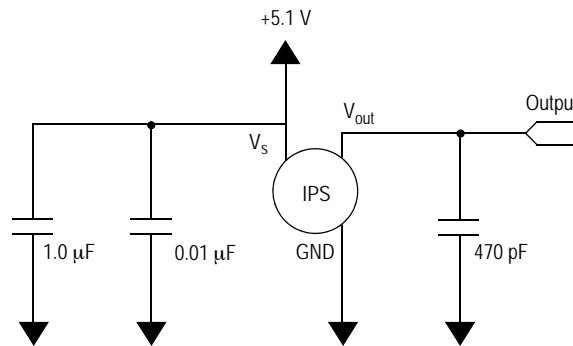


Figure 3. Recommended Power Supply Decoupling and Output Filtering

(For additional output filtering, please refer to Application Note AN1646)

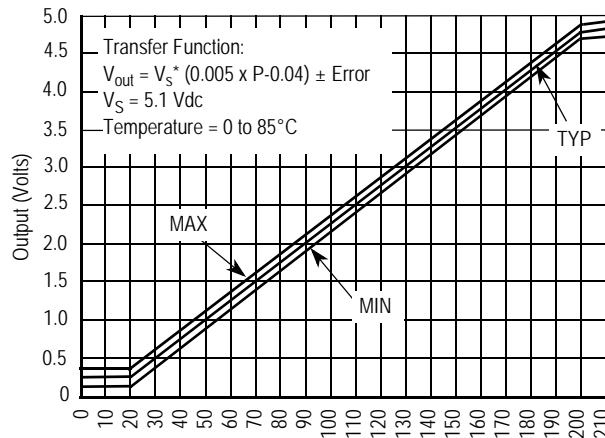
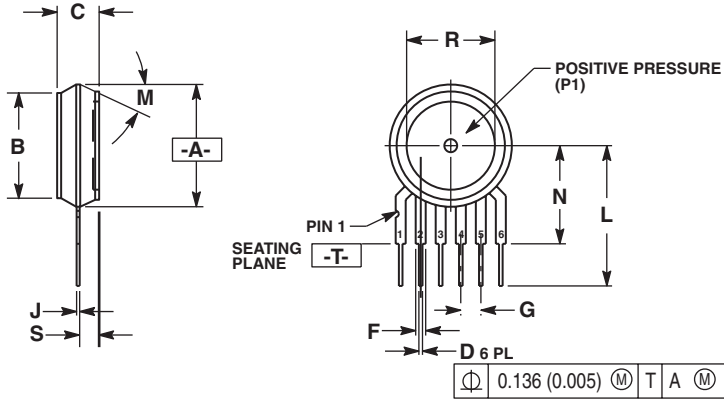


Figure 4. Output versus Absolute Pressure

PACKAGE DIMENSIONS



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION -A- IS INCLUSIVE OF THE MOLD STOP RING. MOLD STOP RING NOT TO EXCEED 16.00 (0.630).

- STYLE 1:
 PIN 1. VOUT
 2. GROUND
 3. VCC
 4. V1
 5. V2
 6. VEX

- STYLE 2:
 PIN 1. OPEN
 2. GROUND
 3. -VOUT
 4. VSUPPLY
 5. +VOUT
 6. OPEN

- STYLE 3:
 PIN 1. OPEN
 2. GROUND
 3. +VOUT
 4. +VSUPPLY
 5. -VOUT
 6. OPEN

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