

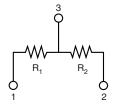
Molded, SOT-23 Thin Film Resistor, Surface Mount Divider Network





Vishay Dale Thin Film MPM Series Dividers provide $\pm\,2$ ppm/°C tracking and a ratio tolerance as tight as 0.01 %, small size, and exceptional stability for all surface mount applications. The standard SOT-23 package format with unity and common standard resistance divider ratios provide easy selection for most applications requiring matched pair resistor elements. The ratios listed are available for off the shelf delivery. If you require a non-standard ratio, consult the applications engineering group as we may be able to meet your requirements.

SCHEMATIC



FEATURES

- Excellent long term ratio stability (ΔR ± 0.015 %, 2000 h, + 70 °C)
- Ratio tolerances to ± 0.01 %
- Low TCR tracking ± 2 ppm
- Standard JEDEC TO-236 package variation AB
- Material categorization:

 For definitions of compliance please see
 www.vishav.com/doc?99912



RoHS³

HALOGEN FREE

Note

This datasheet provides information about parts that are RoHS-compliant and/or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information/tables in this datasheet for details.

TYPICAL PERFORMANCE

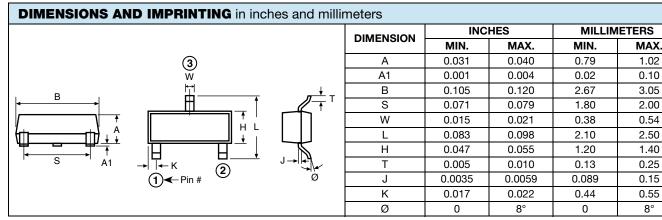
	ABSOLUTE	TRACKING
TCR	25	2
	ABSOLUTE	RATIO
TOL.	0.1	0.05

STANDARD DIVIDER RATIO (R ₂ /R ₁)			
RATIO	R ₂ (Ω)	R ₁ (Ω)	
100:1	100K	1K	
50:1	50K	1K	
25:1	25K	1K	
20:1	20K	1K	
10:1	10K	1K	
9:1	9K	1K	
6:1	6K	1K	
5:1	10K	2K	
5:1	5K	1K	
4:1	8K	2K	
4:1	4K	1K	
2:1	10K	5K	
2:1	2K	1K	
1:1	50K	50K	
1:1	25K	25K	
1:1	10K	10K	
1:1	5K	5K	
1:1	2.5K	2.5K	
1:1	1K	1K	
1:1	500	500	
1:1	250	250	

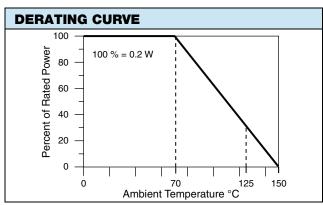
STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Pin/Lead Number	3	-
Resistance Range	250 Ω to 100 k Ω per resistor	-
TCR: Absolute	± 25 ppm/°C	- 55 °C to + 125 °C
TCR: Tracking	± 2 ppm/°C (typical)	- 55 °C to + 125 °C
Tolerance: Absolute	± 0.05 % to ± 1.0 %	+ 25 °C
Tolerance: Ratio	± 0.01 % to 0.5 %	+ 25 °C
Power Rating: Resistor	100 mW	Maximum at + 70 °C
Power Rating: Package	200 mW	Maximum at + 70 °C
Stability: Absolute	$\Delta R \pm 0.05 \%$	2000 h at + 70 °C
Stability: Ratio	ΔR ± 0.015 %	2000 h at + 70 °C
Voltage Coefficient	0.1 ppm/V	-
Working Voltage	100 V max. not to exceed √P x R	-
Operating Temperature Range	- 55 °C to + 125 °C	-
Storage Temperature Range	- 55 °C to + 150 °C	-
Noise	< - 30 dB	-
Thermal EMF	0.2 μV/°C	-
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C
Shelf Life Stability: Ratio	ΔR ± 0.002 %	1 year at + 25 °C

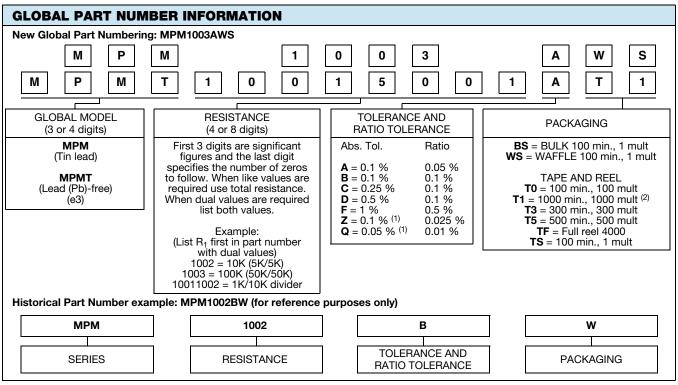
Revision: 12-Jul-13 Document Number: 60001





MECHANICAL SPECIFICATIONS		
Resistive Element	Passivated nichrome	
Substrate Material	Silicon	
Body	Molded epoxy	
Terminals	Copper alloy	
Lead (Pb)-free Option	100 % matte tin	
Tin Lead Option	Sn85	
Tin Lead and Lead (Pb)-free Finish	Plated	





Notes

(1) Tol. available 1K and up equal values only

(2) Preferred packaging code

0.125

- 0.202 -(5.131)

0.049 (1.245)



Vishay Dale Thin Film

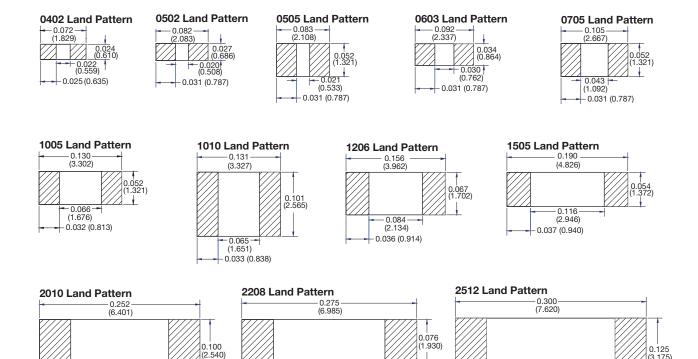
Vishay Dale Thin Film Land Patterns

1. Scope

This technical note provides sample land patterns for Vishay Dale Thin Film SMT resistive products. The following drawings are based on IPC-SM-782 Surface Mount Design and Land Pattern Standard. These drawings are for reference only Vishay Thin Film recommends that the user contacts their PC board supplier for actual land patterns required. The pads are intended for lead (Pb)-free and tin / lead solder types.

2. Product Series

Thin Film Surface Mount Chip Resistors (L, P, PTN, PLT, PLTT, PAT, PATT, PNM, M/D55342 QPL Series)



- 0.175 -(4.445)

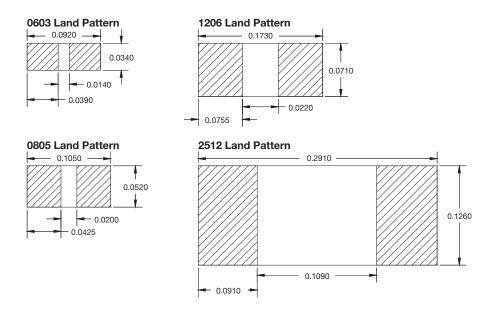
0.050

- 0.154 -(3.912)

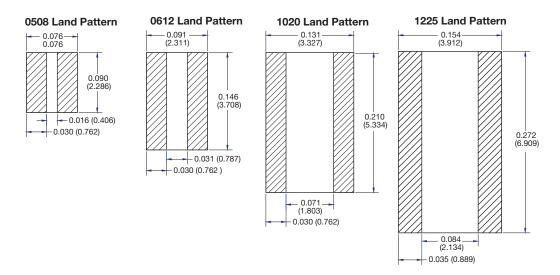
0.049 (1.245)



Thin Film Surface Mount Chip Resistors (PHP, PCAN Series)



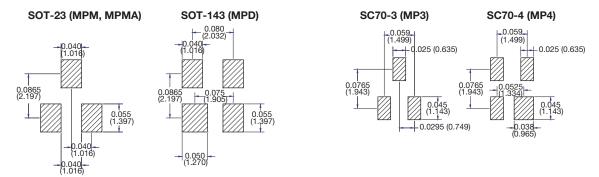
Thin Film Surface Mount Chip Resistors Long Axis Termination (L Series)



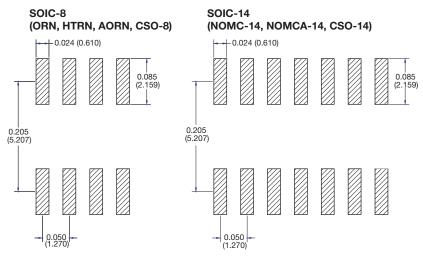


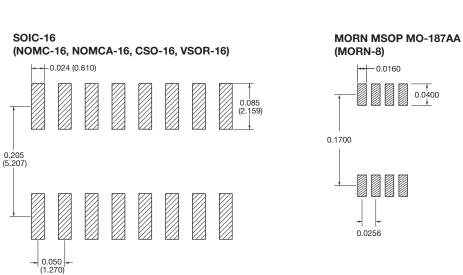


Surface Mount Networks (MPM, MPD, MP3, MP4 Series)

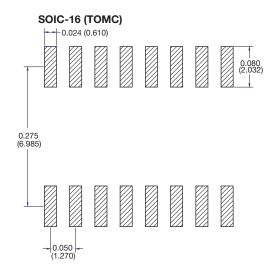


Surface Mount Networks SOIC Narrow Body 150 mils (ORN, CSO, MOMC, HTRN, AORN, MORN Series)

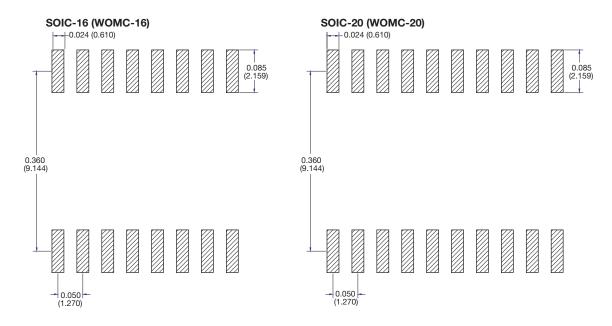




Surface Mount Networks SOIC Medium Body 220 mils (TOMC Series)

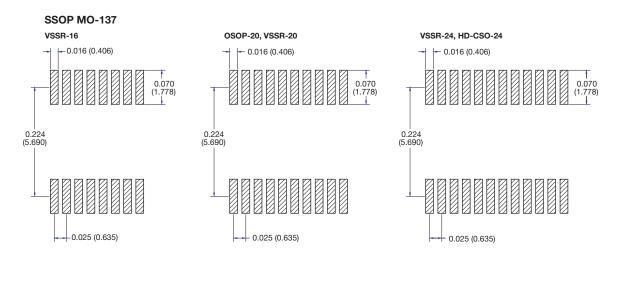


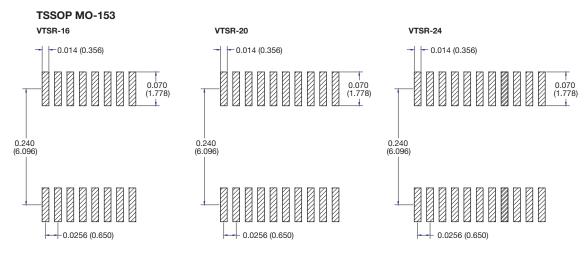
Surface Mount Networks SOIC Wide Body 300 mils (WOMC Series)





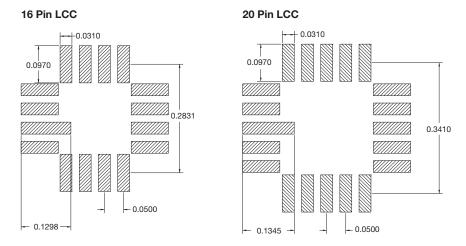
Surface Mount Networks High Density SSOP, TSOP (VSSR, VTSR Series)



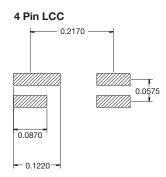




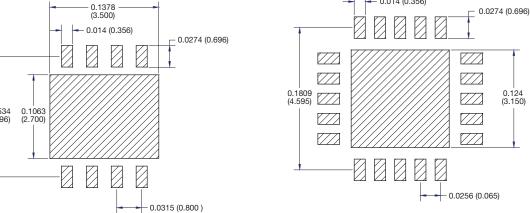
Surface Mount Leadless Networks (LCC Series)



Surface Mount Leadless Networks (MPH Series)

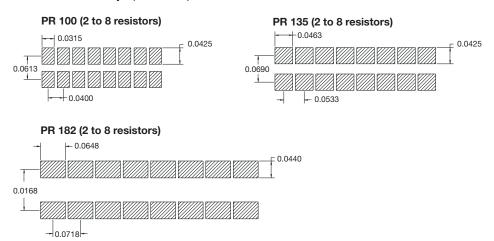


Surface Mount Leadless Packages DUAL/ QUAD Flat No Lead (DFN, QFN Series)





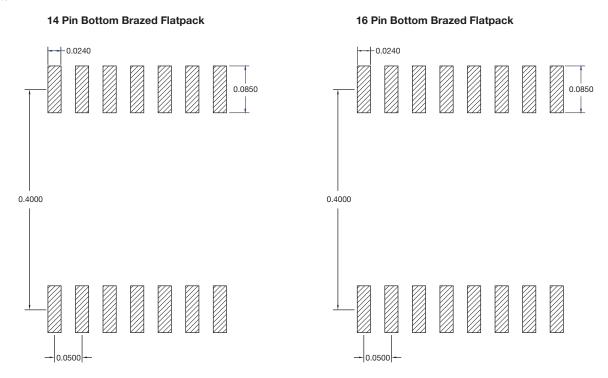
Surface Mount Leadless Resistor Arrays (PR Series)



Note

• All dimensions in inches (mm)

Flatpack





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Vishay

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