



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _{D max} T _A = +25°C
60V	16mΩ @ V _{GS} = 10V	8.9A
000	$27m\Omega @ V_{GS} = 4.5V$	6.8A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- Adaptor Switch

Top View

Notebook PC

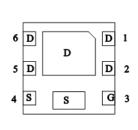
U-DFN2020-6 Pin1

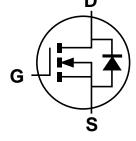
Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe.
 Solderable per MIL-STD-202, Method 20864
- Weight: 0.007 grams (Approximate)





Pin Out Bottom View

Equivalent Circuit

Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Quantity per reel
DMT6016LFDF-7	T6	7	3,000
DMT6016LFDF-13	T6	13	10,000

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

Bottom View

- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



T6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2014) M = Month (ex: 9 = September)

Date Code Key

Year	2014	4	2015		2016	20	17	2018		2019	2	2020
Code	В		С		D	E		F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		* * *	I _D	8.9 7.1	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I_D	11.1 8.9	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	60	Α		
Maximum Body Diode Continuous Current	Is	2.2	Α		
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	15.3	A		
Avalanche Energy (Note 7) L = 0.1mH	E _{AS}	11.7	mJ		

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	C	0.82	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P_{D}	0.52	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	153	°C/W	
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	97		
Total Dower Discinction (Note C)	T _A = +25°C	D	1.9	W	
Total Power Dissipation (Note 6)	T _A = +70°C	P_{D}	1.2	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	0	66	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	42		
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	14.7		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

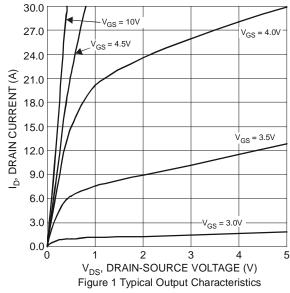
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

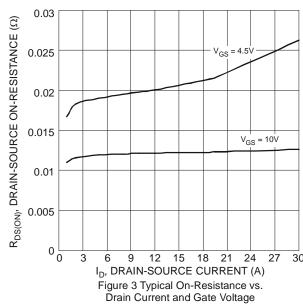
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1.0		3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_		16	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Dialif-Source Off-Resistance	R _{DS(ON)}	_		27	11122	$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		864	_		V 20V V 0V	
Output Capacitance	Coss	_	282	_	pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	27.1	_		I = 1.0WII IZ	
Gate Resistance	Rg	_	1.35	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g	_	17	_			
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	8.4	_	nC	V _{DS} = 30V, I _D = 10A	
Gate-Source Charge	Qgs	_	3.1	_	110	$V_{DS} = 30V$, $I_D = 10A$	
Gate-Drain Charge	Q_{gd}	_	4.3	_			
Turn-On Delay Time	t _{D(on)}	_	3.4	_			
Turn-On Rise Time	t _r	_	5.2	_	nS	$V_{GS} = 10V, V_{DD} = 30V, R_G = 6\Omega,$	
Turn-Off Delay Time	t _{D(off)}	_	12.9	_	113	$I_D = 10A$	
Turn-Off Fall Time	t _f	_	6.8				
Body Diode Reverse Recovery Time	t _{rr}	_	22	_	nS	$I_S = 10A$, $dI/dt = 100A/\mu s$	
Body Diode Reverse Recovery Charge	Qrr	_	11.1	_	nC	$I_S = 10A$, $dI/dt = 100A/\mu s$	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Currented by degree, Net subject to product totaling. Notes:

9. Guaranteed by design. Not subject to product testing.







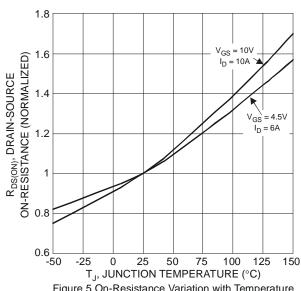
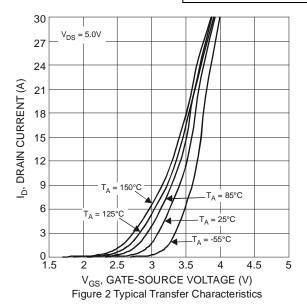
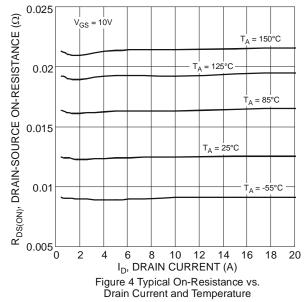


Figure 5 On-Resistance Variation with Temperature





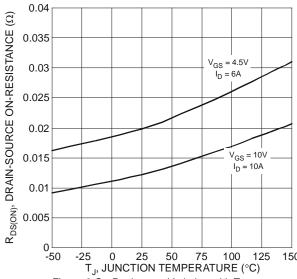


Figure 6 On-Resistance Variation with Temperature



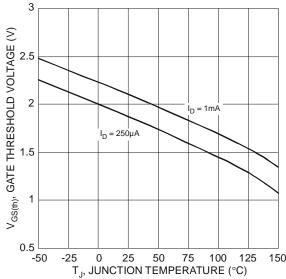


Figure 7 Gate Threshold Variation vs. Ambient Temperature

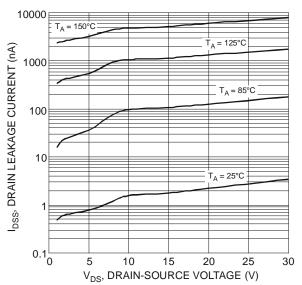
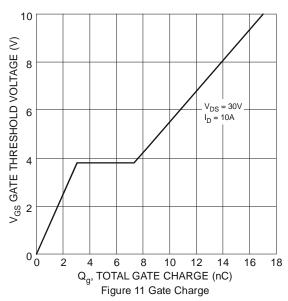
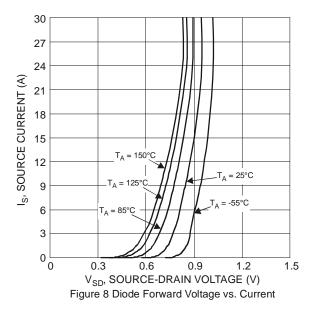
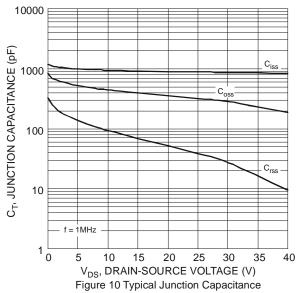
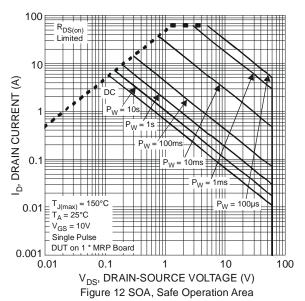


Figure 9 Typical Drain-Source Leakage Current vs. Voltage

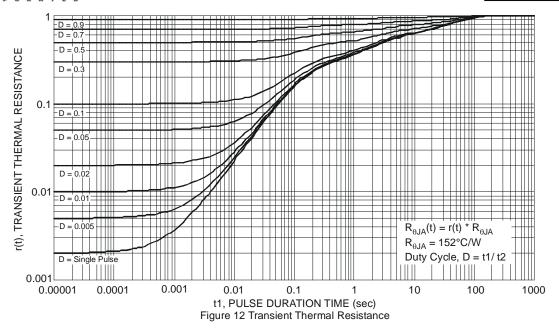






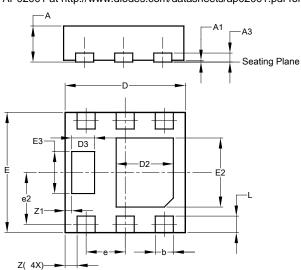






Package Outline Dimensions

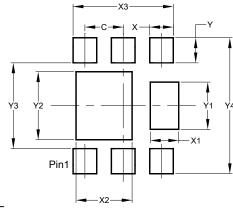
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



U-DFN2020-6								
Dim	Min Max Typ							
Α	0.57	0.63	0.60					
A1	0	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D3	0.33	0.43	0.38					
е		0.65 BSC						
e2	C).863 B	SC					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E3	0.65	0.75	0.70					
L	0.225	0.325	0.275					
Z	0.20 BSC							
Z 1	0.110 BSC							
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	0.650		
X	0.400		
X1	0.480		
X2	0.950		
Х3	1.700		
Y	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



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