

Photocouplers GaAs Infrared LED & Photo Transistor

# TLP184(SE

# 1. Applications

- Programmable Logic Controllers (PLCs)
- · Private Branch Exchanges (PBXs)

#### 2. General

The TLP184(SE is a AC input type photocoupler that consist of a photo transistor optically coupled to two gallium arsenide infrared emitting diodes.

The TLP184(SE is housed in the very small and thin SO6 package, it has a high noise immunity and a high isolation voltage.

Since the TLP184(SE is smaller than DIP package, it's suitable for high-density surface mounting application such as Hybrid ICs.

#### 3. Features

- (1) Collector-emitter voltage: 80 V (min)
- (2) Current transfer ratio: 50% (min)

Rank GB: 100% (min)

- (3) Isolation voltage: 3750 Vrms (min)
- (4) Operating temperature: -55 to 110 °C
- (5) Safety standards

UL-approved: UL1577, File No.E67349

cUL-approved: CSA Component Acceptance Service No.5A File No.E67349

VDE-approved: EN60747-5-5 (Note 1)

CQC-approved: GB4943.1, GB8898 Thailand Factory

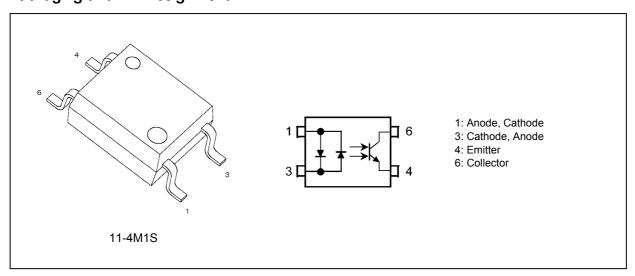




仅适用干海拔 2000m 以下地区安全使用

Note 1: When an EN60747-5-5 approved type is needed, please designate the Option (V4).

## 4. Packaging and Pin Assignment



Start of commercial production



# 5. Principle of Operation

### 5.1. Mechanical Parameters

Characteristics	Min	Unit
Creepage distances	5.0	mm
Clearance	5.0	
Internal isolation thickness	0.4	

# 6. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

	Characteristics		Symbol	Note	Rating	Unit
LED	R.M.S. forward current		I <sub>F(RMS)</sub>		±50	mA
	Input forward current derating	$(T_a \ge 90  ^{\circ}C)$	$\Delta I_F/\Delta T_a$		-1.5	mA/°C
	Input forward current (pulsed)		I <sub>FP</sub>	(Note 1)	±1	А
	Input power dissipation		$P_{D}$		100	mW
	Input power dissipation derating	$(T_a \ge 90  ^{\circ}C)$	$\Delta P_D/\Delta T_a$		-2.9	mW/°C
	Junction temperature		Tj		125	°C
Detector	Collector-emitter voltage		V <sub>CEO</sub>		80	V
	Emitter-collector voltage		V <sub>ECO</sub>		7	V
	Collector current		Ic		50	mA
	Collector power dissipation		Pc		150	mW
	Collector power dissipation derating	$(T_a \ge 25  ^{\circ}C)$	$\Delta P_{C}/\Delta T_{a}$		-1.5	mW/°C
	Junction temperature		Tj		125	℃
Common	Operating temperature		T <sub>opr</sub>		-55 to 110	℃
	Storage temperature		T <sub>stg</sub>		-55 to 125	℃
	Lead soldering temperature	(10 s)	T <sub>sol</sub>		260	℃
	Total power dissipation		P <sub>T</sub>		200	mW
	Input power dissipation derating	$(T_a \ge 25  ^{\circ}\text{C})$	$\Delta P_D/\Delta T_a$		-2.0	mW/°C
	Isolation voltage	AC, 60 s, R.H. ≤ 60%	BV <sub>S</sub>	(Note 2)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width (PW)  $\leq$  100  $\mu$ s, f = 100 Hz

Note 2: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.



# 7. Electrical Characteristics (Unless otherwise specified, $T_a$ = 25 °C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V <sub>F</sub>		I <sub>F</sub> = ±10 mA	1.1	1.25	1.4	V
	Input capacitance	Ct		V = 0 V, f = 1 MHz		60	_	pF
Detector	Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>		I <sub>C</sub> = 0.5 mA	80			V
	Emitter-collector breakdown voltage	V <sub>(BR)ECO</sub>		I <sub>E</sub> = 0.1 mA	7			V
	Dark Current	I <sub>DARK</sub>		V <sub>CE</sub> = 48 V		0.01	0.08	μА
				V <sub>CE</sub> = 48 V, T <sub>a</sub> = 85 °C		2	50	μА
	Collector-emitter capacitance	C <sub>CE</sub>		V = 0 V, f = 1 MHz		10		pF



# 8. Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	I <sub>C</sub> /I <sub>F</sub>	(Note 1)	$I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$	50	_	600	%
			$I_F = \pm 5$ mA, $V_{CE} = 5$ V, Rank GB	100	_	600	
Saturated current transfer ratio	I <sub>C</sub> /I <sub>F(sat)</sub>		$I_F = \pm 1 \text{ mA}, V_{CE} = 0.4 \text{ V}$	_	60	_	
			$I_F = \pm 1$ mA, $V_{CE} = 0.4$ V, Rank GB	30	_		
Collector-emitter saturation	V <sub>CE(sat)</sub>		$I_C = 2.4 \text{ mA}, I_F = \pm 8 \text{ mA}$	_	_	0.3	٧
voltage			$I_C = 0.2 \text{ mA}, I_F = \pm 1 \text{ mA}$	_	0.2	_	
			$I_C$ = 0.2 mA, $I_F$ = ±1 mA, Rank GB	_	_	0.3	
OFF-state collector current	I <sub>C(off)</sub>		$V_F = \pm 0.7 \text{ V}, V_{CE} = 48 \text{ V}$	_	1	10	μΑ
Collector current ratio	I <sub>C</sub> (ratio)		See Fig. 8.1 $I_C(I_F = -5 \text{ mA}) / I_C(I_F = 5 \text{ mA})$	0.33	1	3	

Note 1: See Table 8.1 for current transfer ratio.

Table 8.1 Current Transfer Ratio (CTR) Rank (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Rank	Test Condition	Current transfer ratio I <sub>C</sub> /I <sub>F</sub> Min	Current transfer ratio I <sub>C</sub> /I <sub>F</sub> Max	Marking of Classification	Unit
Blank	$I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$	50	600	Blank, YE, GR, GB, BL	%
Υ		50	150	YE	
GR		100	300	GR	
GB		100	600	GB,GR,BL	
BL		200	600	BL	

Note: Specify both the part number and a rank in this format when ordering.

Example: TLP184(GB,SE

For safety standard certification, however, specify the part number alone.

Example: TLP184(GB,SE: TLP184

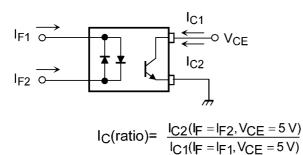


Fig. 8.1 Collector Current Ratio Test Circuit

# 9. Isolation Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	Cs	(Note 1)	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8		pF
Isolation resistance	R <sub>S</sub>	(Note 1)	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	1 × 10 <sup>12</sup>	1014		Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	3750			Vrms
			AC, 1 s in oil	_	10000	_	
			DC, 60 s in oil		10000		Vdc

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.



# 10. Switching Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>		V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA,	_	2	_	μS
Fall time	t <sub>f</sub>		$R_L = 100 \Omega$	_	3	_	
Turn-on time	t <sub>on</sub>			_	3	_	
Turn-off time	t <sub>off</sub>			_	3	_	
Turn-on time	t <sub>on</sub>		See Fig. 10.1	_	0.5	_	
Storage time	t <sub>s</sub>		$V_{CC} = 5 \text{ V, I}_{F} = 16 \text{ mA,}$ $R_{L} = 1.9 \text{ k}\Omega$	_	30	_	
Turn-off time	t <sub>off</sub>		11/1 - 1:9 1/75	_	50	_	

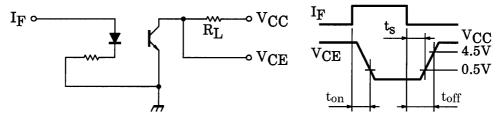


Fig. 10.1 Switching Time Test Circuit and Waveform



# 11. Characteristics Curves (Note)

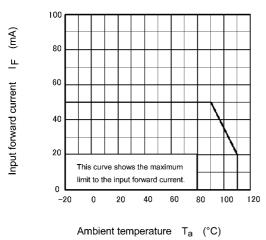


Fig. 11.1 I<sub>F</sub> - T<sub>a</sub>

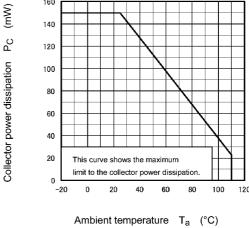


Fig. 11.2 P<sub>C</sub> - T<sub>a</sub>

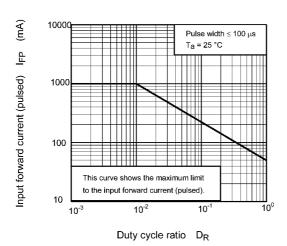


Fig. 11.3 I<sub>FP</sub> - D<sub>R</sub>

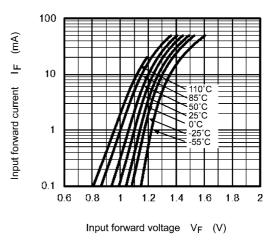


Fig. 11.4 I<sub>F</sub> - V<sub>F</sub>

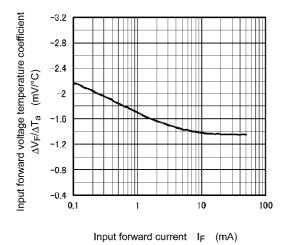
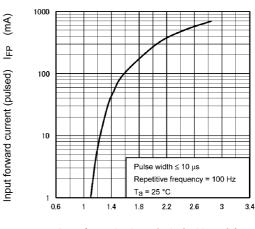
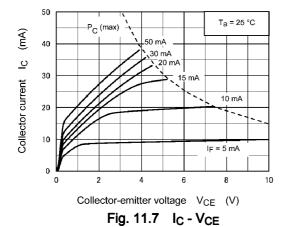


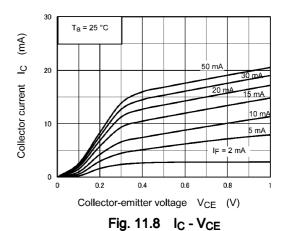
Fig. 11.5  $\Delta V_F/\Delta T_a - I_F$ 

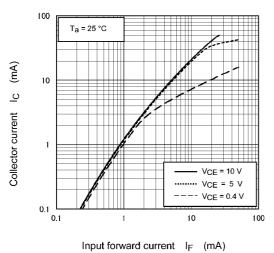


Input forward voltage (pulse) V<sub>FP</sub> (V)

Fig. 11.6 I<sub>FP</sub> - V<sub>FP</sub>







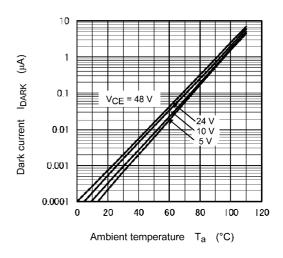
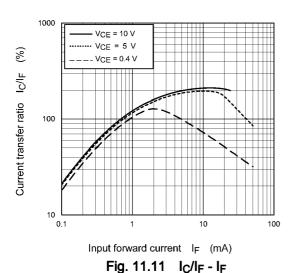


Fig. 11.9 I<sub>C</sub> - I<sub>F</sub>

Fig. 11.10 I<sub>DARK</sub> - T<sub>a</sub>



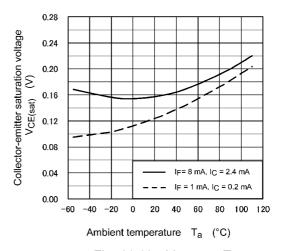
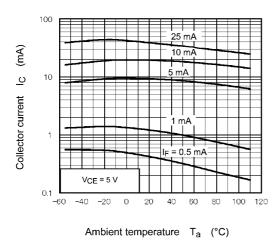


Fig. 11.12 V<sub>CE(sat)</sub> - T<sub>a</sub>



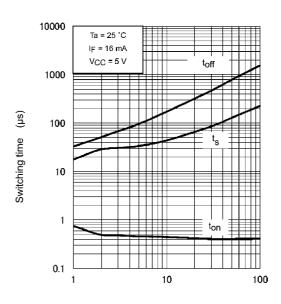
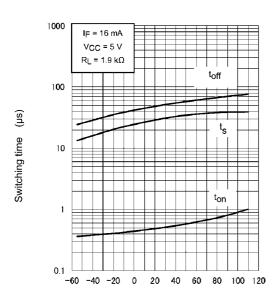


Fig. 11.13 I<sub>C</sub> - T<sub>a</sub>

 $\label{eq:load_resistance} \begin{array}{ll} \mbox{Rod resistance} & \mbox{R}_L & (\mbox{k}\Omega) \\ \mbox{Fig. 11.14} & \mbox{Switching Time - $R_L$} \end{array}$ 



Ambient temperature  $T_a$  (°C) Fig. 11.15 Switching Time -  $T_a$ 

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# 12. Soldering and Storage

### 12.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

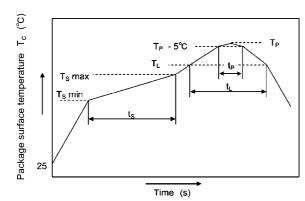
· When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	S
Ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )			3	°C/s
Liquidus temperature	TL	217		°C
Time above T <sub>L</sub>	t <sub>L</sub>	60	150	S
Peak temperature	T <sub>P</sub>		260	°C
Time during which $T_c$ is between ( $T_P - 5$ ) and $T_P$	t <sub>P</sub>		30	s
Ramp-down rate (T <sub>P</sub> to T <sub>L</sub> )			6	°C/s

Fig. 12.1.1 An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

· When using soldering flow

Preheat the device at a temperature of 150 °C (package surface temperature) for 60 to 120 seconds. Mounting condition of 260 °C within 10 seconds is recommended.

Flow soldering must be performed once.

· When using soldering Iron

Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C

Heating by soldering iron must be done only once per lead.

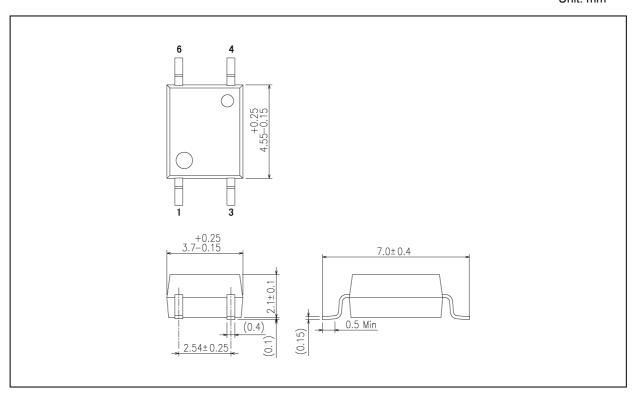
### 12.2. Precautions for General Storage

- · Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- · When restoring devices after removal from their packing, use anti-static containers.
- · Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.



# **Package Dimensions**

Unit: mm



Weight: 0.08 g (typ.)

	Package Name(s)
TOSHIBA: 11-4M1S	

Rev.5.0



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