Lead (Pb) Free Product - RoHS Compliant

Red SLR2016
Super-red SLO2016
Green SLG2016
Yellow SLY2016

DESCRIPTION
The SLR/SLO/SLG/SLY2016 is a four digit 5 x 7 dot matrix display module with a built-in CMOS integrated circuit. This display is X/Y stackable.

The integrated circuit contains memory, a 128 ASCII ROM decoder, multiplexing circuitry and drivers. Data entry is asynchronous. A display system can be built using any number of SLR/SLO/SLG/SLY2016 since each digit can be addressed independently and will continue to display the character last stored until replaced by another.

System interconnection is very straightforward. Two address bits (A0, A1) are normally connected to the like-named inputs of all displays in the system. Data lines are connected to all SLR/SLO/SLG/SLY2016s directly and in parallel as is the write line (WR). The display will then behave as a write-only memory.

The SLR/SLO/SLG/SLY2016 has several features superior to competitive devices. 100% burn-in processing insures that the SLR/SLO/SLG/SLY2016 will function in more stressful assembly and use environments. True “blanking” allows the designer to dim the display for more flexibility of display presentation. Finally the CLR clear function will clear the ASCII character RAM.

The character set consists of 128 special ASCII characters for English, German, Italian, Swedish, Danish, and Norwegian.

All products are 100% burned-in and tested, then subjected to out-going AQLs of 0.25% for brightness matching, visual alignment and dimensions, 0.065% for electrical and functional.

FEATURES
- Very Close Multi-line Spacing, 0.4” Centers
- 4.72 mm (0.180”) 5 x 7 Dot Matrix Characters
- 128 Special ASCII Characters for English, German, Italian, Swedish, Danish, and Norwegian Languages
- Wide Viewing Angle: X axis 50° Maximum, Y Axis ± 75° Maximum
- Fast Access Time, 110 ns at 25°C
- Full Size Display for Stationary Equipment
- Built-in Memory
- Built-in Character Generator
- Built-in Multiplex and LED Drive Circuitry
- Direct Access to Each Digit Independently and Asynchronously
- Clear Function that Clears Character Memory
- True Blanking for Intensity Dimming Applications
- End-stackable, 4-Character Package
- Intensity Coded for Display Uniformity
- Extended Operating Temperature Range: -40°C to +85°C
- Superior ESD Immunity
- 100% Burned-in and Tested
- Wave Solderable
- TTL Compatible over Operating Temperature Range

See Appnotes 18, 19, 22, and 23 at www.osram-os.com for additional information.
**Ordering Information**

<table>
<thead>
<tr>
<th>Type</th>
<th>Color of Emission</th>
<th>Character Height mm (inch)</th>
<th>Ordering Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>SLR2016</td>
<td>standard red</td>
<td>4.72 (0.180)</td>
<td>Q68000A8640</td>
</tr>
<tr>
<td>SLO2016</td>
<td>super-red</td>
<td></td>
<td>Q68000A8641</td>
</tr>
<tr>
<td>SLG2016</td>
<td>green</td>
<td></td>
<td>Q68000A8642</td>
</tr>
<tr>
<td>SLY2016</td>
<td>yellow</td>
<td></td>
<td>Q68000A8643</td>
</tr>
</tbody>
</table>

**Package Outlines**

Dimensions in mm (inch)

![Diagram of package outlines with dimensions and indicators.]
## Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating temperature range</td>
<td>$T_{op}$</td>
<td>$-40 \ldots +85$</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature range</td>
<td>$T_{stg}$</td>
<td>$-40 \ldots +100$</td>
<td>°C</td>
</tr>
<tr>
<td>DC Supply Voltage</td>
<td>$V_{CC}$</td>
<td>-0.5 to +7.0</td>
<td>V</td>
</tr>
<tr>
<td>Input Voltage, Respect to GND (all inputs)</td>
<td></td>
<td>-0.5 to $V_{CC}$ +0.5</td>
<td>V</td>
</tr>
<tr>
<td>Solder Temperature 1.59 mm (0.063&quot;) below seating plane, $t &lt; 5.0$ s</td>
<td>$T_S$</td>
<td>260</td>
<td>°C</td>
</tr>
<tr>
<td>Relative Humidity</td>
<td></td>
<td>85</td>
<td>%</td>
</tr>
</tbody>
</table>

## Optical Characteristics at 25°C

($V_{CC}=5.0$ V at 100% brightness level, viewing angle: X axis ± 50°, Y axis ± 75°)

<table>
<thead>
<tr>
<th>Description</th>
<th>Symbol</th>
<th>Values</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Time Averaged Luminous Intensity $^1)$</td>
<td>$I_v$</td>
<td>50</td>
<td>60</td>
</tr>
<tr>
<td>Spectral Peak Wavelength</td>
<td>$\lambda_{peak}$</td>
<td>660</td>
<td>635</td>
</tr>
</tbody>
</table>

Notes:
1. Peak luminous intensity values can be calculated by multiplying these values by 7.
2. LED to LED intensity matching 1.8:1.0

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**Top View**

![Top View Diagram]

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2006-01-23
**Timing Characteristics—Write Cycle Waveforms**

**Loading Data**

The desired data code (D0–D6) and digit address (A0, A1) must be held stable during the write cycle for storing new data. Data entry may be asynchronous. Digit 0 is defined as right hand digit with A1=A2=0.

Clearing the entire internal four-digit memory can be accomplished by holding the clear (CLR) low for 1.0 msec minimum. The clear function will clear the ASCII RAM. Loading an illegal data code will display a blank.
Display Blanking
Blank the display by loading a blank or space into each digit of the display or by using the (BL) display blank input. Setting the (BL) input low does not affect the contents of data memory.

A flashing circuit can easily be constructed using a 555 as table multivibrator. Figure 3 illustrates a circuit in which varying R1 (100K~10K) will have a flash rate of 1.0 Hz~10 Hz.

The display can be dimmed by pulse width modulating the (BL) at a frequency sufficiently fast to not interfere with the internal clock. The dimming signal frequency should be 2.5 kHz or higher. Dimming the display also reduces power consumption.

An example of a simple dimming circuit using a 556 is illustrated in Figure 4. Adjusting potentiometer R3 will dim the display by changing the blanking pulse duty cycle.

Typical Loading State Table

<table>
<thead>
<tr>
<th>WR</th>
<th>A1</th>
<th>A0</th>
<th>D6</th>
<th>D5</th>
<th>D4</th>
<th>D3</th>
<th>D2</th>
<th>D1</th>
<th>D0</th>
<th>Digit</th>
</tr>
</thead>
<tbody>
<tr>
<td>H</td>
<td>L</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>G</td>
<td>GREY</td>
</tr>
<tr>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>R</td>
<td>GREEN</td>
</tr>
<tr>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>E</td>
<td>RED</td>
</tr>
<tr>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>Y</td>
<td>YELLOW</td>
</tr>
<tr>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>H</td>
<td>L</td>
<td>L</td>
</tr>
<tr>
<td>L</td>
<td>L</td>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>L</td>
<td>H</td>
<td>H</td>
<td>B</td>
<td>BLUE</td>
</tr>
<tr>
<td>L</td>
<td>L</td>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>L</td>
<td>H</td>
<td>H</td>
<td>L</td>
<td>E</td>
</tr>
<tr>
<td>L</td>
<td>L</td>
<td>X</td>
<td>X</td>
<td>see character code</td>
<td>see char. set</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Character Set

| ASCII CODE | D0 | D1 | D2 | D3 | D4 | D5 | D6 | HEX  | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C | D | E | F |
|------------|----|----|----|----|----|----|----|------|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| High=1     |    |    |    |    |    |    |    | 1111  | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Low=0      |    |    |    |    |    |    |    | 0000  | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Initialize |    |    |    |    |    |    |    | 0000  | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

Notes:
1. High=1 level
2. Low=0 level
3. Upon power up, the device will initialize in a random state.
Block Diagram

- **RAM Read Logic**
  - **Address Bus**
  - **Write**
  - **Address Decoder**
  - **RAM Memory 4 x 7 bit**
  - **7-bit ASCII Code**

- **Timing and Control Logic**
  - **Row Decoder**
  - **Column Decoder**
  - **Row Control Logic & Row Drivers**
  - **OSC**
  - **CLR**
  - **128 Counter**
  - **7 Counter**
  - **Display**
  - **Display Output Logic**

- **128 x 7 bit ASCII Character Decode (4.48 kbits)**
- **Display Data Columns 0 to 19**
- **Display Rows 0 to 6**
Design Considerations
For details on design and applications of the SLX2016 in multiple display systems, refer to Appnote 15 at www.osram-os.com

Electrical & Mechanical Considerations
Voltage Transient Suppression
We recommend that the same power supply be used for the display and the components that interface with the display to avoid logic inputs higher than $V_{CC}$. Additionally, the LEDs may cause transients in the power supply line while they change display states. The common practice is to place 0.01 mF capacitors close to the displays across $V_{CC}$ and GND, one for each display, and one 10 μF capacitor for every second display.

ESD Protection
The CMOS IC of the SLX2016 is resistant to ESD damage and capable of withstanding discharges less than 2.0 kV. However, take all the standard precautions, normal for CMOS components. These include properly groundig personnel, tools, tables, and transport carriers that come in contact with unshielded parts. If these conditions are not, or cannot be met, keep the leads of the device shorted together or the parts in anti-static packaging.

Soldering Considerations
The SLX2016 can be hand soldered using a grounded iron set to 260°C.

The display is compatible with leadfree and tin/lead solder. Wave soldering is also possible following these conditions. Preheat does not exceed 93°C on the solder side of the PC board or a package surface temperature of 85°C. Water soluble organic acid flux (except carboxylic acid) or resin-based RMA flux without alcohol can be used.

Wave temperature of 245°C +/-5°C with a dwell between 1.5 sec. to 3 sec. Exposure to the wave should not exceed temperatures above 260°C for five seconds at 0.063 inches below the seating plane. The packages should not be immersed in the wave.

Post Solder Cleaning Procedures
The least offensive cleaning solution is hot D.I. water (60°C) for less than 15 minutes. Addition of mild saponifiers is acceptable. Do not use commercial dishwasher detergents.

For faster cleaning, solvents may be used. Carefully select any solvent for chemical compatibility. The exposure should not exceed two minutes at elevated temperatures. Acceptable solvents are TF (trichlorotrifluorethane), TA, 111 (chlorodifluoroethane), 134a (chlorodifluoroethane), 1131 (chlorodifluoroethane), and unheated acetone.

Note:
Acceptable commercial solvents are: Basic TF, Arklone, P. Genesolv, D. Genesolv DA, Blaco-Tron TF and Blaco-Tron TA.

Unacceptable solvents contain alcohol, methanol, methylene chloride, ethanol, TPS, TCM, TMC, TMS+, TE, or TES. Since many commercial mixtures exist, contact a solvent vendor for chemical composition information. Some major solvent manufacturers are: Allied Chemical Corporation, Specialty Chemical Division, Morris- town, NJ; Baron-Blakeslee, Chicago, IL; Dow Chemical, Midland, MI; E.I. DuPont de Nemours & Co., Wilmington, DE.

For further information refer to Appnotes 18 and 19 at www.osram-os.com.

An alternative to soldering and cleaning the display modules is to use sockets. Standard pin DIP sockets 7.62 mm (0.300") wide with 2.54 mm (0.100") centers work well for single displays. Multiple display assemblies are best handled by longer SIP sockets or DIP sockets when available for uniform package alignment. Socket manufacturers are Aries Electronics, Inc., Frenchtown, NJ; Garry Manufacturing, New Brunswick, NJ; Robinson-Nugent, New Albany, IN; and Samtec Electronic Hardware, New Albany, IN.

For further information refer to Appnote 22 at www.osram-os.com.

Optical Considerations
The 4.72 mm (0.180") high characters of the SLX2016 give readability up to eight feet. Proper filter selection enhances read- ability over this distance.

Filters enhance the contrast ratio between a lit LED and the character background intensifying the discrimination of different characters. The only limitation is cost. Take into consideration the ambient lighting envi- ronment for the best cost/benefit ratio for filters.

Incandescent (with almost no green) or fluorescent (with almost no red) lights do not have the flat spectral response of sunlight. Plastic band-pass filters are an inexpensive and effective way to strengthen contrast ratios. The SLR2016 is a standard red display and should be matched with long wavelength pass filter in the 600 nm to 620 nm range.

The SLO2016 is a super-red display and should be matched with a long wavelength pass filter in the 470 nm to 590 nm range. The SLG/SLY2016 should be matched with a yellow-green band-pass fil- ter that peaks at 565 nm. For displays of multiple colors, neutral den- sity gray filters offer the best compromise.

Additional contrast enhancement is gained by shading the display. Plastic band-pass filters with built-in louvers offer the next step up in contrast improvement. Plastic filters can be improved further with anti-reflective coatings to reduce glare. The trade-off is fuzzy characters. Mounting the filters close to the display reduces this effect. Take care not to overheat the plastic filter by allowing for proper air flow.

Optimal filter enhancements are gained by using circular polarized, anti-reflective, band-pass filters. Circular polarizing further enhances contrast by reducing the light that travels through the filter and reflects back off the display to less than 1%.

Several filter manufacturers supply quality filter materials. Some of them are: Panelgraphic Corporation, W. Caldwell, NJ; SGL Homo- lite, Wilmington, DE; 3M Company, Visual Products Division, St. Paul, MN; Polaroid Corporation, Polarizer Division, Cambridge, MA; Marks Polarized Corporation, Deer Park, NY, Hoya Optics, Inc., Fremont, CA.

One last note on mounting filters: recessing displays and bezel assemblies is an inexpensive way to provide a shading effect in overhead lighting situations. Several Bezel manufacturers are: R.M.F. Products, Batavia, IL; Nobex Components, Griffith Plastic Corp., Burlingame, CA; Photo Chemical Products of California, Santa Monica, CA; I.E.E.-Atlas, Van Nuys, CA.

Refer to Appnote 23 at www.osram-os.com for further information.

2006-01-23
<table>
<thead>
<tr>
<th>Page</th>
<th>Subjects (major changes since last revision)</th>
<th>Date of change</th>
</tr>
</thead>
<tbody>
<tr>
<td>all</td>
<td>Lead free device</td>
<td>2006-01-23</td>
</tr>
</tbody>
</table>
Attention please!

The information describes the type of component and shall not be considered as assured characteristics. Terms of delivery and rights to change design reserved. Due to technical requirements components may contain dangerous substances. For information on the types in question please contact our Sales Organization. If printed or downloaded, please find the latest version in the Internet.

Packing

Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office. By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

Components used in life-support devices or systems must be expressly authorized for such purpose! Critical components¹) may only be used in life-support devices or systems²) with the express written approval of OSRAM OS.

¹) A critical component is a component used in a life-support device or system whose failure can reasonably be expected to cause the failure of that life-support device or system, or to affect its safety or the effectiveness of that device or system.

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