

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P6-P5-P4) : (J6-J3)

(P3-P2-P1) : (J2-J1)

2.0 INDUCTANCE (P6-P4)

(P3-P1)

3.0 LEAKAGE INDUCTANCE P6-P4 (WITH J6 AND J3 SHORT)

P3-P1 (WITH J2 AND J1 SHORT)

4.0 INTERWINDING CAPACITANCE (P6,P5,P4) TO (J6,J3)

(P3,P2,P1) TO (J2,J1)

5.0 DC RESISTANCE (J6-J3)=(J2-J1) : 1.2 ohms Max. NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

: 350uH MIN. @ 0.1V , 100KHz, 8mA DC Bias

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

: 0.3 MAX. @ 1MHz

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: 1CT : 1CT± 3%

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: 30pf MAX @ 1MHz

: 30pf MAX. @ 1MHZ

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6.0 RETURN LOSS: (P6-P4)=100 OHMS AND (P1-P3)=100 OHM REF. 1MHz T□ 30MHz : 18dB MIN. 60MHz T□ 80MHz : 12dB MIN. NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3). 7.0 VOLTAGE WITHSTAND: (J1, J2) T0 (P1, P3) : 1500 VAC (J3, J6) TD (P4,P6) : 1500 VAC 8.0 INSERTION LOSS: RS=RL=100 ohms 100KHz T□ 100MHz 1.1 dB TYP 9.0 RISE TIME: RS=100 □HMS AND RL = 100 □HMS DUTPUT VOLTAGE = 1 V peak 3.0 nS MAX 3.0 nS MAX PULSE WIDTH= 112nS 10.0 CROSS TALK: 1MHz T□ 100MHz 40 dB TYP 11.0 COMMON TO COMMON MODE ATTENUATION: 35dB TYP 30MHz T□ 100MHz

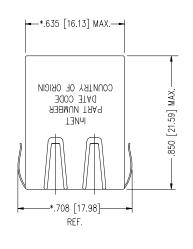
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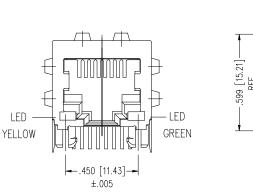
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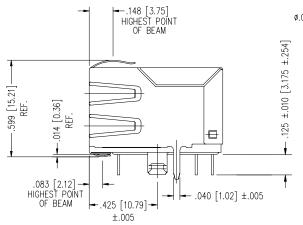
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- .050 [1.27] TYP TOL NON-ACCUM - .025 [0.64] - .100 [2.54] TYP ø.035 [0.89] (8) TOL NON-ACCUM PIN 1 .100 [2.54] ø.062 [1.57] (2) .250 [6.35] .120 [3.05] .160 [4.06] ->> $\phi \frac{.128 \ [3.25]}{.125 \ [3.18]} (2)$ ø.040 [1.02] (4) *.322 [8.18] *.450 [11.43] *.522 [13.26] *.610 [15.49]

P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

NOTES:

CT720034X1/24-001302

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING.

LED SPECIFICATIONS

COLOR: YELLOW GREEN FORWARD VOLTAGE(20mA): 2.5v (MAX) 2.5v (MAX) FORWARD VOLTAGE(20mA): 2.1v (TYP) 2.2v (TYP) POWER DISSIPATION: 105mW 105mW - WAVE LENGTH: 565nm 590nm - LUMINOUS INTENSITY (10mA): 2-8 MCD 8-32 MCD

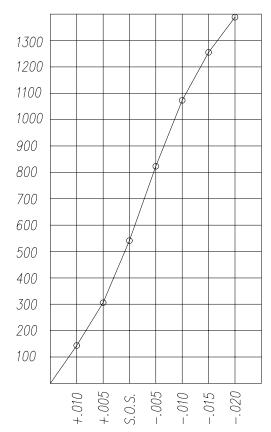
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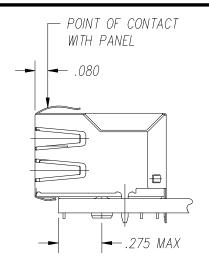
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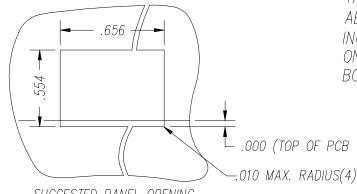
61-40138 REV.



PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING THESE VARIABLES CAN BE ADJUSTED IN FITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.



.000 (TOP OF PCB TO BOTTOM OF OPENING)

SUGGESTED PANEL OPENING

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