



The DNA of tech.™

Product Change Notification



Product Group: OPT/Friday September 4, 2026/PCN-OPT-1503-2026-REV-0

Qualification of new chipset and BOM for 350 V and 400 V solid state relays

For further information, please contact your regional Vishay office.

CONTACT INFORMATION

Americas

Jim Toal
2585 Junction Avenue , CA
-
San Jose United States 95134
Phone: +14085678358
Fax:
jim.toal@vishay.com

Europe

Boris Lazic
Theresienstrasse 2
-
Heilbronn Germany 74072
Phone: +4971317498516
Fax:
boris.lazic@vishay.com

Asia

Jason Soon
37A Tampines Street 92 #07-01
-
Singapore Singapore 528886
Phone: +6587785656
Fax:
jason.soon@vishay.com

Description of Change: Vishay will change the chipset combination (Emitter and Mosfet dies) in the affected products. In addition, the leadframe will be changed.

Reason for Change: The current Mosfet chip has been obsoleted by the supplier. As a result, a new chipset combination has been qualified to sustain the product performance and ensure uninterrupted supply to our valued customers. In addition alternative lead frame has been qualified to have a standardized manufacturing process.

Expected Influence on Quality/Reliability/Performance: There is no change in form, fit and function of the affected parts. The performance as well as the reliability remains unchanged. Min and Max values for the affected parts remain unchanged. However, the deviations in the typical performance are highlighted in the attached comparison report.

Nevertheless, we require customers to test the product in their applications.

Part Numbers/Series/Families Affected: Please see materials list on the succeeding page.

Vishay Brand(S): Vishay Semiconductors

Time Schedule:

Start Shipment Date: Monday December 7, 2026

Sample Availability: Samples available beginning of 15-Sept-2026. The exact shipment date for final products depends on the depletion of existing raw materials.

Product Identification: Product can be identified using lot code

Qualification Data: Available upon request.

This PCN is considered approved, without further notification, unless we receive specific customer concerns before Sunday October 4, 2026 or as specified by contract.

Issued By: Sourabh Kulkarni, sourabh.kulkarni@vishay.com



The DNA of tech.™

Product Change Notification



Product Group: OPT/Friday September 4, 2026/PCN-OPT-1503-2026-REV-0

VOR1142A6	VOR1142B6T	LH1526AACTR	LH1526AB	VOR2142A8
VOR2142B8T	VOR2142B8	LH1546ADFTR	VOR1142B4	VOR1142B4T
LH1500AAB	LH1500AABTR	LH1500AT	LH1525AAB	LH1525AABTR
LH1525AT	LH1540AAB	LH1540AAB-X001	LH1540AABTR	LH1540AT
LH1540AT-X001	LH1546AABTR	LH1546AT	VOR1142B6	LH1520AAC
LH1520AACTR	LH1520AB	LH1526AAC	LH1546ADF	



PCN OPT-1503-2026 Rev. 0

QUALIFICATION OF NEW CHIPSET AND BOM FOR 350V and 400V DIP6, DIP4 AND DIP8 SOLID STATE RELAY

Pre and Post PCN Comparison report for affected parts as below:

LH1500AAB, LH1500AABTR, LH1500AT,
LH1520AAC, LH1520AACTR, LH1520AB,
LH1525AABTR, LH1525AT,
LH1526AB, LH1526AAC, LH1526AACTR,
LH1540AABTR, LH1540AT,
LH1546ADFTR, LH1546ADF, LH1546AABTR, LH1546AT,
VOR1142A6, VOR1142B4T, VOR1142B6, VOR1142B6T,
VOR2142A8, VOR2142B8, VOR2142B8T



1. Comparison of electrical characteristics

Pre- PCN characteristics

ELECTRICAL CHARACTERISTICS ($T_{AMB} = 25^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
OUTPUT						
On-resistance (AC/DC configuration)	$I_F = 5\text{ mA}$, $I_L = 50\text{ mA}$	R_{ON}	-	22	27	Ω
Off-state leakage current	$I_F = 0\text{ mA}$, $V_L = \pm 100\text{ V}$	I_O	-	<1	200	nA
	$I_F = 0\text{ mA}$, $V_L = \pm 300\text{ V}$	I_O	-	6	1000	nA
Output capacitance (AC/DC configuration)	$I_F = 0\text{ mA}$, $V_L = 1\text{ V}$, 1 MHz	C_O	-	39	-	pF
	$I_F = 0\text{ mA}$, $V_L = \pm 50\text{ V}$, 1 MHz	C_O	-	6	-	pF
TRANSFER						
Capacitance (input to output)	$V_{ISO} = 1\text{ V}$	C_{IO}	-	0.4	-	pF

Post- PCN characteristics

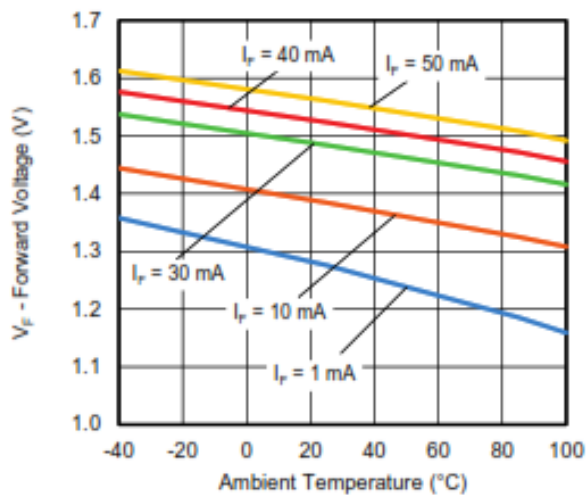
ELECTRICAL CHARACTERISTICS ($T_{AMB} = 25^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
OUTPUT						
On-resistance (AC/DC configuration)	$I_F = 5\text{ mA}$, $I_L = 50\text{ mA}$	R_{ON}	-	18	27	Ω
Off-state leakage current	$I_F = 0\text{ mA}$, $V_L = \pm 100\text{ V}$	I_O	-	<0.25	200	nA
	$I_F = 0\text{ mA}$, $V_L = \pm 300\text{ V}$	I_O	-	0.5	1000	nA
Output capacitance (AC/DC configuration)	$I_F = 0\text{ mA}$, $V_L = 1\text{ V}$, 1 MHz	C_O	-	44	-	pF
	$I_F = 0\text{ mA}$, $V_L = \pm 50\text{ V}$, 1 MHz	C_O	-	6	-	pF
TRANSFER						
Capacitance (input to output)	$V_{ISO} = 1\text{ V}$	C_{IO}	-	1.72	-	pF



2. Typical curves: Below typical curves have been updated in the respective product datasheets.

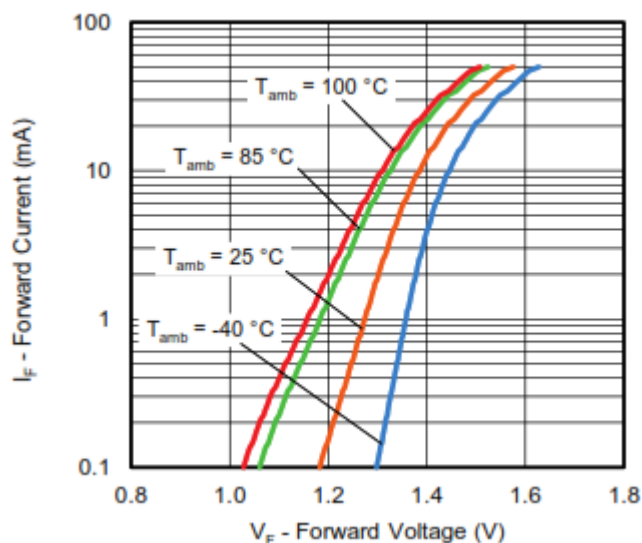
a. Forward voltage vs ambient temperature

(For parts with $T_{amb} \text{ max} = 85^{\circ}\text{C}$, the below diagram will be restricted to 85°C on X-axis)



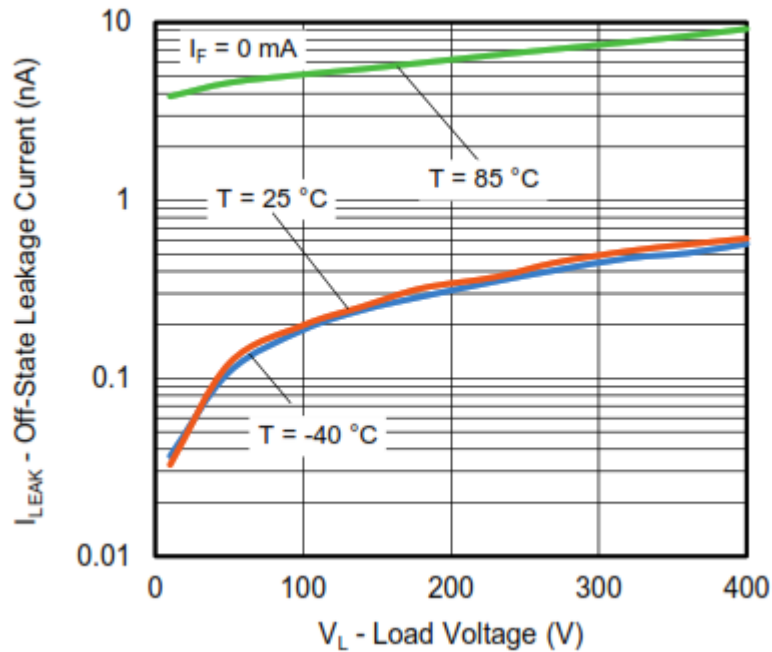
b. Forward current vs forward voltage

(For parts with $T_{amb} \text{ max} = 85^{\circ}\text{C}$, the $T_{amb} = 100^{\circ}\text{C}$ curve is not applicable)





c. Off-state leakage current vs load voltage



Nevertheless, it is recommended to check the actual datasheet online for the latest specifications. In addition, we require to test the products in your applications.

For any questions, please reach out to optocoupleranswers@vishay.com.