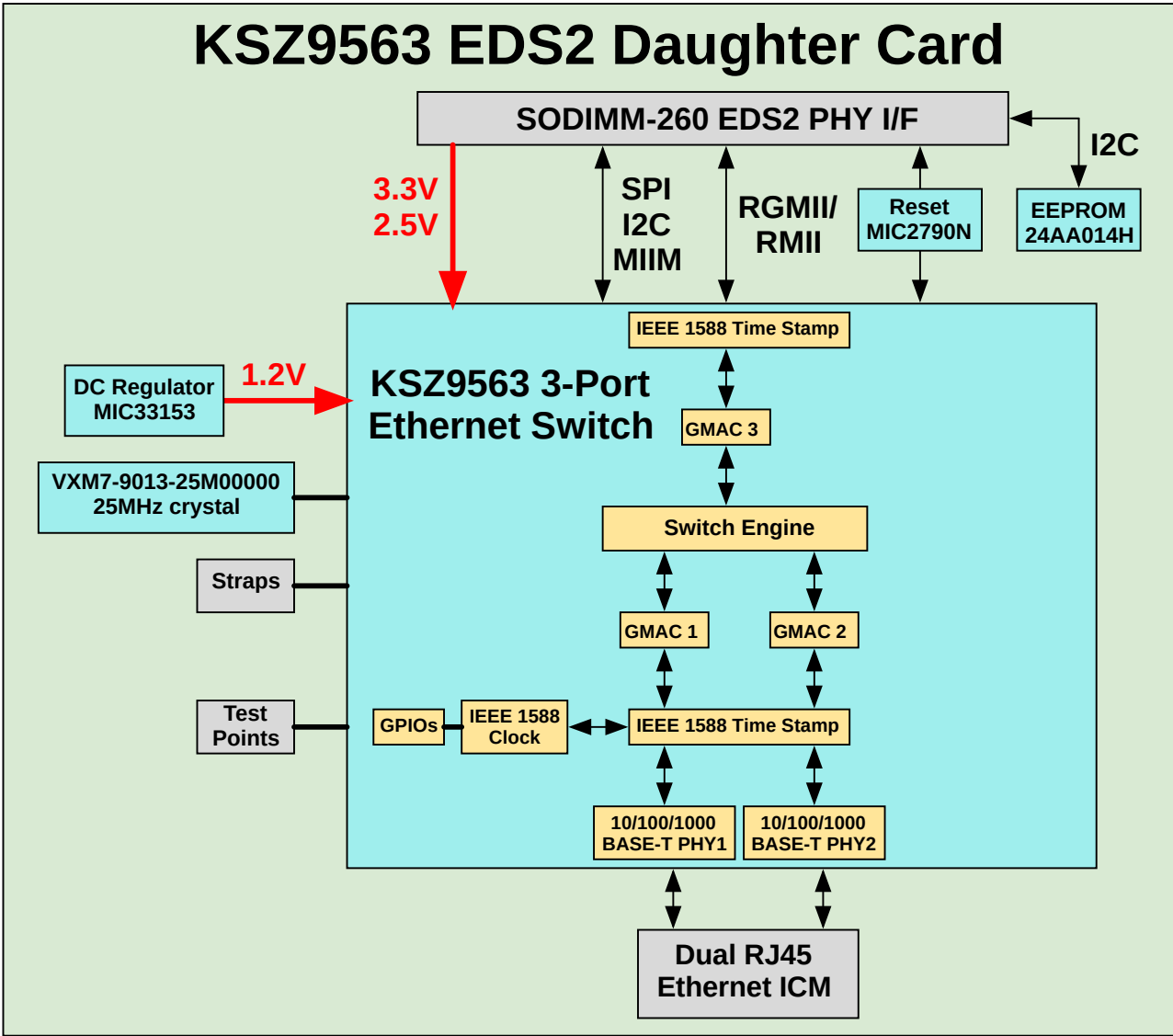
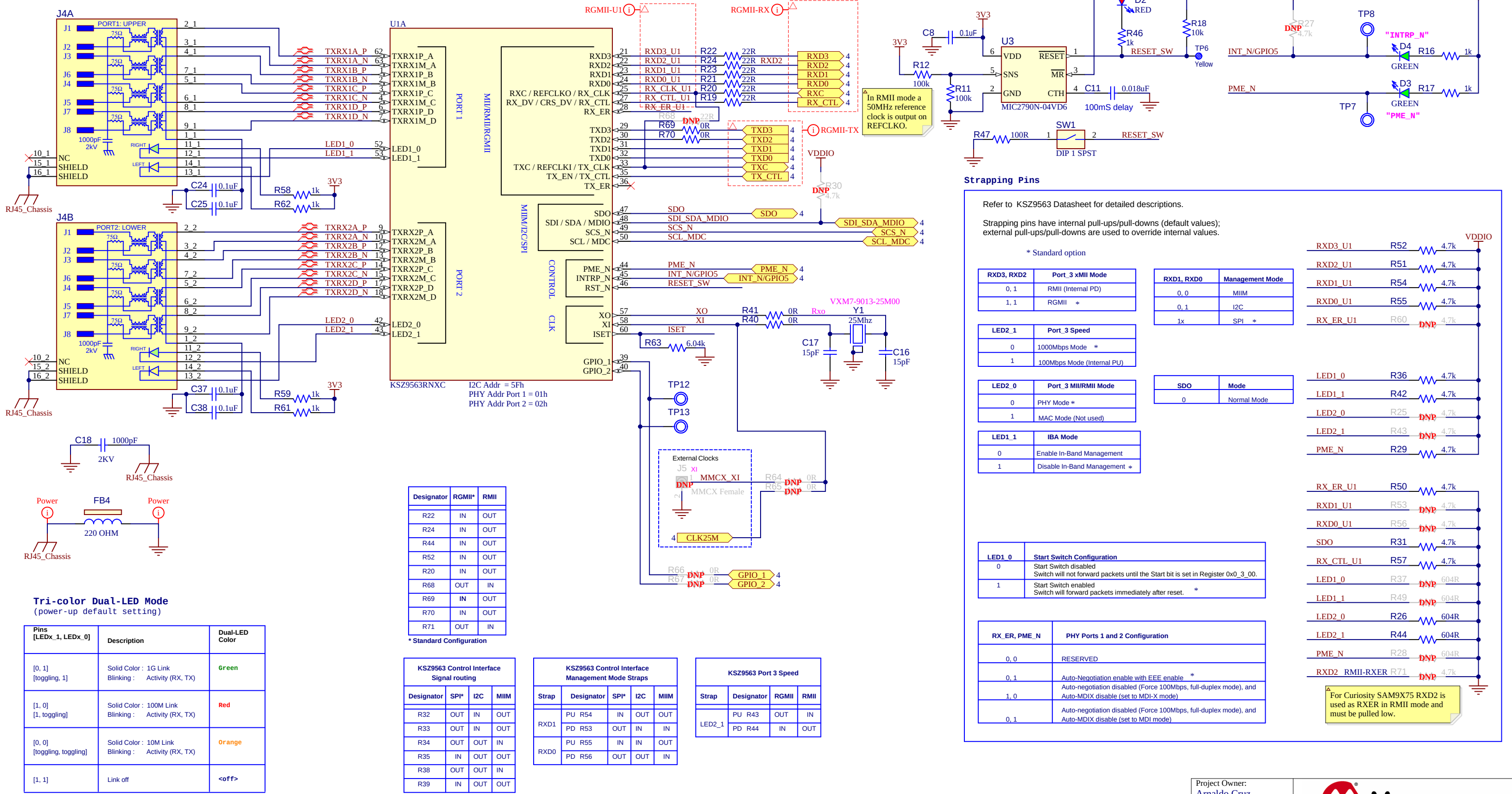


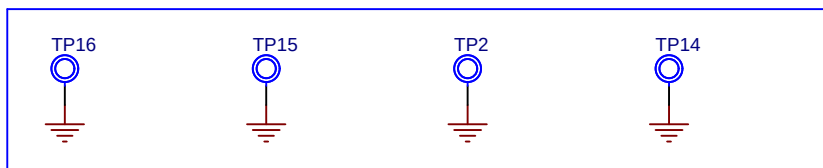
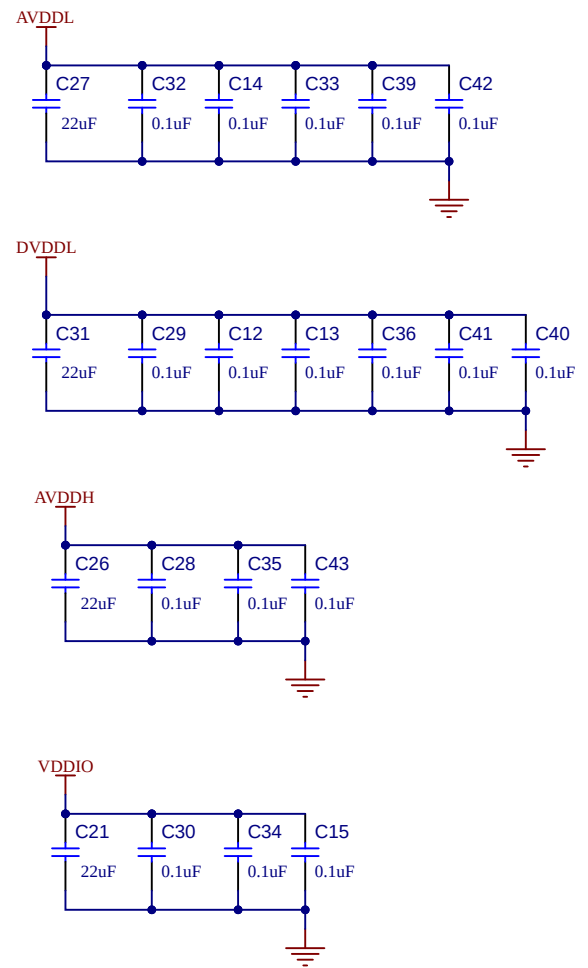
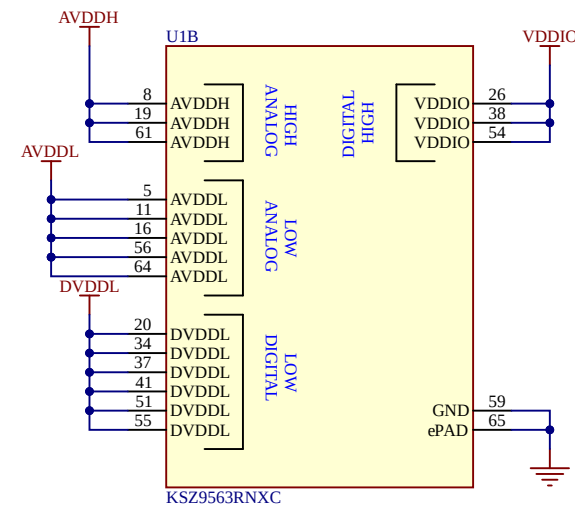
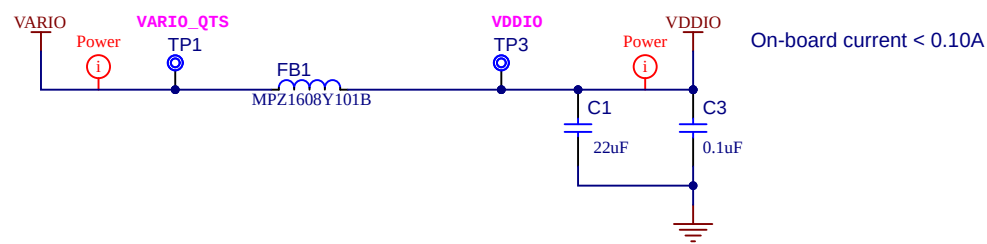
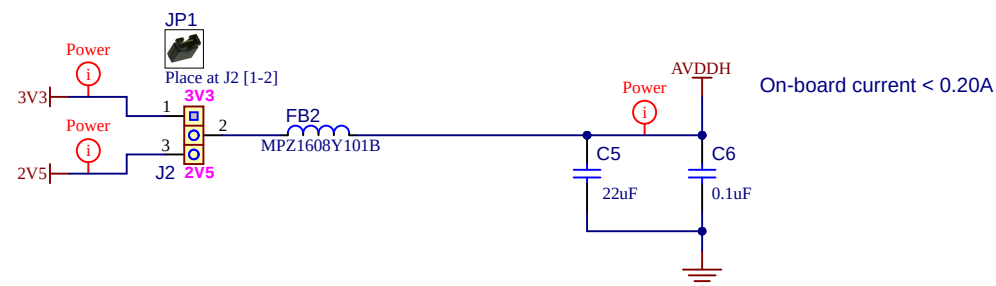
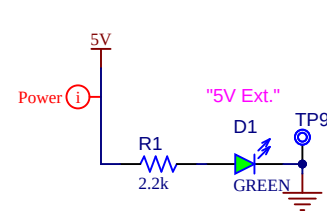
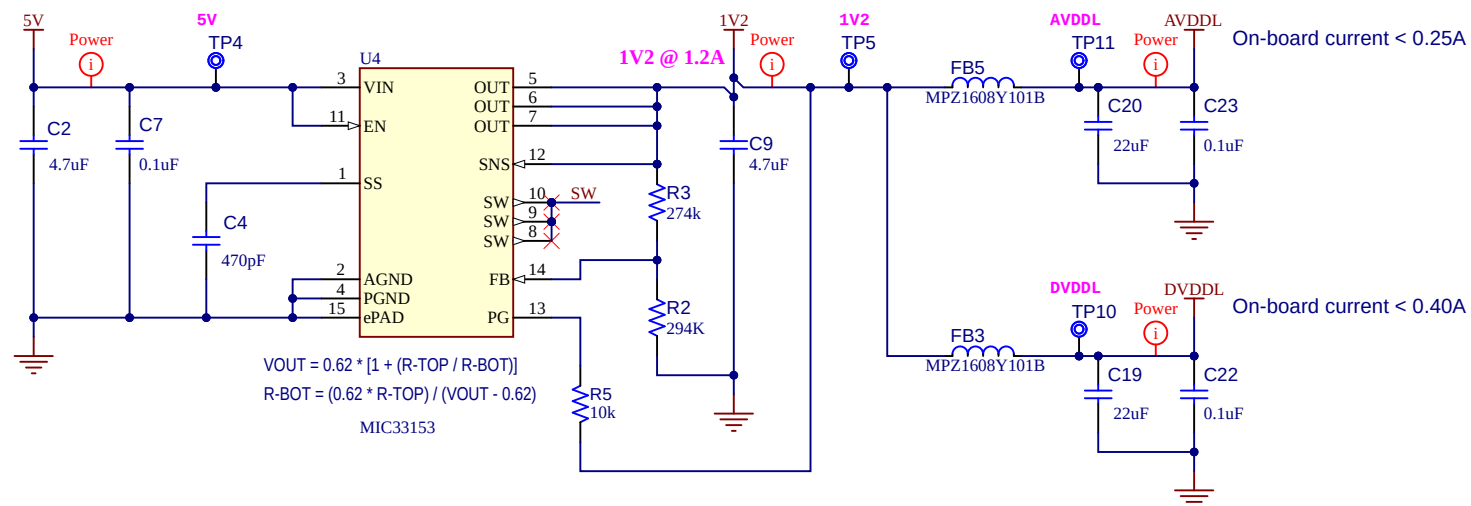
KSZ9563 EDS2 Daughter Card

Table of Contents	
Sheet	Description
1	Table of Contents, Revision History, Block Diagram
2	KSZ9563, Ethernet Ports, Reset, Configuration Straps
3	KSZ9563 Power
4	SODIMM EDS2 Edge Connector

Revision History			
Revision	Date	Revision Summary	Author
A	05/30/2023	Initial Release	Sam Charles
00B	08/27/2024	Added RMII option, changed default assembly to SPI control interface.	Arnaldo Cruz
B	09/09/2024	Updated as per review comments.	Arnaldo Cruz
R2	01/15/2025	Released for production	Arnaldo Cruz







Project Owner: Araldo Cruz		 MICROCHIP		
PCB Layout Contact: Sam Charles				
PartNumber: EV35V13A	Project Title <i>KSZ9563 EDS2 Daughter Card</i>	Variant: <u>Standard</u>		
Sheet Title KSZ9563 Power			<i>Designed with</i>  Altium.com	
Size Tabloid	SCH #: 02-00888	Rev: 2		Date: 2/28/2025
	PCB #: 04-11964	Rev: 2		Sheet 3 of 4
File: 3_KSZ9563_Power_UNG8425_B.SchDoc				

