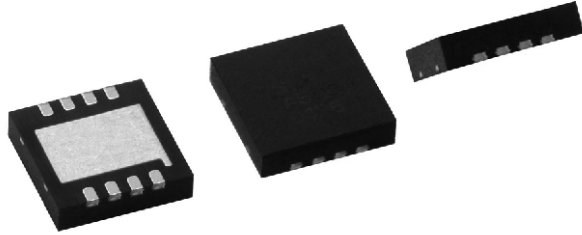


Dual Flat No Lead Molded Surface Mount Precision Thin Film Resistor Network



The DFN series of thin film precision dividers surface mount resistor networks offer a wide ratio range that is listed in the standard resistance offering table. The 4 mm x 4 mm 0.8 mm pitch dual flat no lead package feature 50 % savings in board space over traditional SOIC packages. The DFN dividers are ideal for applications that require tight TC tracking and ratio tolerances over temperature.

FEATURES

- 0.8 mm lead pitch
- MSL level 1 per J-STD-020
- Low profile 1 mm seated height
- Small size 4 mm x 4 mm size 50 % board savings over SOIC packages
- Low TCR ± 25 ppm, TC tracking to ± 5 ppm
- Compliant to RoHS directive 2002/95/EC

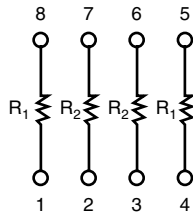


RoHS
COMPLIANT

TYPICAL PERFORMANCE

	ABS	TRACKING
TCR	25	5
	ABS	RATIO
TOL.	0.1	0.05

SCHEMATIC



STANDARD RESISTANCE OFFERING (R₁/R₂)

RATIO	R ₁	R ₂
100:1	100K	1K
50:1	50K	1K
25:1	25K	1K
20:1	20K	1K
10:1	10K	1K
5:1	10K	2K
2:1	10K	5K

STANDARD ELECTRICAL SPECIFICATIONS

TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	
TCR	Tracking	± 5 ppm/ $^{\circ}$ C
	Absolute	± 25 ppm/ $^{\circ}$ C
Tolerance	Absolute	± 0.1 %
	Ratio	± 0.05 %
Power Rating	Resistor	50 mW
	Package	50 mW x number of resistors
Stability	Δ R Absolute	500 ppm
	Δ R Ratio	150 ppm
Voltage Coefficient	< 0.1 ppm/V	
Working Voltage	50 V	
Operating Temperature Range	- 55 $^{\circ}$ C to + 125 $^{\circ}$ C	
Storage Temperature Range	- 55 $^{\circ}$ C to + 150 $^{\circ}$ C	
Noise	< - 30 dB	
Thermal EMF	< 0.08 μ V/ $^{\circ}$ C	
Shelf Life Stability	Absolute	1 year at + 25 $^{\circ}$ C
	Ratio	20 ppm

DFN Divider

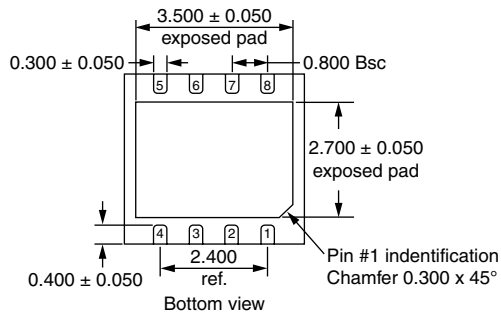
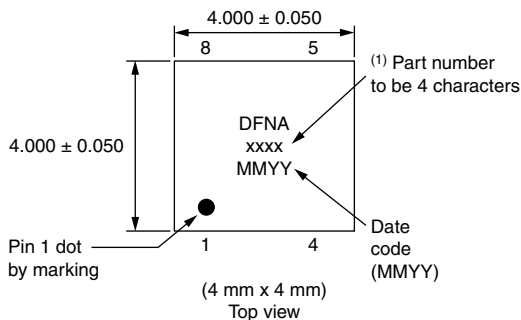


Vishay Thin Film

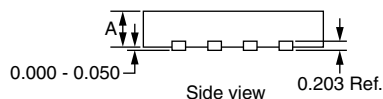
Dual Flat No Lead

Molded Surface Mount Precision Thin Film Resistor Network

DIMENSIONS AND IMPRINTING in millimeters



A	Max.	0.900
	Nom.	0.850
	Min.	0.800



Note

(1) 100-1 resistance ratio part marking to be 100-

- Contact factory for package outlines for higher pin count or custom configurations

MECHANICAL SPECIFICATIONS	
Resistive Element	Passivated nichrome
Substrate Material	Silicon
Body	Molded epoxy
Terminals	Copper alloy
Plating	100 % matte tin plating standard
Marking resistance to solvents	Per MIL-PRF-914

GLOBAL PART NUMBER INFORMATION																					
<table border="1"> <tr> <td style="text-align: center;">D</td> <td style="text-align: center;">F</td> <td style="text-align: center;">N</td> <td style="text-align: center;">A</td> <td style="text-align: center;">5</td> <td style="text-align: center;">-</td> <td style="text-align: center;">1</td> <td style="text-align: center;">U</td> <td style="text-align: center;">F</td> </tr> </table>	D	F	N	A	5	-	1	U	F												
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