

ZXMP2120E5

200V P-CHANNEL ENHANCEMENT MODE MOSFET

SUMMARY

$V_{(BR)DSS} = -200V$; $R_{DS(ON)} = 28\Omega$; $I_D = -122mA$

DESCRIPTION

This 200V enhancement mode P-channel MOSFET provides users with a competitive specification offering efficient power handling capability, high impedance and is free from thermal runaway and thermally induced secondary breakdown. Applications benefiting from this device include a variety of Telecom and general high voltage circuits.

A 4 pin SOT23 version is also available (ZXMP2120G4).

FEATURES

- High voltage
- Low on-resistance
- Fast switching speed
- Low gate drive
- Low threshold
- SOT23-5 package variant engineered to increase spacing between high voltage pins.

APPLICATIONS

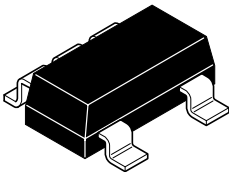
- Active clamping of primary side MOSFETs in 48 volt DC-DC converters

ORDERING INFORMATION

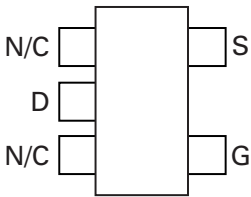
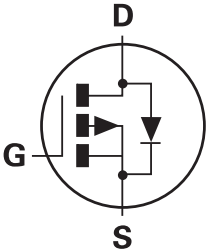
DEVICE	REEL SIZE (inches)	TAPE WIDTH (mm)	QUANTITY PER REEL
ZXMP2120E5TA	7	8mm embossed	3,000 units

DEVICE MARKING

- P120



SOT23-5



PINOUT - TOP VIEW

ZXMP2120E5

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DSS}	-200	V
Gate Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($V_{GS}=10V$; $T_{amb}=25^{\circ}C$) ^(a)	I_D	-122	mA
Pulsed Drain Current (c)	I_{DM}	-0.7	A
Pulsed Source Current (Body Diode) ^(c)	I_{SM}	-0.7	A
Power Dissipation at $T_{amb}=25^{\circ}C$ ^(a)	P_D	0.75	W
Linear Derating Factor		6	mW/ $^{\circ}C$
Operating and Storage Temperature Range	T_j : T_{stg}	-55 to +150	$^{\circ}C$

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient ^(a)	$R_{\theta JA}$	167	$^{\circ}C/W$

NOTES

(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

(b) For a device surface mounted on FR4 PCB measured at $t \leq 5$ secs.

(c) Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

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ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ}\text{C}$ unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	-200			V	I _D =-1mA, V _{GS} =0V
Gate-Source Threshold Voltage	V _{GS(th)}	-1.5		-3.5	V	I _D =-1mA, V _{DS} = V _{GS}
Gate-Body Leakage	I _{GSS}			20	nA	V _{GS} = ±20V, V _{DS} =0V
Zero Gate Voltage Drain Current	I _{DSS}			-10 -100	μA μA	V _{DS} =-200 V, V _{GS} =0 V _{DS} =-160 V, V _{GS} =0V, T=125°C ⁽²⁾
On-State Drain Current ⁽¹⁾	I _{D(on)}	-300			mA	V _{DS} =-25 V, V _{GS} =-10V
Static Drain-Source On-State Resistance ⁽¹⁾	R _{DS(on)}			28	Ω	V _{GS} =-10V, I _D =-150mA
Forward Transconductance ⁽¹⁾⁽²⁾	g _{fs}	50			mS	V _{DS} =-25V,I _D =-150mA
DYNAMIC						
Input Capacitance ⁽²⁾	C _{iss}			100	pF	V _{DS} =-25 V, V _{GS} =0V, f=1MHz
Output Capacitance ⁽²⁾	C _{oss}			25	pF	
Reverse Transfer Capacitance ⁽²⁾	C _{rss}			7	pF	
SWITCHING						
Turn-On Delay Time ^{(2) (3)}	t _{d(on)}			7	ns	V _{DD} =-25V, I _D =-150mA
Rise Time ⁽²⁾⁽³⁾	t _r			15	ns	
Turn-Off Delay Time ^{(2) (3)}	t _{d(off)}			12	ns	
Fall Time ⁽²⁾⁽³⁾	t _f			15	ns	

NOTES:

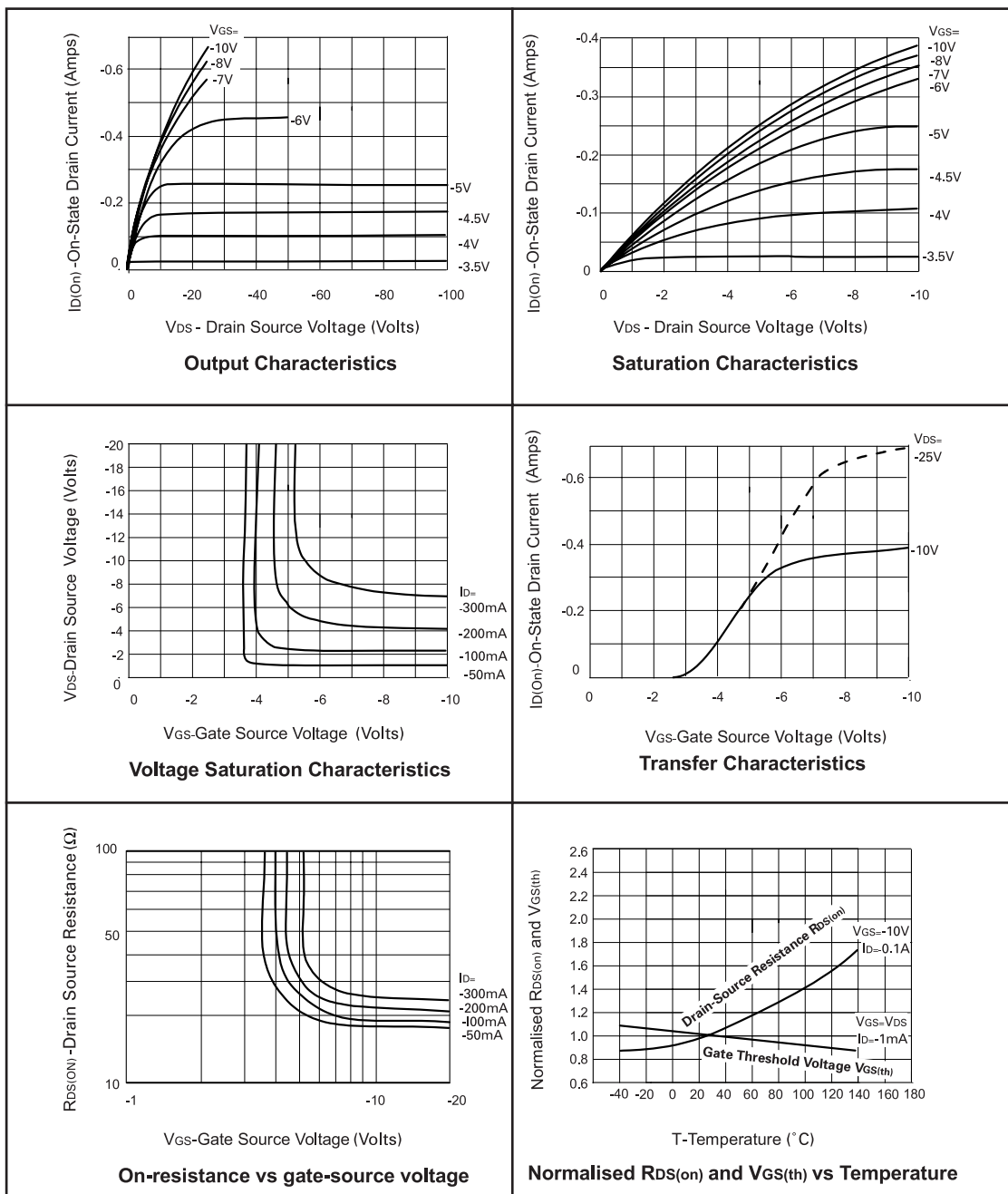
(1) Measured under pulsed conditions. Width=300 μs . Duty cycle $\leq 2\%$.

(2) Sample test.

(3) Switching times measured with 50 Ω source impedance and <5ns rise time on a pulse generator.

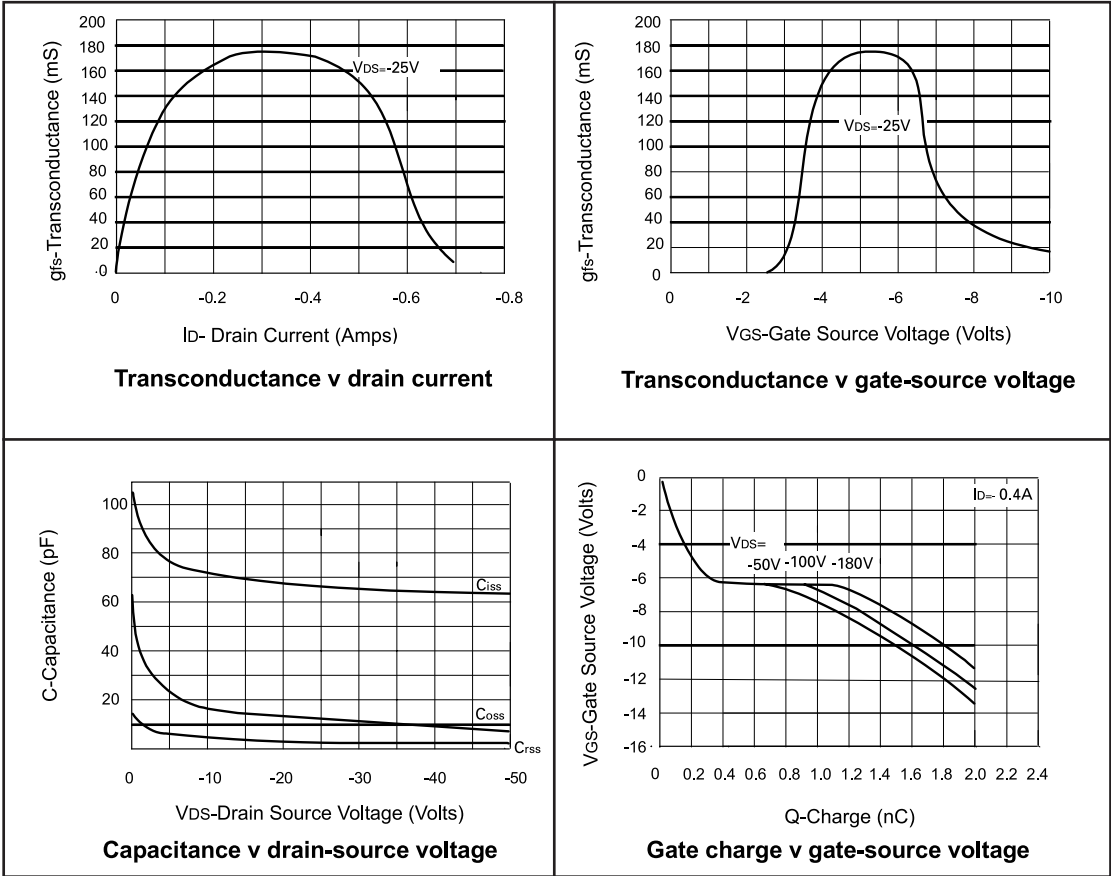
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TYPICAL CHARACTERISTICS



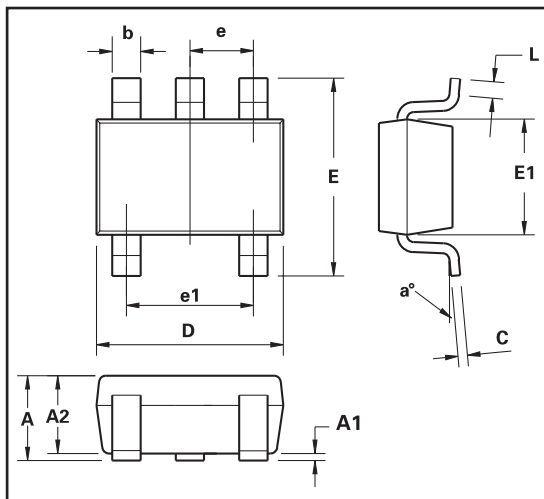
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CHARACTERISTICS

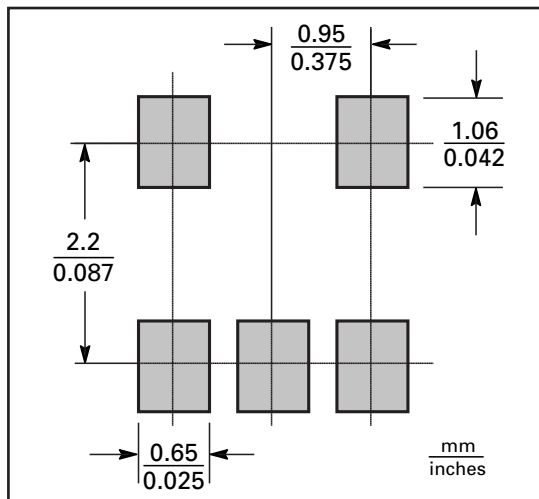


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PACKAGE OUTLINE



PAD LAYOUT DETAILS



Controlling dimensions are in millimeters. Approximate conversions are given in inches

PACKAGE DIMENSIONS

DIM	Millimeters		Inches	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.45	0.0354	0.0570
A1	-	0.15	-	0.0059
A2	0.90	1.30	0.0354	0.0511
b	0.20	0.50	0.0078	0.0196
C	0.09	0.26	0.0035	0.0102
D	2.70	3.10	0.1062	0.1220

DIM	Millimeters		Inches	
	MIN.	MAX.	MIN.	MAX.
E	2.20	3.20	0.0866	0.1181
E1	1.30	1.80	0.0511	0.0708
e	0.95 REF		0.0374 REF	
e1	1.90 REF		0.0748 REF	
L	0.10	0.60	0.0039	0.0236
a	0°	30°	0°	30°

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