Unit: mm

TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process)

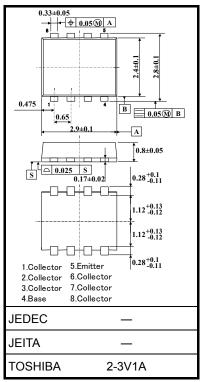
TPCP8505

High-Speed Switching Applications DC-DC Converter Applications

- High DC current gain: $h_{FE} = 400 \text{ to } 1000 \text{ (IC} = 0.3 \text{ A)}$
- Low collector-emitter saturation: $V_{CE (sat)} = 0.14 \text{ V (max)}$
- High-speed switching: tf = 120 ns (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Collector-base voltage		V_{CBO}	100	V	
Collector-emitter voltage		V _{CEX}	80	V	
		V_{CEO}	50		
Emitter-base voltage		V_{EBO}	7	V	
Collector current	DC (Note 1)	Ic	3.0	Α	
	Pulse (Note 1)	I _{CP}	5.0		
Base current		Ι _Β	0.3	Α	
Collector power dissipation (t = 10 s)	t = 10s	P _C (Note 2)	3.0	W	
	DC	FC (Note 2)	1.25		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



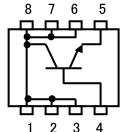
Weight: 0.017 g (typ.)

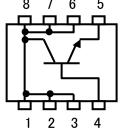
- Note 1: Ensure that the junction temperature does not exceed 150°C during use of this device.
- Note 2: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)
- Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

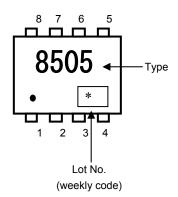
Figure 1. Circuit Configuration (top view)

Figure 2. Marking (Note 4)

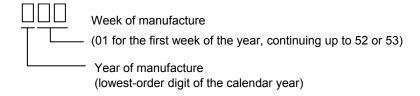




Note 4: • on the lower left of the marking indicates Pin 1.



* Weekly code (three digits):

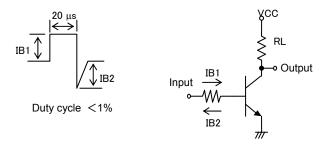


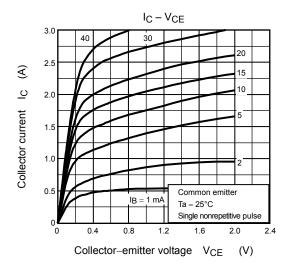
Electrical Characteristics (Ta = 25°C)

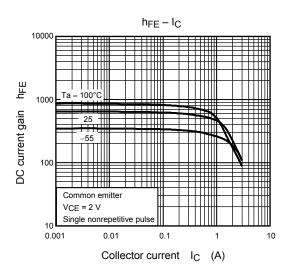
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	V _{CB} = 100 V, I _E = 0	_	_	100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = 7 \text{ V, } I_{C} = 0$	_	_	100	nA
Collector-base breakdown voltage		V (BR) CBO	$I_C = 1 \text{ mA}, I_B = 0$	100	_	_	V
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	50	_	_	V
DC current gain		h _{FE} (1)	$V_{CE} = 2 \text{ V}, I_{C} = 0.3 \text{ A}$	400	_	1000	
		h _{FE} (2)	V _{CE} = 2 V, I _C = 1.0 A	200	_	_	
Collector-emitter saturation voltage		V _{CE} (sat)	I _C = 1 A, I _B = 20 mA	_	_	0.14	V
Base-emitter saturation voltage		V _{BE} (sat)	I _C = 1 A, I _B = 20 mA	_	_	1.1	V
Switching time	Rise time	t _r	See Figure 3 circuit diagram $V_{CC} \simeq 30 \text{ V}, \text{ R}_L = 30 \Omega$ $I_{B1} = -I_{B2} = 33 \text{ mA}$	_	40	_	
	Storage time	t _{stg}		_	500	_	ns
	Fall time	t _f		_	120	_	

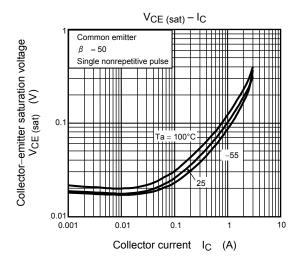
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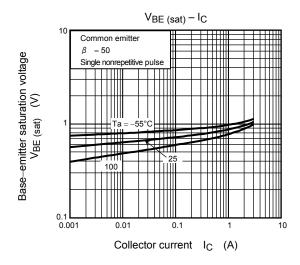
Figure 3. Switching Time Test Circuit & Timing Chart

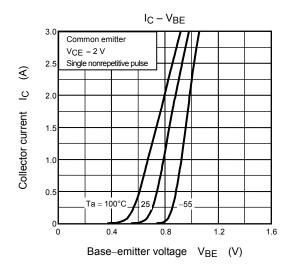




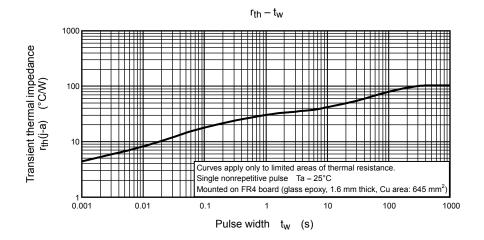




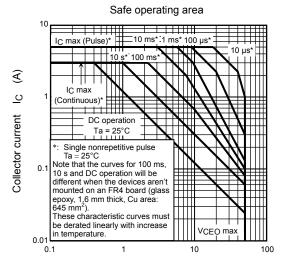




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Collector-emitter voltage V_{CE} (V)

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