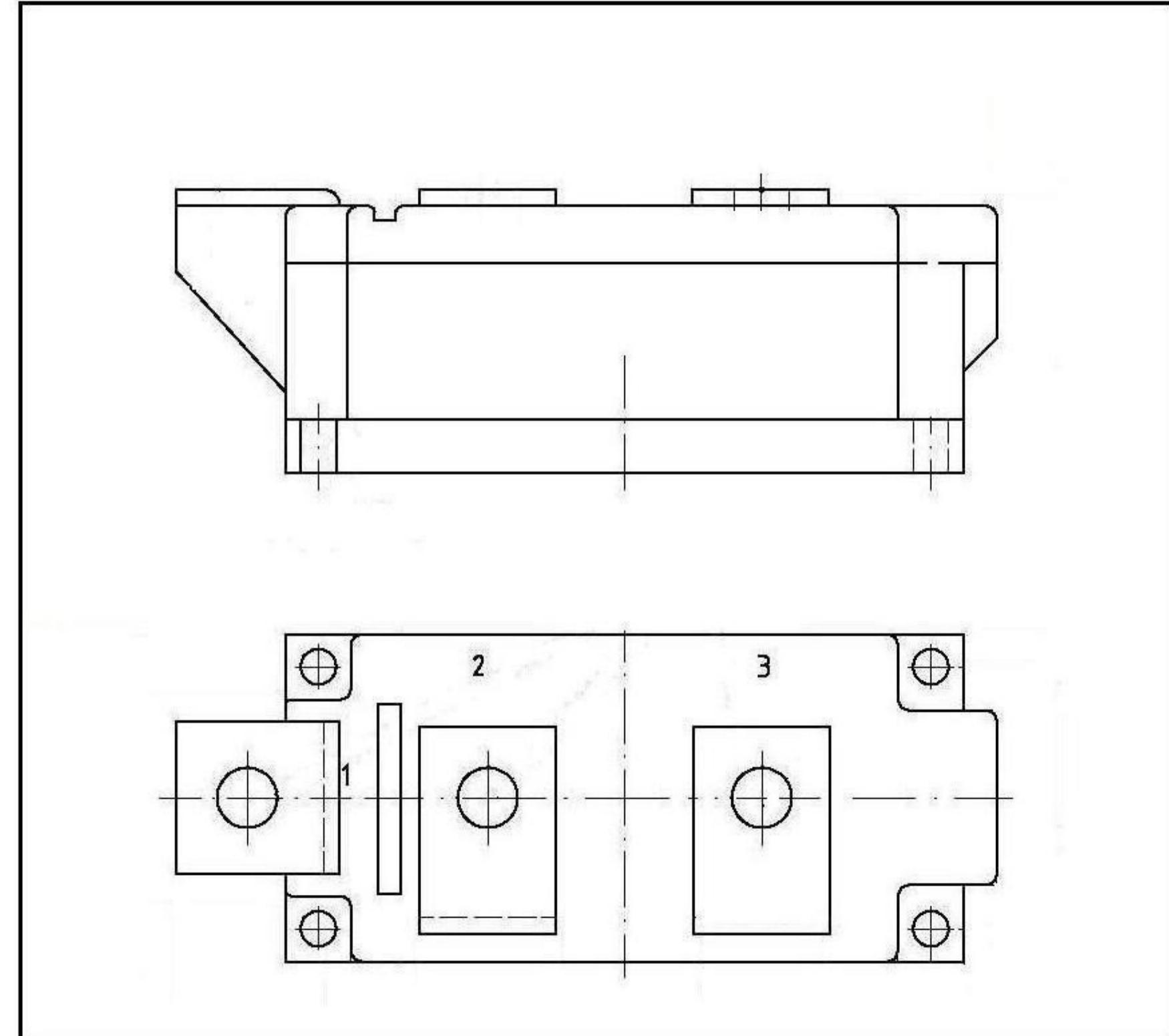


## IRK. 500 SERIES

# High Voltage Thyristor/Diode and Thyristor/Thyristor

### FEATURES

- ❖ Electrically isolated base plate.
- ❖ 3000 V<sub>RMS</sub> isolating voltage.
- ❖ Industrial standard package.
- ❖ Simplified mechanical designs, rapid assembly.
- ❖ High surge capability.
- ❖ Large creepage distances.



### DESCRIPTION

These IRK series of Power Modules use power thyristors/diodes in four basic configurations. The semiconductors are electrically isolated from the metal base, allowing common heatsinks and compact assemblies to be built. They can be interconnected to form single phase or three phase bridges or as AC-switches when modules are connected in anti-parallel.

These modules are intended for general purpose applications such as battery chargers, welders and plating equipment.

### MAJOR RATINGS & CHARACTERISTICS

Parameters	IRK.500	Units
I <sub>T(AV)</sub>	500	A
	460	
I <sub>T(RMS)</sub>	785	A
I <sub>TSM</sub>	14000	A
I <sup>2</sup> t	980	kA <sup>2</sup> s
V <sub>DRM</sub> - V <sub>RRM</sub>	1800 to 2200	V
T <sub>J</sub>	-40 to 125	°C

**IRK. 500 SERIES**
**ELECTRICAL SPECIFICATION**
**VOLTAGE RATINGS**

Type Number	Voltage Code	$V_{RRM} / V_{DRM}$ , max. repetitive peak reverse and off-state voltage blocking voltage V	$V_{RSM}$ , max. non-repetitive peak reverse voltage V	$I_{DRM} / I_{RRM}$ max. @ 130°C mA
IRK.500	18	1800	1900	100
	20	2000	2100	
	22	2200	2300	

**ON-STATE CONDUCTION**

	Parameters	IRK.500	Units	Conditions	
$I_{T(AV)}$	Max. average on-state current	500	A	180° conduction, half sine wave @ Case temperature	
	@ Case temperature	80	°C		
$I_{T(RMS)}$	Max. RMS on-state current	785	A	as AC switch	
$I_{TSM}$	Max. peak, one cycle on-state, non-repetitive surge current	14000	A	$t = 10ms$	Sinusoidal half wave, Initial $T_J = T_J$ max.
$I^2t$	Maximum $I^2t$ for fusing	980	kA²s	$t = 10ms$	Sinusoidal half wave, Initial $T_J = T_J$ max.
$V_{T(TO)}$	threshold voltage	0.88	V	$T_J = T_J$ max.	
$r_t$	on-state slope resistance	0.46	mΩ	$T_J = T_J$ max.	
$V_{TM}$	Max. on-state voltage drop	1.7	V	$I_T = 1700A, 25^\circ C$	
$I_H$	Maximum holding current	500 max,	mA		
$I_L$	Max. latching current	1000 max,	mA	$T_J = 25^\circ C \text{ RG}=33\Omega,$	

**SWITCING**

$t_d$	Delay Time	1.0	μs	$T_J = 25^\circ C$	Gate current = 1A $dIg/dt = 1A/\mu s$
$t_r$	Rise Time	2.0	μs	$T_J = 25^\circ C$	$Vd = 0.67\% V_{DRM}$
$t_q$	Turn-Off Time max,	300	μs	$T_J = T_J$ max.	

**IRK. 500 SERIES**
**BLOCKING**

	Parameter	IRK.500	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	1000	V/ $\mu$ s	$T_J = 130^\circ\text{C}$ , exponential to 67% rated $V_{\text{DRM}}$
$I_{\text{RRM}}$ $I_{\text{DRM}}$	Max. peak reverse and off-state leakage current	100	mA	$T_J = 130^\circ\text{C}$ , rated $V_{\text{DRM}}/V_{\text{RRM}}$ applied
$V_{\text{INS}}$	RMS isolation voltage	3500	V	50Hz,Circuit to base, all terminal shorted, $25^\circ\text{C}$ , 1sec

**TRIGGERING**

	Parameter	IRK.500	Units	Conditions
$I_{\text{GT}}$	DC gate current required to trigger	min 200	mA	$T_J = 25^\circ\text{C}$ Max. required gate trigger/current / voltage are the lowest value which will trigger all units 12V anode-to-cathode applied.
$V_{\text{GT}}$	DC gate voltage required to trigger	min 3.0	V	$T_J = 25^\circ\text{C}$
$V_{\text{GD}}$	DC gate voltage not to trigger	0.25 max	V	$T_J = 130^\circ\text{C}$ Max. gate current / voltage not to trigger the max. value which will not trigger any unit with rated $V_{\text{DRM}}$ anode-to-cathode applied
$I_{\text{GD}}$	DC gate current not to trigger	10.0 max	mA	$T_J = 130^\circ\text{C}$
di/dt	Maximum critical rate of rise of turned-on current	200	A/ $\mu$ s	$T_J = 130^\circ\text{C}$ ,

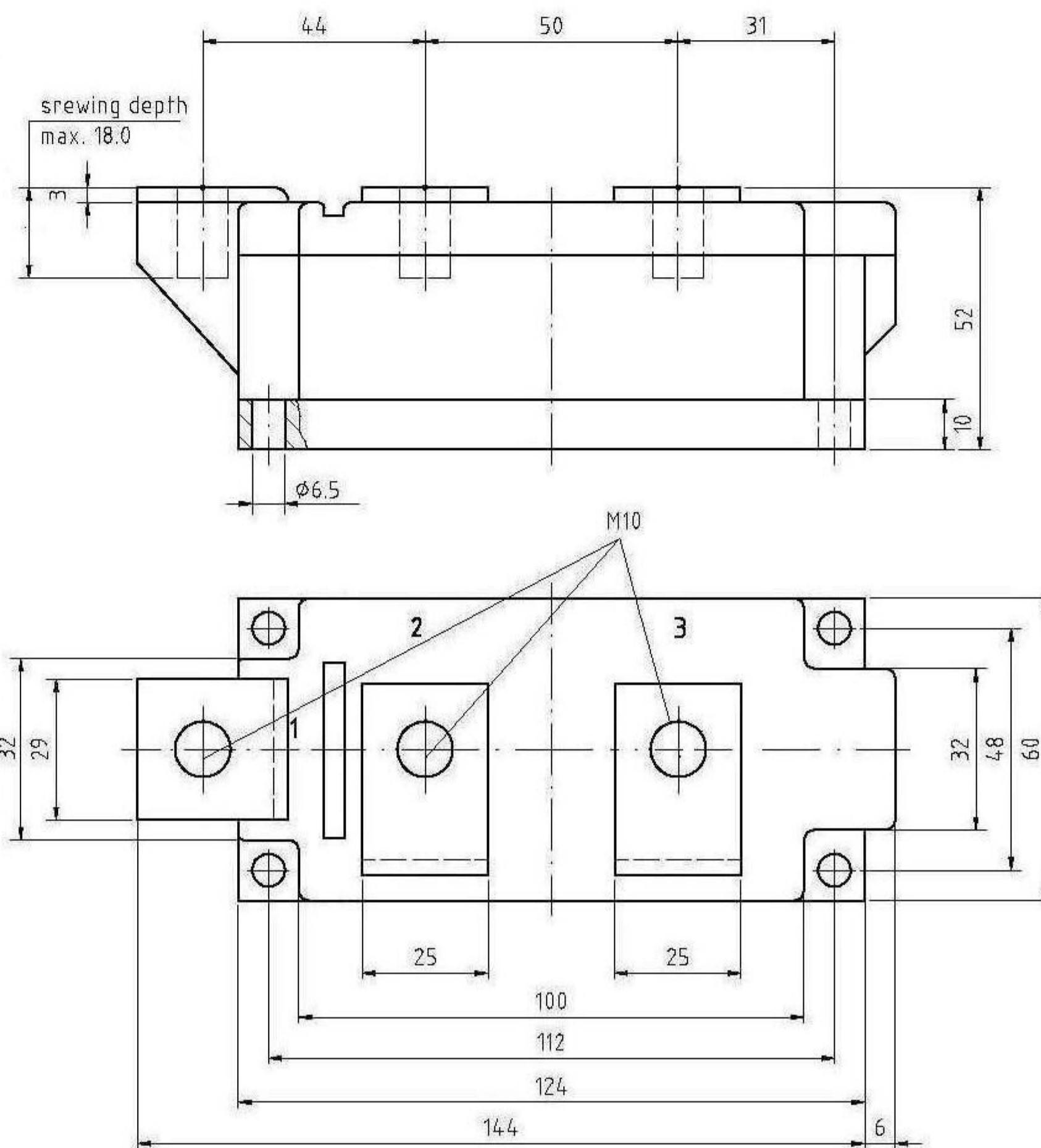
**THERMAL AND MECHANICAL SPECIFICATION**

	Parameter	IRK.500	Units	Conditions
$T_J$	Max. operating temperature range	-40 to 125	$^\circ\text{C}$	
$T_{\text{sig}}$	Max. storage temperature range	-40 to 125		
$R_{\text{thJ-C}}$	Max. thermal resistance, junction to case	0.062/0.031	K/W	Per thyristor / per module
$R_{\text{thJ-C}}$	Max. thermal resistance, junction to heatsink	0.02/0.01	K/W	Per thyristor / per module
T	Mounting torque, $\pm 15\%$	5 (12)	Nm	to heatsink & to (terminals)

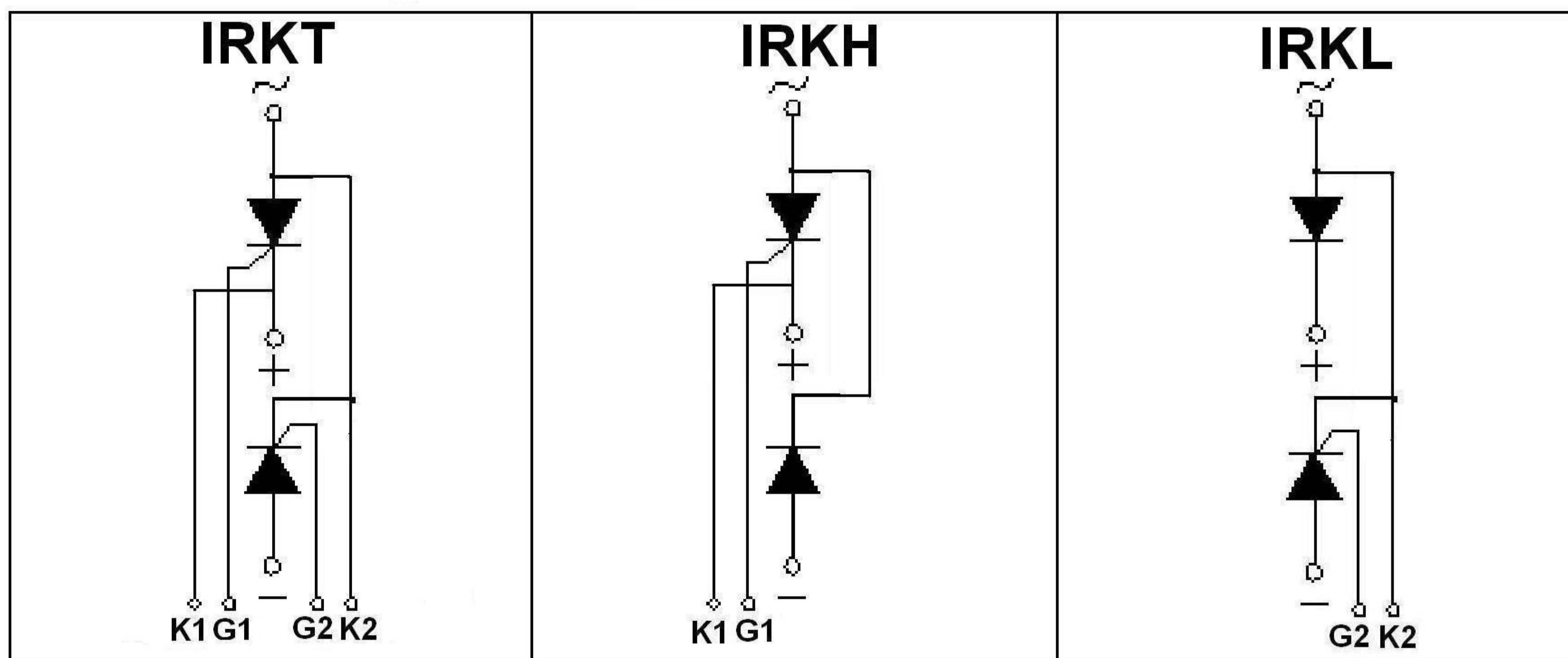
# POWER MODULES

## IRK. 500 SERIES

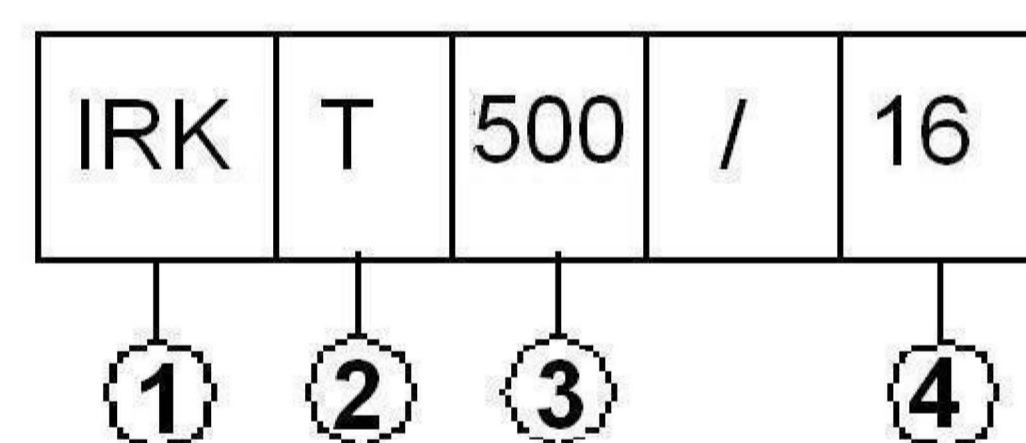
### OUTLINE DIAGRAM



### Circuit Configuration Table



### Ordering Information Table



- ① - Module type
- ② - Circuit configuration (See Circuit Configuration table)
- ③ - Current Code
- ④ - Voltage Code (See Voltage Ratings table)