

## N-channel SiC power MOSFET

$V_{DSS}$	1200V
R <sub>DS(on)</sub> (Typ.)	160m $\Omega$
I <sub>D</sub>	17A
$P_D$	103W

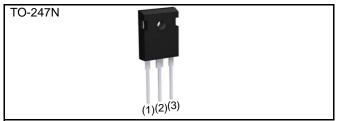
#### Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

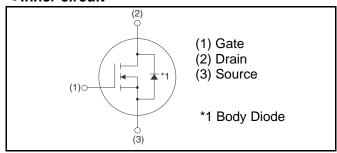
### Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

#### Outline



#### ●Inner circuit



Packaging specifications

	<del></del>	
	Packing	Tube
	Reel size (mm)	-
Type	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3160KL

### ◆Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{ m DSS}$	1200	V	
Continuous drain current	$T_c = 25^{\circ}C$	I <sub>D</sub> *1	17	А
Continuous drain current	$T_c = 100$ °C	I <sub>D</sub> *1	12	А
Pulsed drain current		I <sub>D,pulse</sub> *2	42	А
Gate - Source voltage		$V_{GSS}$	-4 to 22	V
Gate-Source Surge Voltage		$V_{GSS\_surge}$	−4 to 22	V
Recommended Drive Voltage		$V_{GS\_op}$	0 / 18	V
Junction temperature		T <sub>j</sub>	175	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

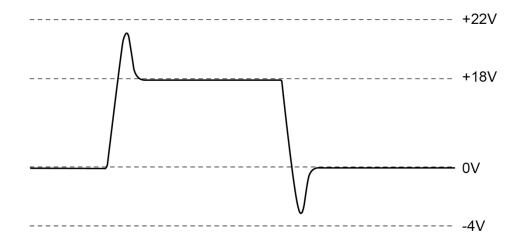
### ●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	
Thermal resistance, junction - case	$R_{thJC}$	-	1.12	1.46	°C/W

# •Electrical characteristics ( $T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$ , $I_D = 1mA$	1200	-	-	V
		$V_{DS} = 1200V, V_{GS} = 0V$				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25$ °C	-	1	10	μΑ
drain barront		T <sub>j</sub> = 150°C	-	2	-	
Gate - Source leakage current	$I_{GSS+}$	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I <sub>GSS</sub> _	$V_{GS} = -4V$ , $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V <sub>GS (th)</sub>	$V_{DS} = 10V, I_{D} = 2.5 \text{mA}$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 5A$				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *3	$T_j = 25$ °C	-	160	208	mΩ
		T <sub>j</sub> = 125°C	-	240	-	
Gate input resistance	$R_{G}$	f = 1MHz, open drain	-	18	-	Ω

## ●Example of acceptable Vgs waveform



# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Symbol Conditions -		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g <sub>fs</sub> *3	$V_{DS} = 10V, I_{D} = 5A$	-	2.5	-	S
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0V$	-	398	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 800V	-	41	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	18	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V$ to 600V	-	45	-	pF
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} = 400V, I_{D} = 5A$	-	14	-	
Rise time	t <sub>r</sub> *3	V <sub>GS</sub> = 18V/0V	-	18	-	no
Turn - off delay time	t <sub>d(off)</sub> *3	$R_L = 80\Omega$	-	24	-	ns
Fall time	t <sub>f</sub> *3	$R_G = 0\Omega$	-	25	-	
Turn - on switching loss	E <sub>on</sub> *3	$V_{DD} = 600V, I_{D} = 5A$ $V_{GS} = 18V/0V$	-	62	-	1
Turn - off switching loss	E <sub>off</sub> *3	R <sub>G</sub> = 0Ω L=750μH *E <sub>on</sub> includes diode reverse recovery	-	12	-	μJ

# ● Gate Charge characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol Condition	Conditions	Values			Unit
		Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*3}$	V <sub>DD</sub> = 600V	-	42	ı	
Gate - Source charge	Q <sub>gs</sub> *3	I <sub>D</sub> = 5A	-	11	-	nC
Gate - Drain charge	Q <sub>gd</sub> *3	V <sub>GS</sub> = 18V	-	18	-	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 600V, I_D = 5A$	-	9.6	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> PW  $\leq$  10  $\mu s,$  Duty cycle  $\leq$  1%

<sup>\*3</sup> Pulsed

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol Conditions		Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l <sub>S</sub> *1	-T <sub>c</sub> = 25°C	-	1	17	А
Inverse diode direct current, pulsed	I <sub>SM</sub> *2		-	-	42	А
Forward voltage	V <sub>SD</sub> *3	$V_{GS} = 0V, I_{S} = 5A$	-	3.2	-	V
Reverse recovery time	t <sub>rr</sub> *3	I <sub>F</sub> =5A, V <sub>R</sub> = 600V di/dt = 1100A/μs	-	13	1	ns
Reverse recovery charge	Q <sub>rr</sub> *3		-	26	-	nC
Peak reverse recovery current	I <sub>rrm</sub> *3		-	4	-	Α

Fig.1 Power Dissipation Derating Curve

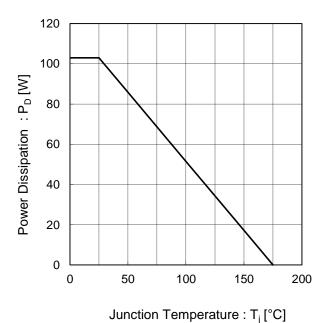
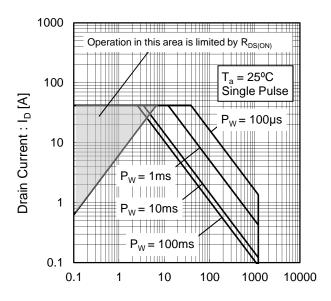
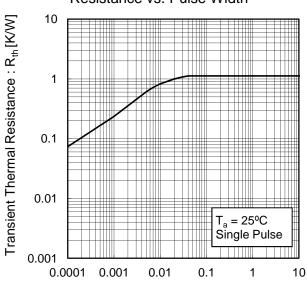


Fig.2 Maximum Safe Operating Area



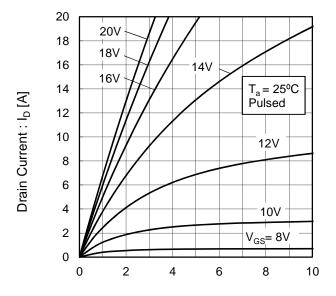
Drain - Source Voltage :  $V_{DS}$  [V]

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



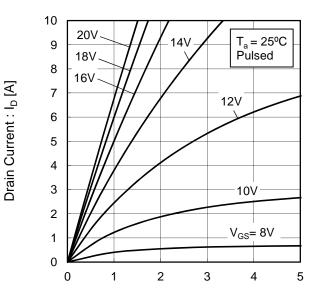
Pulse Width :  $P_W$  [s]

Fig.4 Typical Output Characteristics(I)

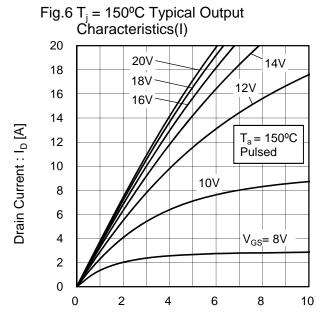


Drain - Source Voltage : V<sub>DS</sub> [V]

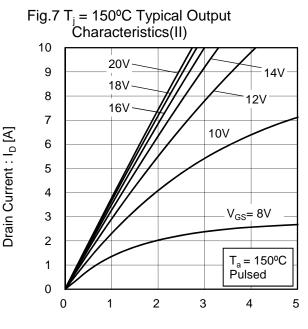
Fig.5 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]

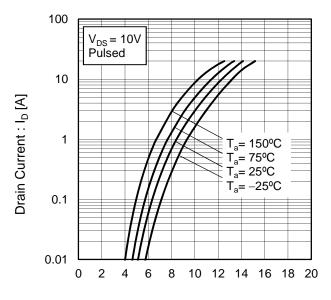


Drain - Source Voltage : V<sub>DS</sub> [V]



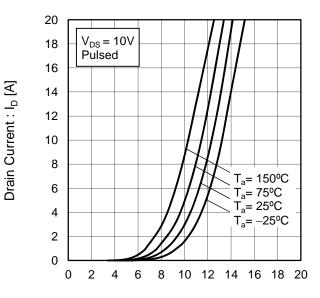
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.8 Typical Transfer Characteristics (I)



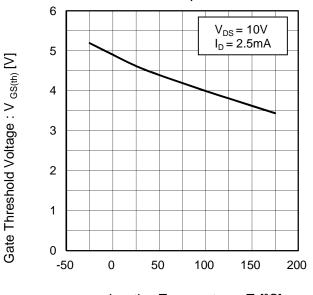
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.9 Typical Transfer Characteristics (II)



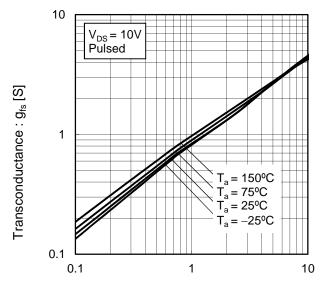
Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

Fig.11 Transconductance vs. Drain Current



Drain Current : I<sub>D</sub> [A]

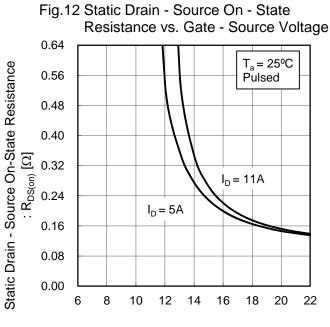
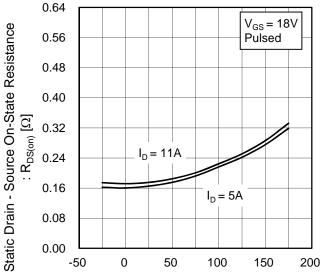
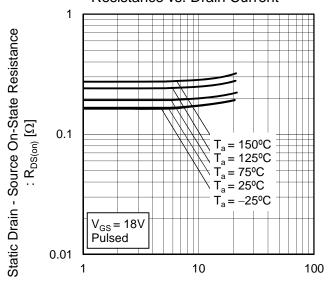


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature



Gate - Source Voltage :  $V_{GS}[V]$  Junction Temperature :  $T_i[^{\circ}C]$ 

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current



Drain Current: I<sub>D</sub> [A]

 $T_a = 25$ °C f = 1MHz

 $V_{GS} = 0V$ 

1

0.1

Fig.15 Typical Capacitance
vs. Drain - Source Voltage

10000

1000

Coss

Tr = 25°C

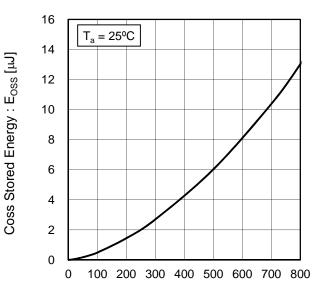
Drain - Source Voltage : V<sub>DS</sub> [V]

100

1000

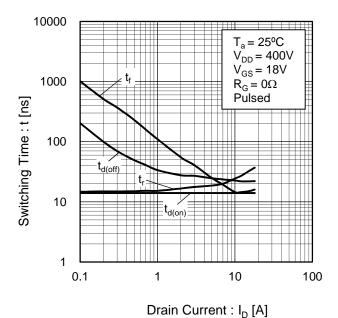
10

Fig.16 Coss Stored Energy



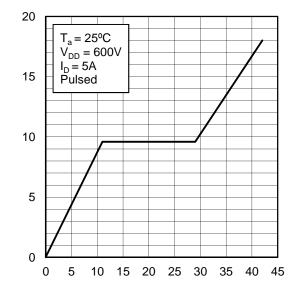
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.17 Switching Characteristics

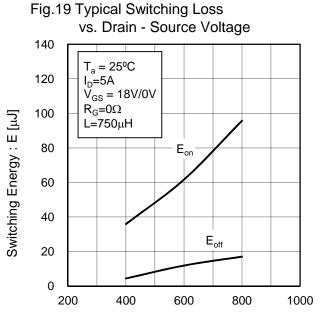


Sate - Source Voltage : V<sub>GS</sub> [V]

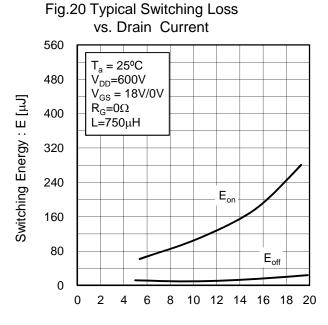
Fig.18 Dynamic Input Characteristics



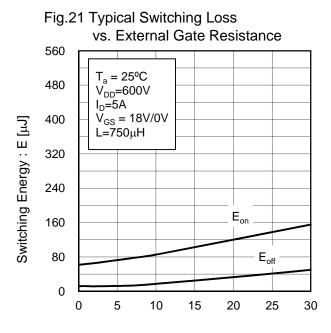
Total Gate Charge : Q<sub>g</sub> [nC]



Drain - Source Voltage :  $V_{DS}$  [V]

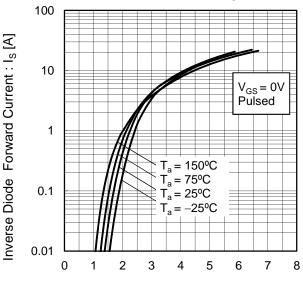


Drain Current : I<sub>D</sub> [A]



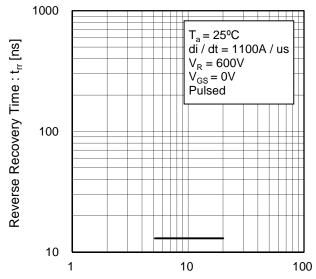
External Gate Resistance :  $R_G [\Omega]$ 

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage :  $V_{SD}$  [V]

Fig.23 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

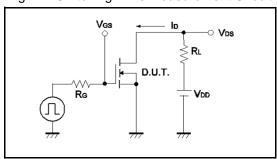


Fig.2-1 Gate Charge Measurement Circuit

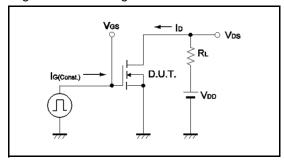


Fig.3-1 Switching Energy Measurement Circuit

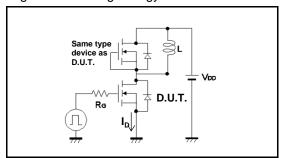


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

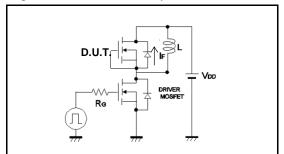


Fig.1-2 Switching Waveforms

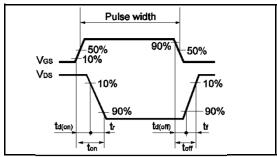


Fig.2-2 Gate Charge Waveform

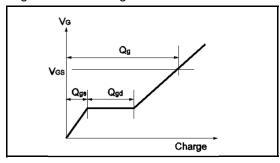
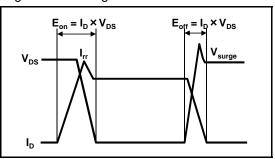
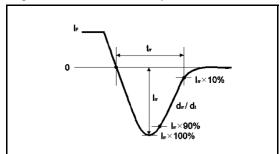


Fig.3-2 Switching Waveforms





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# SCT3160KL - Web Page

**Distribution Inventory** 

Part Number	SCT3160KL
Package	TO-247N
Unit Quantity	450
Minimum Package Quantity	30
Packing Type	Tube
Constitution Materials List	inquiry
RoHS	Yes