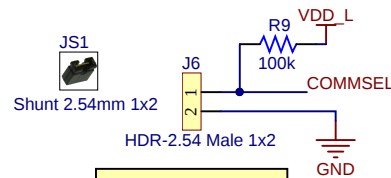
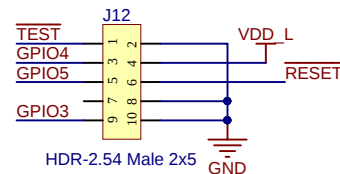
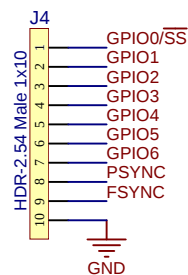


JTAG

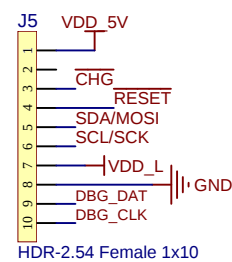


Comms Select
(VDD=I2C; GND=SPI)
Default I2C
Fit JS1 to enable SPI

GPIOs

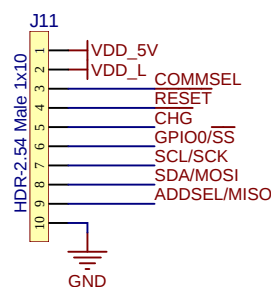


USB Bridge Board

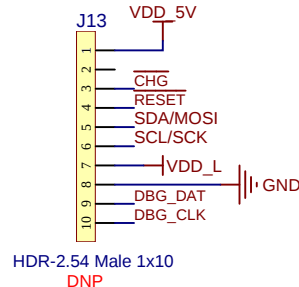


HDR-2.54 Female 1x10

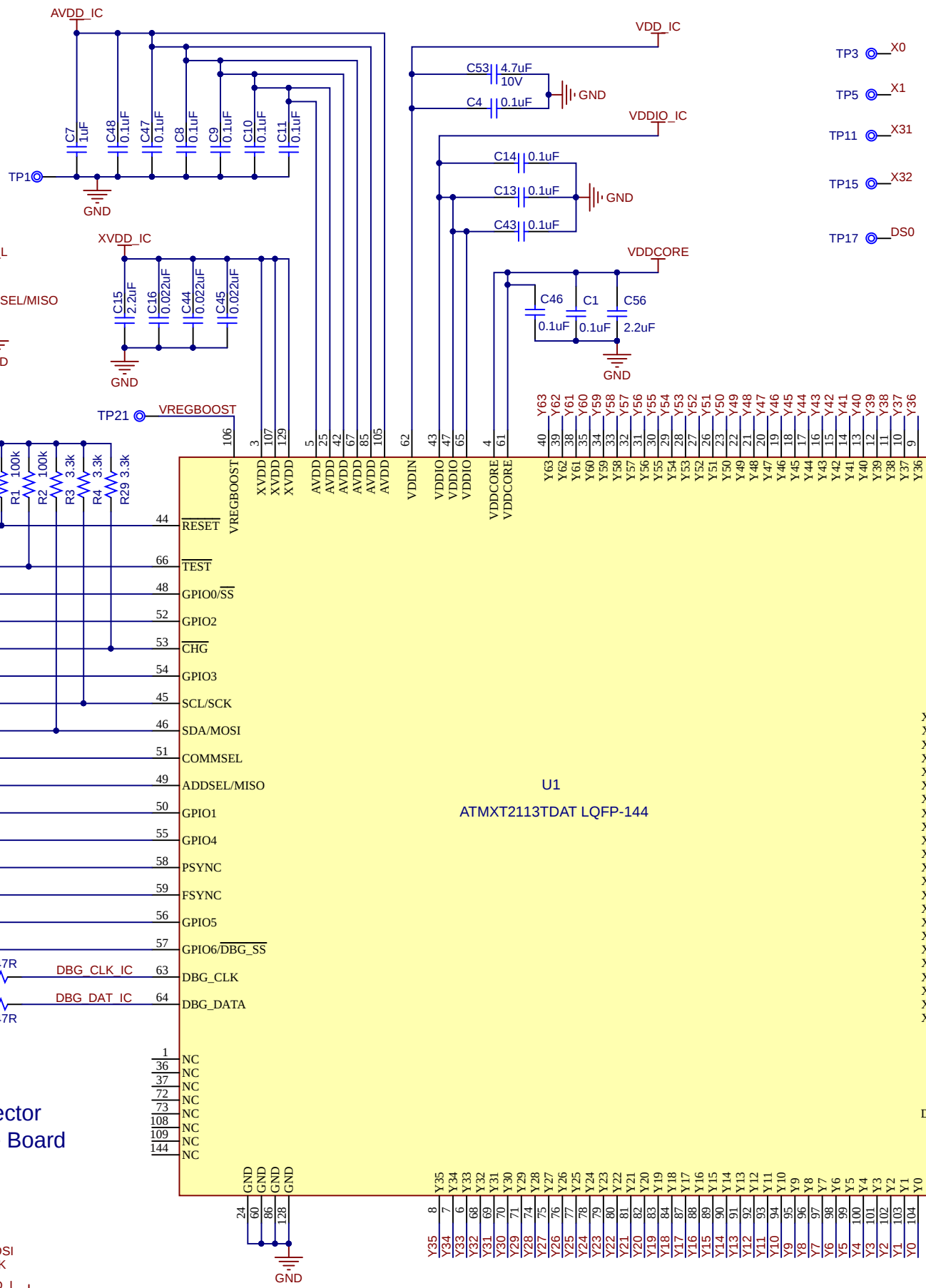
SPI



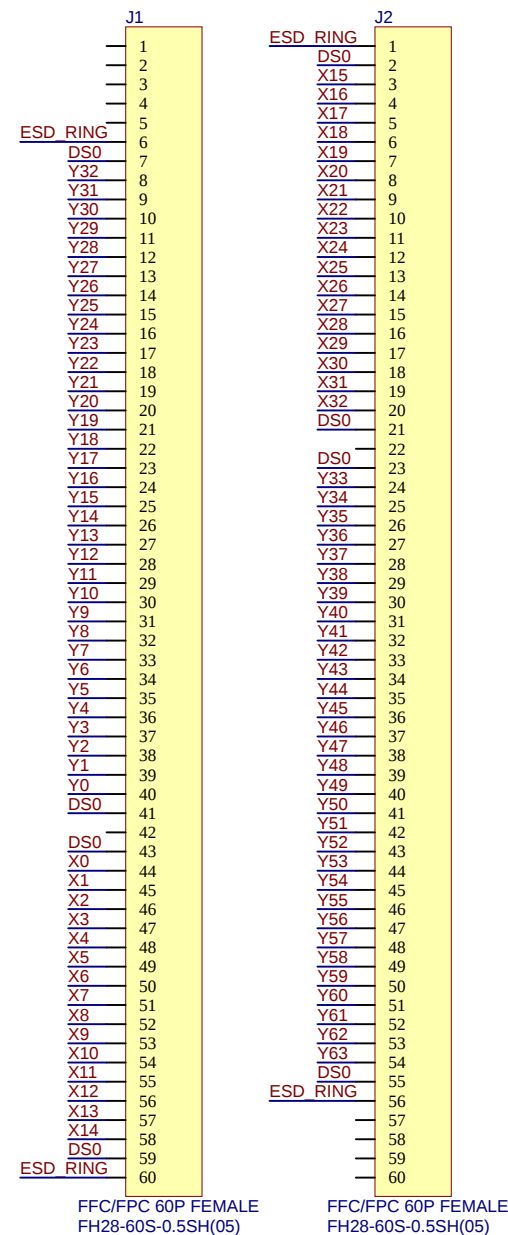
Optional Connector From USB Bridge Board



HDR-2.54 Male 1x10
DNP



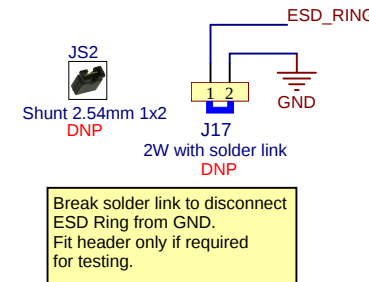
Sensor FPC Connectors



FFC/FPC 60P FEMALE
FH28-60S-0.5SH(05)

FFC/FPC 60P FEMALE
FH28-60S-0.5SH(05)

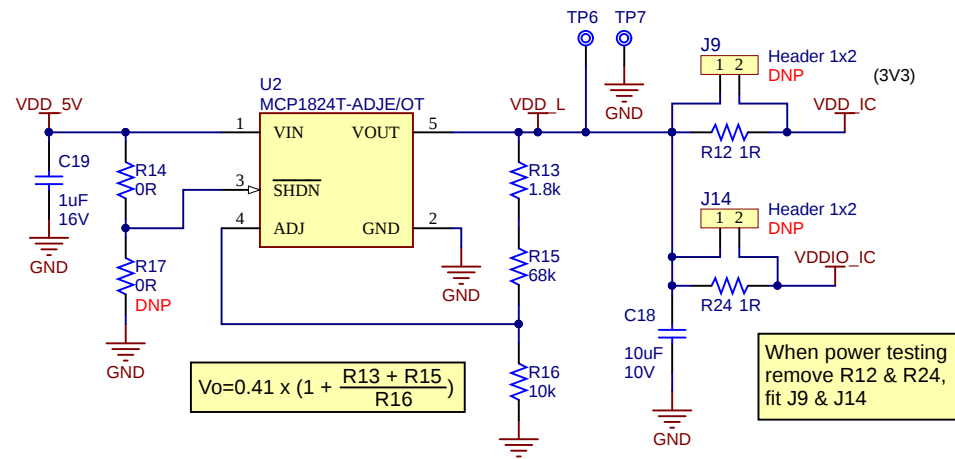
ESD Ring



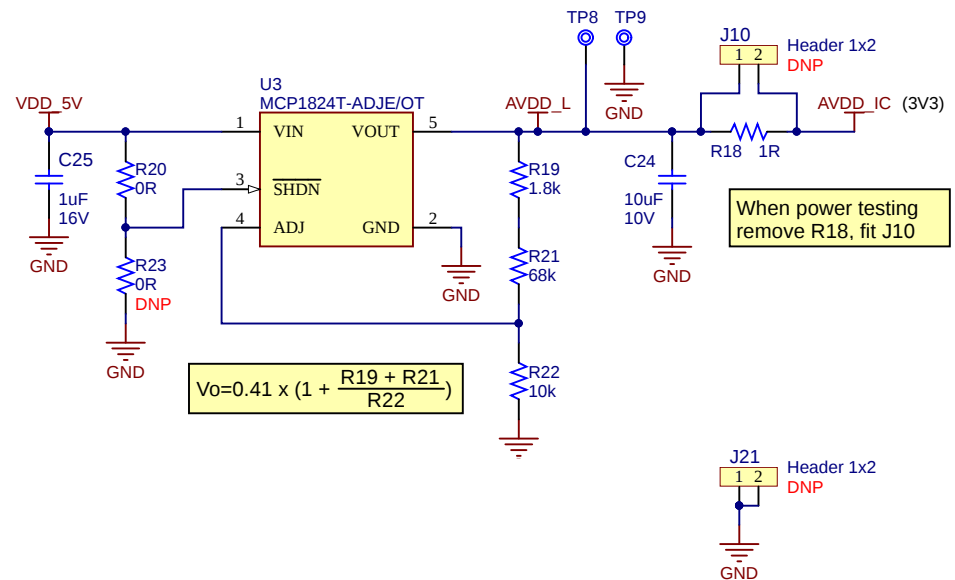
Break solder link to disconnect
ESD Ring from GND.
Fit header only if required
for testing.

Drawn By: Patrick Cassidy			
Engineer: Patrick Cassidy			
PartNumber: 10932-2	Project Title ATMX2113TD-AT EVK SPI Control Board	Variant: 10932-2	
Sheet Title ATMX2113TD-AT EVK SPI Control Board			
Size A3	SCH #: 03-10932-2	Rev:2	Date: 21/05/2020
PCB #: 04-10932		Rev:2	Sheet 1 of 2
File: 10932_01.SchDoc			
			 Altium.com

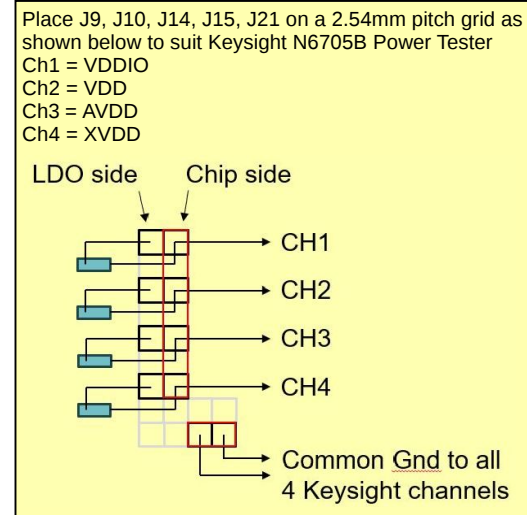
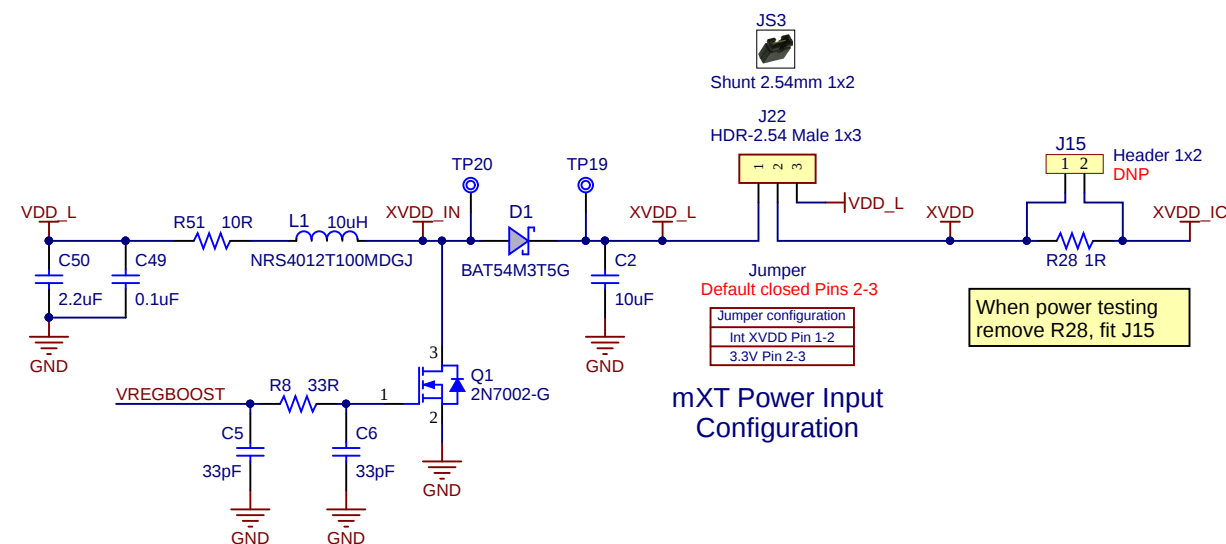
Digital Supply



Analogue Supply



High Voltage Supply



Drawn By: Patrick Cassidy		 MICROCHIP	
Engineer: Patrick Cassidy			
PartNumber: 10932-2	Project Title	Variant: 10932-2	
ATMXT2113TD-AT EVK SPI Control Board			
Sheet Title		Designed with	
ATMXT2113TD-AT EVK SPI Control Board			
Size A3	SCH #: 03-10932-2	Rev:2	Date: 21/05/2020
	PCB #: 04-10932	Rev:2	Sheet 2 of 2
File: 10932_02.SchDoc		Altium.com	