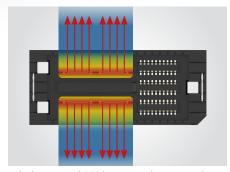
ACCELERATE®mP

HIGH-DENSITY, HIGH-SPEED POWER/SIGNAL ARRAYS



- Rotated power blades improve performance and simplify breakout region (BOR)
- Open-pin-field design for routing and grounding flexibility
- Low profile 5 mm stack height; up to 16 mm in development
- Up to 8 power and 240 signal positions; additional position counts in development
- 0.635 mm signal pitch
- Supports 64 Gbps PAM4 (32 Gbps NRZ) applications
- PCIe® 6.0/CXL® 3.1 capable
- Weld tabs included for a secure connection to the board
- · Polarized guide posts for blind mating
- Optional alignment pins



Blades rotated 90° have equal access to heat escape for uniform cooling, increased current capacity and reduced crowding

KEY SPECIFICATIONS (UDM6/UDF6)

PITCH	STACK HEIGHTS	INSULATOR MATERIAL	CONTACT MATERIAL	PLATING	OPERATING TEMP RANGE	CURRENT RATING	VOLTAGE RATING	LEAD-FREE SOLDERABLE
0.635 mm (Signal) 6.00 mm (Power)	5 mm	Black LCP	Copper Alloy	Au or Sn over 50 μ" (1.27 μm) Ni	-55 °C to +125 °C	15 A per power pin	200 VAC/ 283 VDC	Yes

PCi-SIG®, PCI Express® and the PCle® design marks are register trademarks and/or service marks of PCI-SIG.



В

(24.42)

.961

(55.47)

2.184

(29.68)

1.168

(60.73)

2.391

(0.635 mm) .025" PITCH • UDM6/UDF6 SERIES

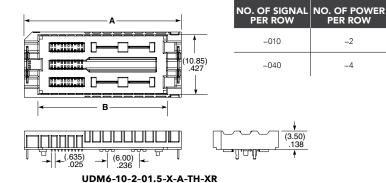


UDM6

Board Mates: UDF6

Standoffs: **GPSO**





ALSO AVAILABLE Contact Samtec

Other platings

Notes:

Some sizes, styles and options are non-standard, non-returnable

View complete specifications at: samtec.com?UDM6

(Some center features removed for clarity)



UDM6

Standoffs: **GPSO**

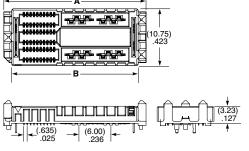


ALSO AVAILABLE

Other platings

Notes: Some sizes, styles and options are non-standard, non-returnable

NO. OF SIGNAL PER ROW	NO. OF POWER PER ROW	A	В
-010	-2	(26.88) 1.058	(23.90) .941
-040	-4	(57.93) 2.281	(54.95) 2.163



UDF6-10-2-03.5-X-A-TH-XR (Some center features removed for clarity)

View complete specifications at: samtec.com?UDF6