

Reliability

The requirements concerning quality and reliability of products are always increasing. It is not sufficient to only deliver fault-free parts. In addition, it must be ensured that the delivered goods serve their purpose safely and failure free, i.e. reliably. From the delivery of the device up to its use in a final product, there are some occasions where the device or the final product may fail despite testing and outgoing inspection.

In principle, this sequence is valid for all components of a product.

For these reasons, the negative consequences of a failure, which become more serious and expensive the later they occur, are obvious. The manufacturer is therefore interested in supplying products with the lowest possible

- AOQ (Average Outgoing Quality) value
- EFR (Early Failure Rate) value
- LFR (Long-term Failure Rate) value

Average Outgoing Quality (AOQ)

All outgoing products are sampled after 100% testing. This is known as “Average Outgoing Quality” (AOQ). The results of this inspection are recorded in ppm (parts per million) using the method defined in JEDEC 16.

Early Failure Rate (EFR)

EFR is an estimate (in ppm) of the number of early failures related to the number of devices used. Early failures are normally those which occur within the first 300 to 1000 hours. Essentially, this period of time covers the guarantee period of the finished unit. Low EFR values are therefore very important to the device user. The early life failure rate is heavily influenced by complexity. Consequently, ‘designing-in’ of better quality during the development and design phase, as well as optimized process control during manufacturing, significantly reduces the EFR value. Normally, the early failure rate should not be significantly higher than the random failure rate. EFR is given in ppm (parts per million).

Long-Term Failure Rate (LFR)

LFR shows the failure rate during the operational period of the devices. This period is of particular interest to the manufacturer of the final product. Based on the LFR value, estimations concerning long-term failure rate, reliability and a device’s or module’s usage life may be derived. The usage life time is normally the period of constant failure rate. All failures occurring during this period are random.

Within this period the failure rate is:

$$\lambda = \frac{\text{Sum of failures}}{\sum (\text{Quantity} \times \text{Time to failure})} \frac{1}{\text{hours}}$$

The measure of λ is FIT (Failures In Time = number of failures in 10^9 device hours).

Example

A sample of 500 semiconductor devices is tested in a operating life test (dynamic electric operation). The devices operate for a period of 10,000 hours.

Failures: 1 failure after 1000 h
1 failure after 2000 h

The failure rate may be calculated from this sample by

$$\lambda = \frac{2}{1 \times 1000 + 1 \times 2000 + 498 \times 10000} \frac{1}{\text{h}}$$

$$\lambda = \frac{2}{4983000} \frac{1}{\text{h}} = 4.01 \times 10^{-7} \frac{1}{\text{h}}$$

This is a λ -value of 400 FIT, or this sample has a failure rate of 0.04% / 1000 h on average.

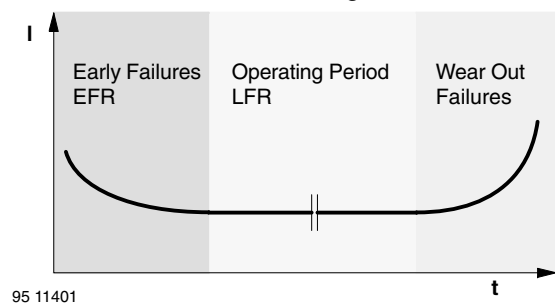


Figure 4. Bath tub curve



Confidence Level

The failure rate λ calculated from the sample is an estimate of the unknown failure rate of the lot.

The interval of the failure rate (confidence interval) may be calculated, depending on the confidence level and sample size.

The following is valid:

- The larger the sample size, the narrower the confidence interval.
- The lower the confidence level of the statement, the narrower the confidence interval.

The confidence level applicable to the failure rate of the whole lot when using the estimated value of λ is derived from the χ^2 -distribution. In practice, only the upper limit of the confidence interval (the maximum average failure rate) is used.

Therefore:

$$\lambda_{\max} = \frac{\chi^2/2 (r; P_A)}{n \times t} \text{ in } \frac{1}{h}$$

$$\text{LFR} = \frac{\chi^2/2 (r; P_A)}{n \times t} \times 1 \times 10^9 \text{ in [FIT]}$$

r: Number of failures

P_A : Confidence level

n: Sample size

t: Time in hours

$n \times t$: Device hours

The $\chi^2/2$ for λ are taken from table 15.

For the above example from table 15:

$$\chi^2/2 (r=2; P_A = 60\%) = 3.08$$

$$n \times t = 4983000 \text{ h}$$

$$\lambda_{\max} = \frac{3.08}{4983000} = 6.18 \times 10^{-7} \frac{1}{h}$$

This means that the failure rate of the lot does not exceed 0.0618% / 1000 h (618 FIT) with a probability of 60%.

If a confidence level of 90% is chosen from the table 15 :

$$\chi^2/2 (r = 2; P_A = 90\%) = 5.3$$

$$\lambda_{\max} = \frac{5.3}{4983000} = 1.06 \times 10^{-6} \frac{1}{h}$$

This means that the failure rate of the lot does not exceed 0.106% / 1000 h (1060 FIT) with a probability of 90%.

Table 15.

Number of Failures	Confidence Level			
	50%	60%	90%	95%
0	0.60	0.93	2.31	2.96
1	1.68	2.00	3.89	4.67
2	2.67	3.08	5.30	6.21
3	3.67	4.17	6.70	7.69
4	4.67	5.24	8.00	9.09
5	5.67	6.25	9.25	10.42
6	6.67	7.27	10.55	11.76
7	7.67	8.33	11.75	13.16
8	8.67	9.35	13.00	14.30
9	9.67	10.42	14.20	15.63
10	10.67	11.42	15.40	16.95

Operating Life Tests

Number of devices tested: $n = 50$

Number of failures (positive qualification): $c = 0$

Test time: $t = 2000$ hours

Confidence level: $P_A = 60\%$

$$\chi^2/2 (0; 60\%) = 0.93$$

$$\lambda_{\max} = \frac{0.93}{50 \times 2000} = 9.3 \times 10^{-6} \frac{1}{h}$$

This means, that the failure rate of the lot does not exceed 0.93% / 1000 h (9300 FIT) with a probability of 60%.

This example demonstrates that it is only possible to verify LFR values of 9300 FIT with a confidence level of 60% in a normal qualification tests (50 devices, 2000 h).

To obtain LFR values which meet today's requirements (< 50 FIT), the following conditions have to be fulfilled:

- Very long test periods
- Large quantities of devices
- Accelerated testing (e.g. higher temperature)



Mean Time to Failure (MTTF)

For systems which can not be repaired and whose devices must be changed, e.g. semiconductors, the following is valid:

$$MTTF = \frac{1}{\lambda}$$

MTTF is the average fault-free operating period per a monitored (time) unit.

Accelerating Stress Tests

Innovation cycles in the field of semiconductors are becoming shorter and shorter. This means that products must be brought to the market quicker. At the same time, expectations concerning the quality and reliability of the products have become higher.

Manufacturers of semiconductors must therefore assure long operating periods with high reliability but in a short time. Sample stress testing is the most commonly used way of assuring this.

The rule of Arrhenius describes this temperature-dependent change of the failure rate.

$$\lambda(T_2) = \lambda(T_1) \times e^{\left[\frac{E_A}{k} \times \left(\frac{1}{T_1} - \frac{1}{T_2}\right)\right]}$$

Boltzmann's constant

$$k = 8.63 \times 10^{-5} \text{ eV/K}$$

Activation energy

E_A in eV

Junction temperature real operation

T_1 in Kelvin

Junction temperature stress test

T_2 in Kelvin

Failure rate real operation

$\lambda(T_1)$

Failure rate stress test

$\lambda(T_2)$

The acceleration factor is described by the exponential function as being:

$$AF = \frac{\lambda(T_2)}{\lambda(T_1)} = e^{\left[\frac{E_A}{k} \times \left(\frac{1}{T_1} - \frac{1}{T_2}\right)\right]}$$

Example

The following conditions apply to an operating life stress test:

Environmental temperature during stress test

$$T_A = 70^\circ\text{C}$$

Power dissipation of the device

$$P_V = 100 \text{ mW}$$

Thermal resistance junction/environment

$$R_{thJA} = 300 \text{ K/W}$$

The system temperature / junction temperature results from:

$$T_J = T_A + R_{thJA} \times P_V$$

$$T_J = 70^\circ\text{C} + 300 \text{ K/W} \times 100 \text{ mW}$$

$$T_J = 100^\circ\text{C}$$

Operation in the field at an ambient temperature of 50°C and at an average power dissipation of 80 mW is utilized. This results in a junction temperature in operation of $T_J = 74^\circ\text{C}$. The activation energy used for opto components is $E_A = 0.8 \text{ eV}$.

The resulting acceleration factor is:

$$AF = \frac{\lambda(373\text{K})}{\lambda(347\text{K})} = e^{\left[\frac{E_A}{k} \times \left(\frac{1}{347\text{K}} - \frac{1}{373\text{K}}\right)\right]}$$

$$AF \approx 6.5$$

This signifies that, regarding this example, the failure rate is lower by a factor of 6.5 compared to the stress test.

Other accelerating stress tests may be:

- Humidity (except displays type TDS.)
 $T_A = 85^\circ\text{C}$
RH = 85%
- Temperature cycling
Temperature interval as specified

The tests are carried out according to the requirements of appropriate IEC-standards (see also chapter 'Qualification and Release').

Activation Energy

There are some conditions which need to be fulfilled in order to use Arrhenius' method:

- The validity of Arrhenius' rule has to be verified.
- 'Failure-specific' activation energies must be determined.

These conditions may be verified by a series of tests. Today, this procedure is generally accepted and used as a basis for estimating operating life. The values of activation energies can be determined by experiments for different failure mechanisms.

Values often used for different device groups are:

Opto components	0.8 eV
Bipolar ICs	0.7 eV
MOS ICs	0.6 eV
Transistors	0.7 eV
Diodes	0.7 eV

By using this method, it is possible to provide long-term predictions for the actual operation of semiconductors even with relatively short test periods.

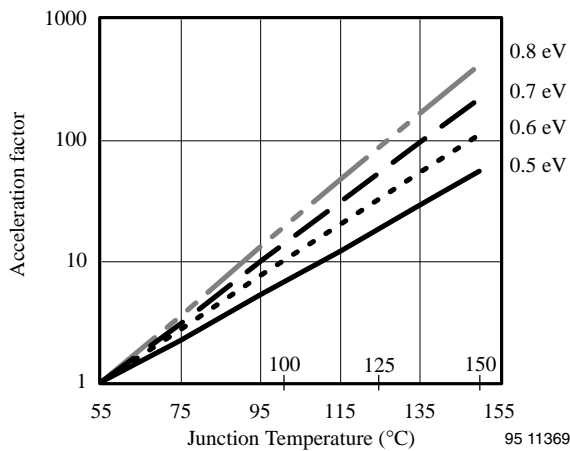


Figure 5. Acceleration factor for different activation energies normalized to $T_j = 55^\circ\text{C}$

Degradation of IREDS

The diagrams in figure 6 and figure 7 are based on electrical life tests, calculated with $E_a = 0.8 \text{ eV}$ at $T_j = 25^\circ\text{C}$.

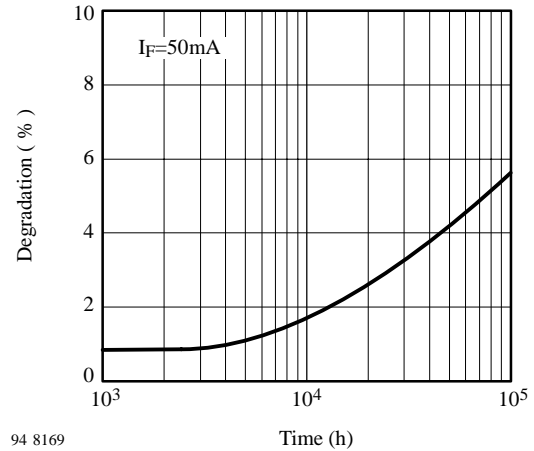


Figure 6. Average degradation of TSUS 5.

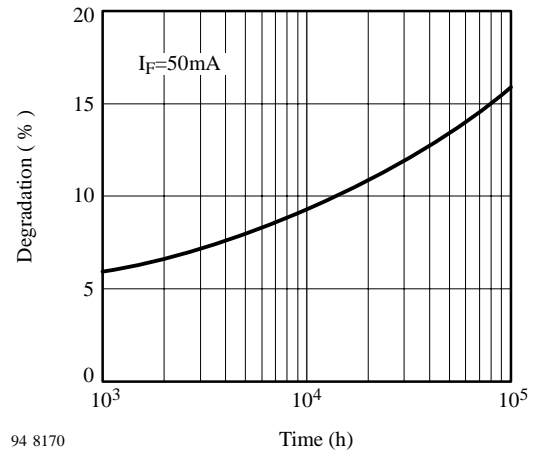


Figure 7. Average degradation of TSHA 5.