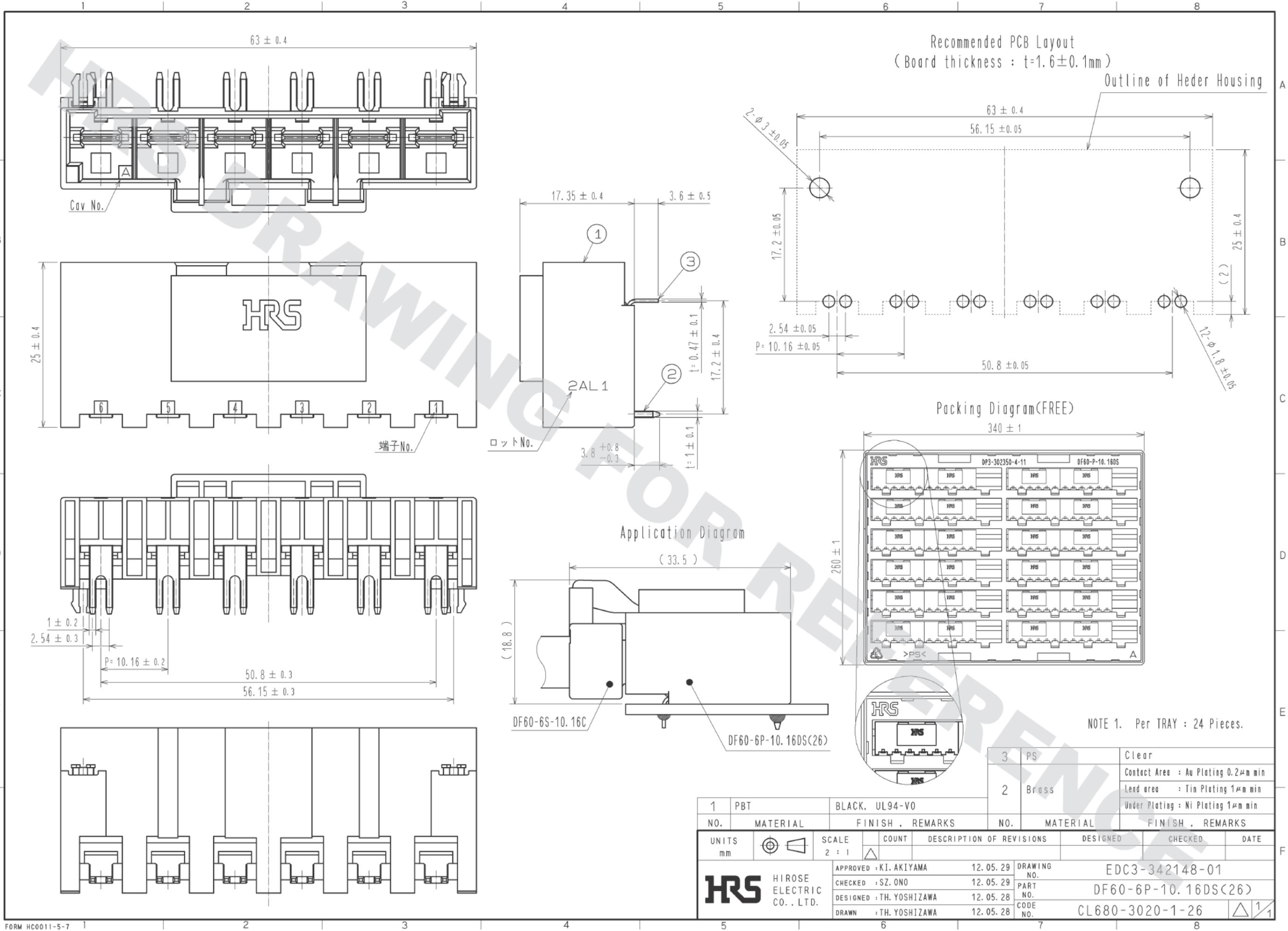


DRAWING FOR REFERENCE: This is subject to change without notice  
05/31/2013



Recommended PCB Layout  
(Board thickness :  $t=1.6 \pm 0.1$ mm)  
Outline of Heder Housing

Packing Diagram(FREE)

Application Diagram  
(33.5)

NOTE 1. Per TRAY : 24 Pieces.

3	PS	Clear
2	Brass	Contact Area : Au Plating 0.2 $\mu$ m min Lead area : Tin Plating 1 $\mu$ m min Under Plating : Ni Plating 1 $\mu$ m min

NO.	MATERIAL	FINISH . REMARKS	NO.	MATERIAL	FINISH . REMARKS
1	PBT	BLACK. UL94-V0			

UNITS	SCALE	COUNT	DESCRIPTION OF REVISIONS	DESIGNED	CHECKED	DATE
mm	2 : 1	$\Delta$				

<b>HRS</b> HIROSE ELECTRIC CO., LTD.	APPROVED : KI. AKIYAMA 12.05.29	DRAWING NO. EDC3-342148-01
	CHECKED : SZ. ONO 12.05.29	PART NO. DF60-6P-10.16DS(26)
	DESIGNED : TH. YOSHIZAWA 12.05.28	CODE NO. CL680-3020-1-26
	DRAWN : TH. YOSHIZAWA 12.05.28	