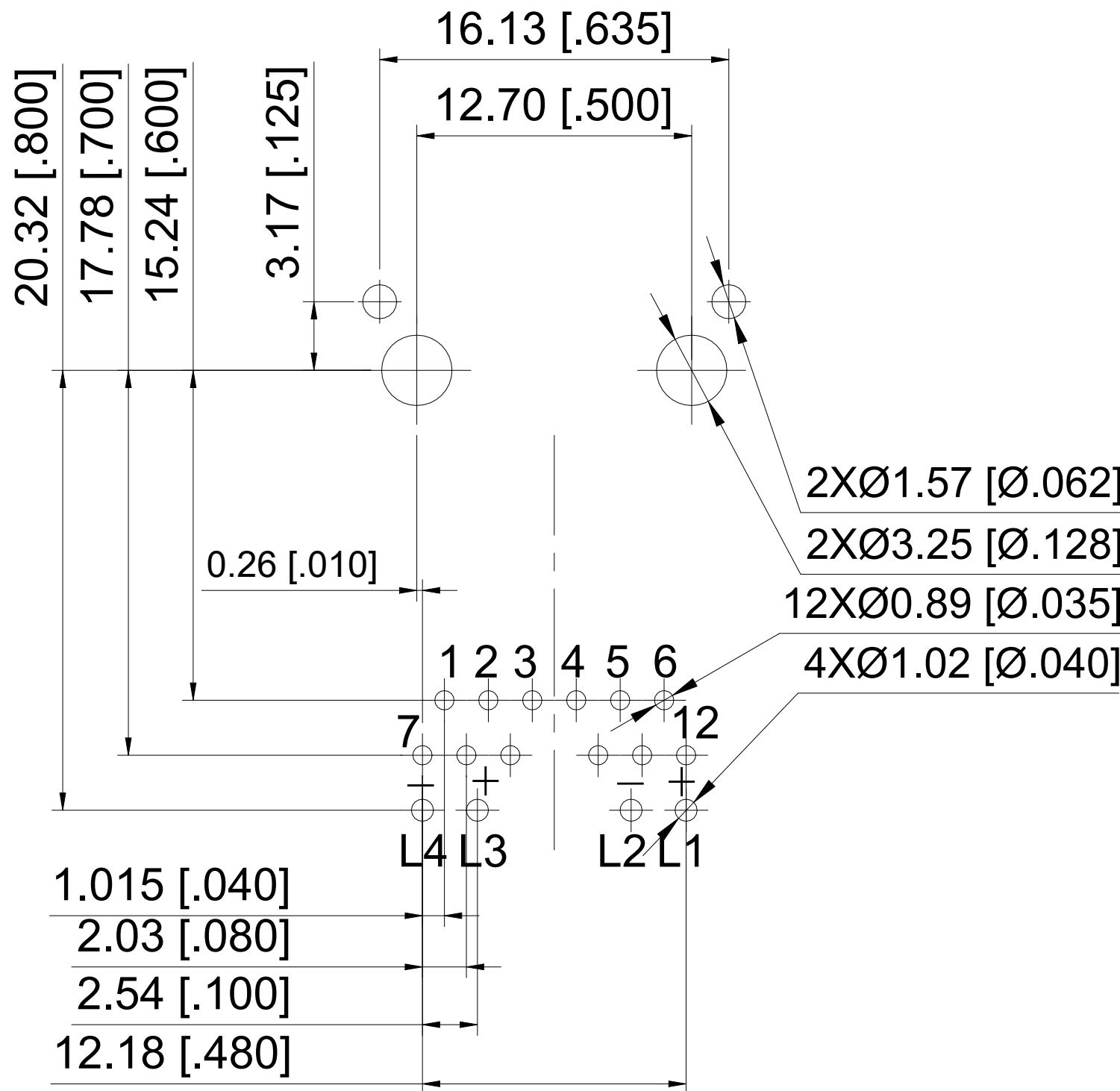
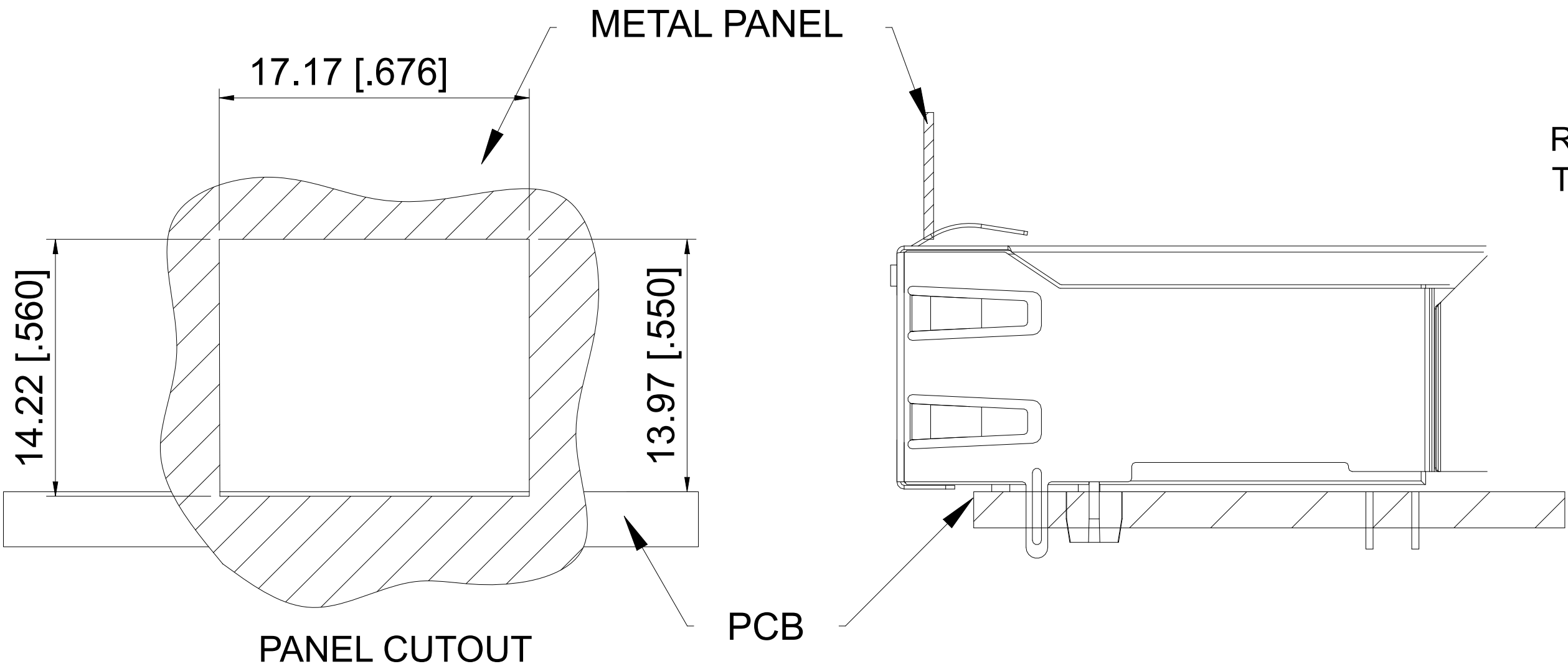
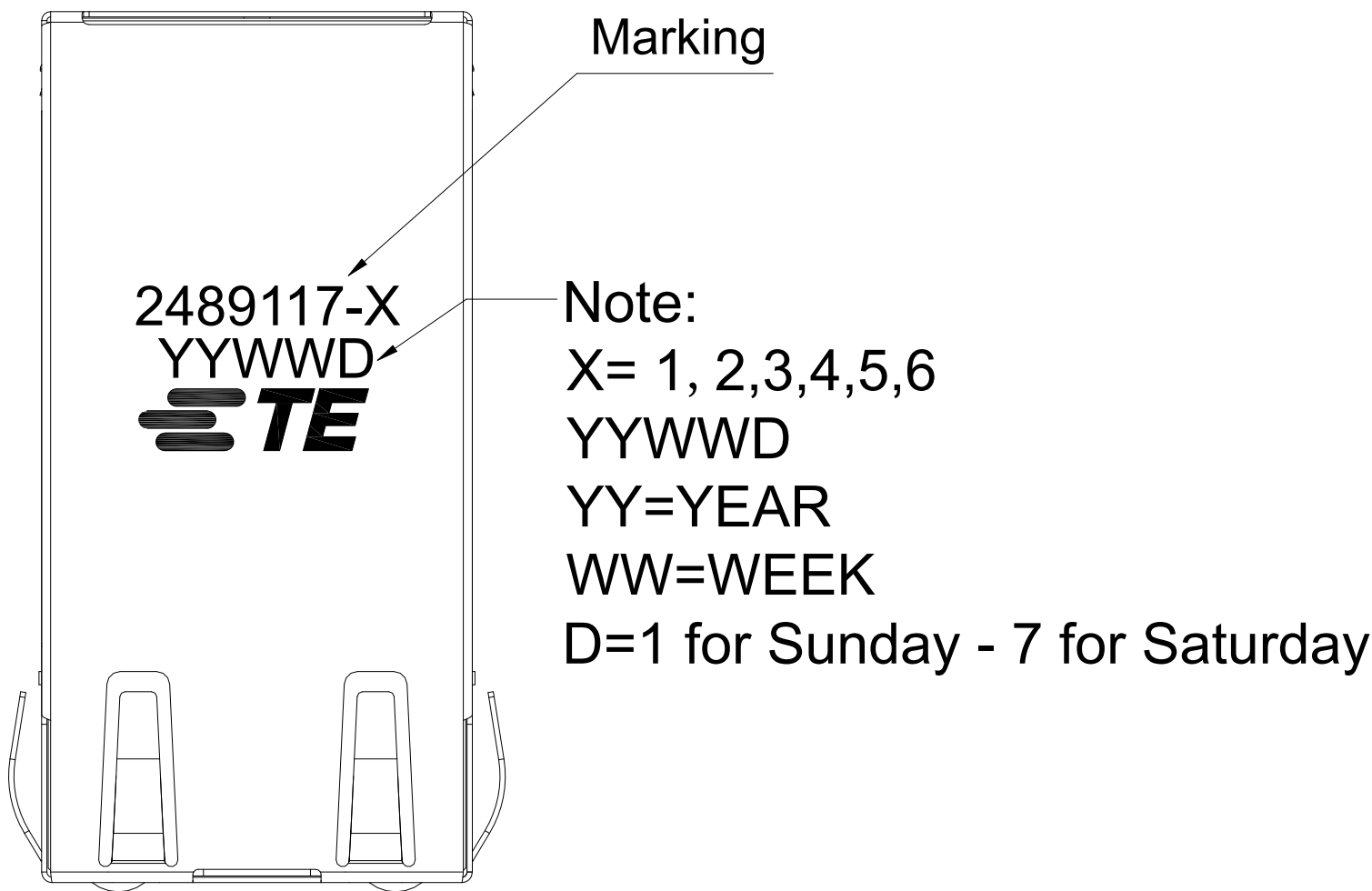
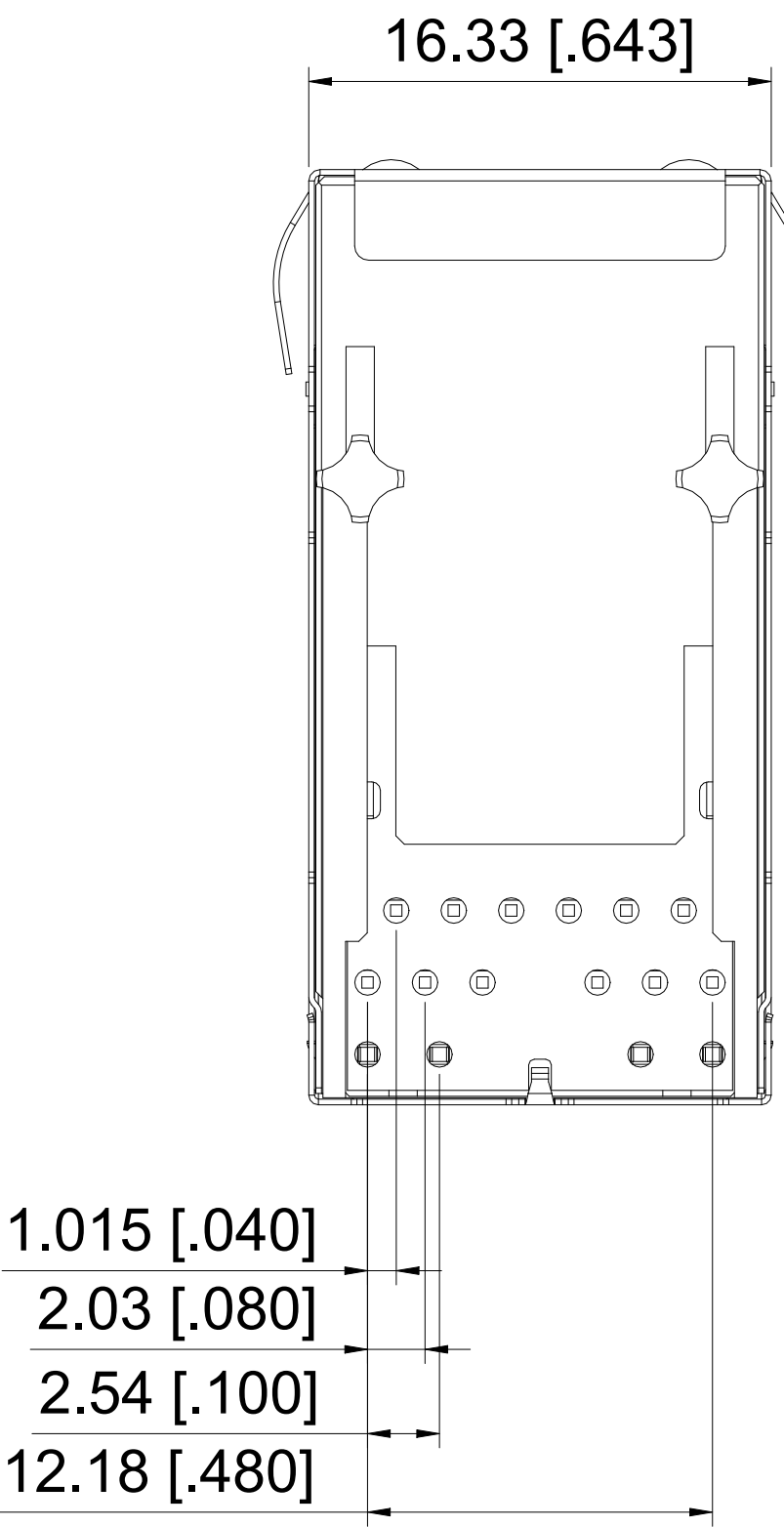
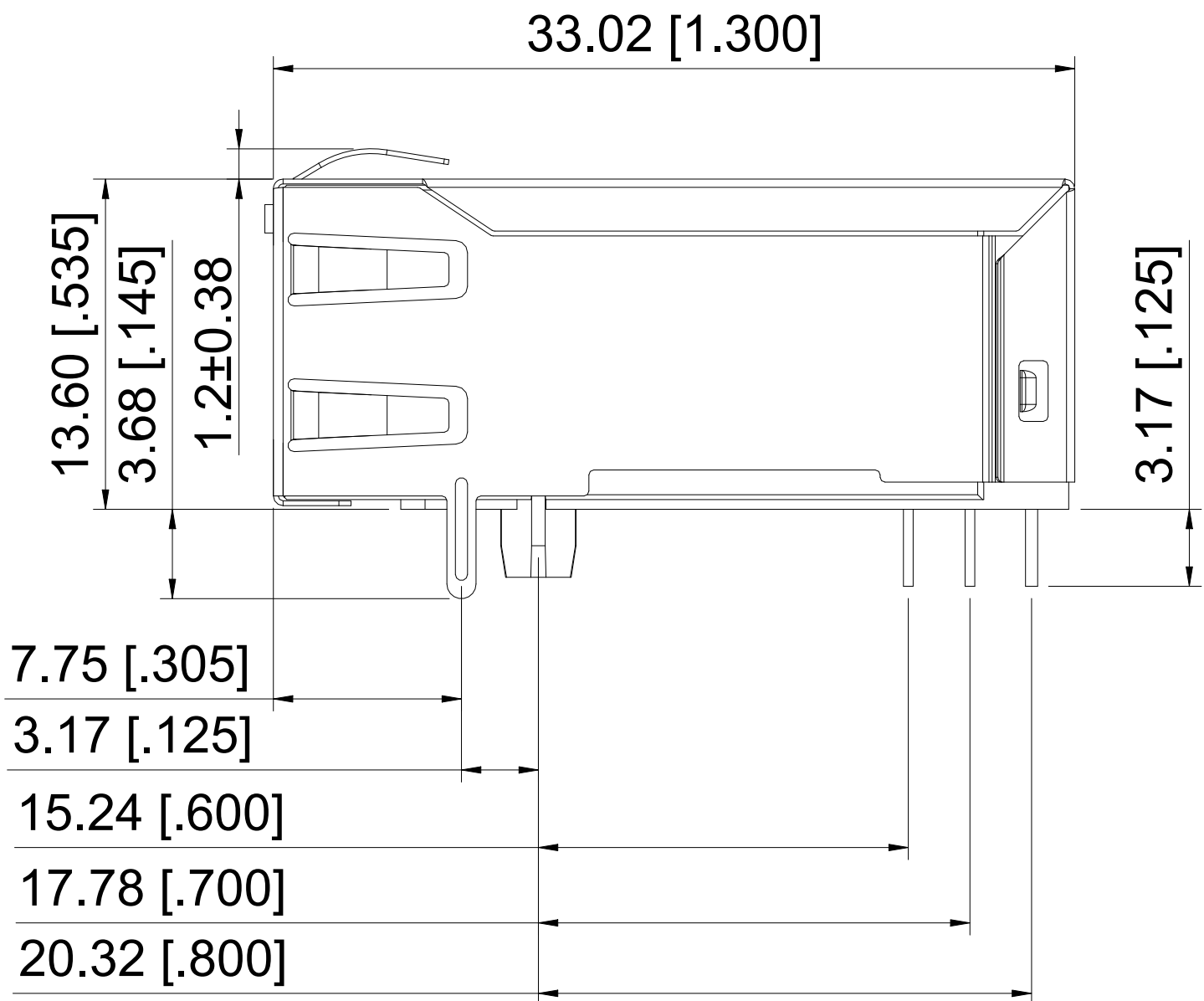
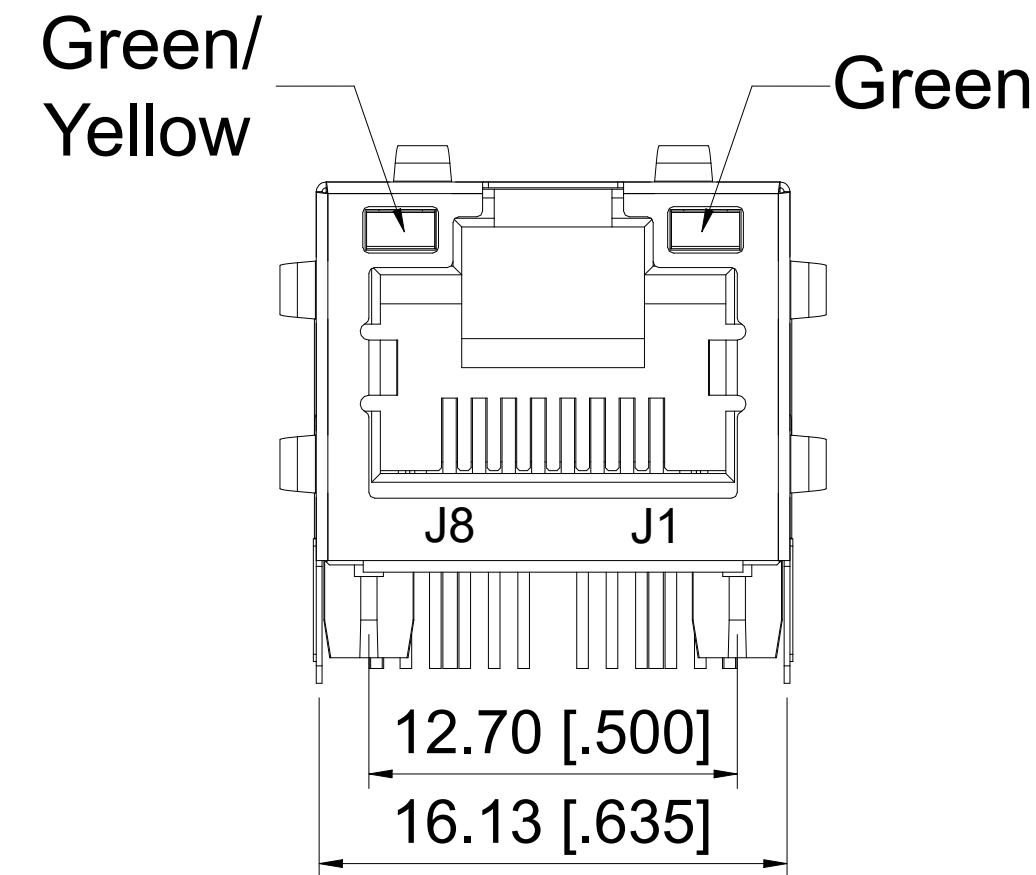


| REVISIONS |     |                           |           |     |      |
|-----------|-----|---------------------------|-----------|-----|------|
| P         | LTR | DESCRIPTION               | DATE      | DWN | APVD |
| A1        |     | ADDED TE LOGO, PN & YYWWD | 17SEP2024 | TR  | RCG  |

NOTES:

- 1.HOUSING MATERIAL:GLASS FILLED PA46(BLACK) UL94V-0(ROHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED (REFER P/N TABLE) MICROINCHES GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS.
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE:-40°C TO +85°C.  
STORAGE TEMPERATURE:-40°C TO +85°C.



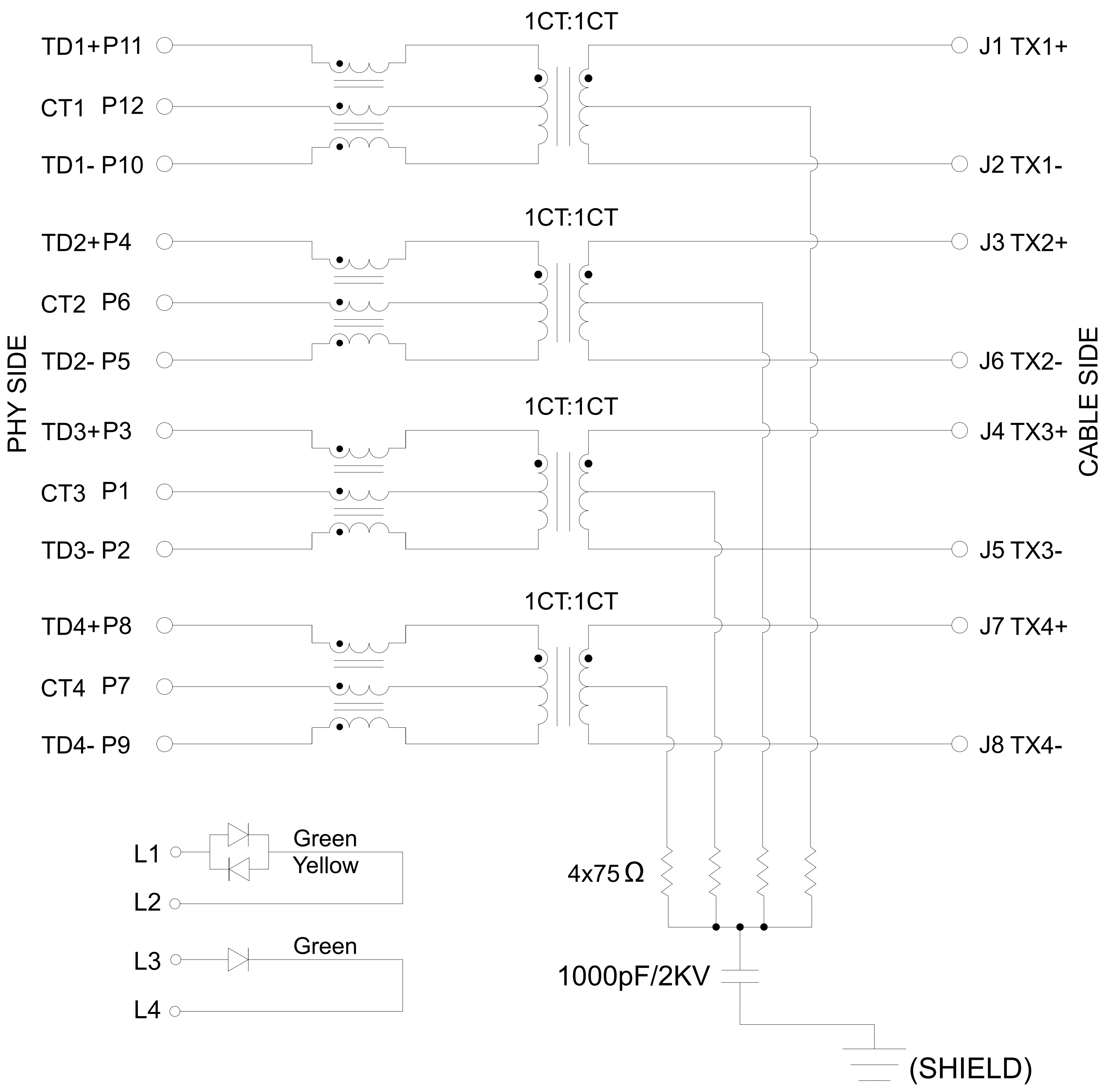
RECOMMENDED P.C.B LAYOUT  
TOP VIEW (COMPONENT SIDE)

Dimension Tolerance:  
All PCB LAYOUT  
Tolerance:±0.05[.002]

|                        |           |
|------------------------|-----------|
| 0.04572 μm [1.8μ"]     | 2489117-6 |
| 0.0762 μm [3μ"]        | 2489117-5 |
| 0.1524 μm [6μ"]        | 2489117-4 |
| 0.381 μm [15μ"]        | 2489117-3 |
| 0.762 μm [30μ"]        | 2489117-2 |
| 1.27 μm [50μ"]         | 2489117-1 |
| GOLD PLATING THICKNESS |           |
| PART NUMBER            |           |

|                                                                                                    |  |                                    |                                                         |  |
|----------------------------------------------------------------------------------------------------|--|------------------------------------|---------------------------------------------------------|--|
| THIS DRAWING IS A CONTROLLED DOCUMENT.                                                             |  | DWN<br>RAMESH KIVADER<br>13JUN2024 | <b>TE</b> Connectivity                                  |  |
| DIMENSIONS:<br>mm [INCHES]                                                                         |  | CHK<br>RAMESH KIVADER<br>13JUN2024 |                                                         |  |
| TOLERANCES UNLESS OTHERWISE SPECIFIED:                                                             |  | APVD<br>P.S. SATHISH<br>13JUN2024  | NAME                                                    |  |
| 0. PLC ± 0.10<br>1. PLC ± 0.25<br>2. PLC ± 0.25<br>3. PLC ± 0.25<br>4. PLC ± 0.25<br>ANGLES ± 0.25 |  | PRODUCT SPEC<br>108-161443         | RJ45 1X1 Tab_Up/Through Hole<br>10/100/1000Base-T W/LED |  |
| MATERIAL                                                                                           |  | APPLICATION SPEC<br>114-161076     | SIZE                                                    |  |
| FINISH                                                                                             |  | WEIGHT<br>0                        | CAGE CODE                                               |  |
|                                                                                                    |  | CUSTOMER DRAWING                   | DRAWING NO                                              |  |
|                                                                                                    |  | SCALE 1:1                          | RESTRICTED TO                                           |  |
|                                                                                                    |  | SHEET 1 OF 2                       | REV A1                                                  |  |

# Schematic



| PARAMETER                        | SPECIFICATIONS                                                                                 |
|----------------------------------|------------------------------------------------------------------------------------------------|
| OPERATING TEMPERATURE            | -40°C To +85°C                                                                                 |
| URNS RATIO                       | 1:1±2%                                                                                         |
| INDUCTANCE(OCL)                  | 350uH MIN@100KHz/100mV With 8mA DC Bias                                                        |
| INSERTION LOSS                   | -1.0dB MAX@0.3MHz-100MHz;                                                                      |
| RETURN LOSS<br>(Z out=100 OHM)   | -18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz;<br>-12dB MIN@60MHz-80MHz; -10dB MIN@80MHz-100MHz; |
| CROSSTALK<br>(ADJACENT CHANNELS) | -30dB MIN@1MHz-100MHz;                                                                         |
| COMMON MODE REJECTION<br>RATIO   | -30dB MIN@1MHz-100MHz;                                                                         |
| HI-POT                           | 2250 VDC@60 SECONDS                                                                            |

| LED SPECIFICATION |            |                |      |
|-------------------|------------|----------------|------|
| STANDARD LED      | WAVELENGTH | Farward V(max) | TYP  |
| GREEN             | 570nm      | 2.4V           | 2.2V |
| YELLOW            | 590nm      | 2.5V           | 2.1V |

\*WITH A FORWARD CURRENT OF 20mA